

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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2				*****
3				*
4				*Testcase IEEE DIVIDE TO INTEGER
5				* Test case capability includes IEEE exceptions trappable and
6				* otherwise. Test results, FPCR flags, and any DXC are saved for all
7				* tests.
8				*
9				*
10				* *****
11				** IMPORTANT! **
12				* *****
13				*
14				* This test uses the Hercules Diagnose X'008' interface
15				* to display messages and thus your .tst runtest script
16				* MUST contain a "DIAG8CMD ENABLE" statement within it!
17				*
18				*
19				*****
21				*****
22				*
23				* bfp-001-divtoint.asm
24				*
25				* This assembly-language source file is part of the
26				* Hercules Binary Floating Point Validation Package
27				* by Stephen R. Orso
28				*
29				* Copyright 2016 by Stephen R Orso.
30				* Runtest *Compare dependency removed by Fish on 2022-03-08
31				* PADCSECT macro/usage removed by Fish on 2022-03-08
32				*
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				57 * OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
				58 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE
				59 * OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
				60 *
				61 *****

				63 *
				64 *Outstanding Issues:
				65 * - 'A' versions of instructions are not tested. Space for these added
				66 * results has not been allowed for in the results areas. Eight
				67 * additional results are needed per input pair.
				68 * - Initial execution on real hardware shows no inexact / truncated on
				69 * underflow; not sure this case can be created on Add. Finite
				70 * rounding mode test cases must be reviewed.
				71 * - The quantum exception is not tested. This is only available in the
				72 * 'A' mode instructions, and only the finite tests will detect a
				73 * quantum trap. This has implications for the test case selection
				74 * and the selection of the instruction used for the test. Note: the
				75 * M4 rounding mode used with the 'A' instructions must be in the
				76 * range 8-15.
				77 * - Note that the test case values selected for the rounding mode tests
				78 * will never trigger the quantum flag.
				79 *
				80 * - If Quantum exceptions can be created, they will be tested in the
				81 * Finite tests.
				82 * - A fourth test run will perform pathlength validation on the M4
				83 * Rounding Mode tests, rather than run 16 additional tests on each of
				84 * 8 (at present) rounding mode test pairs. A pair of tests is
				85 * sufficient: a positive RNTE odd and a negative RNTE even. (Or the
				86 * other way around.)
				87 *
				88 *
				89 *
				90 *****

				92 *****
				93 *
				94 * Tests the following three conversion instructions
				95 * DIVIDE TO INTEGER (short BFP, RRE)
				96 * DIVIDE TO INTEGER (long BFP, RRE)
				97 *
				98 * Test data is compiled into this program. The test script that runs
				99 * this program can provide alternative test data through Hercules R
				100 * commands.
				101 *
				102 * Test Case Order
				103 * 1) Short BFP basic tests, including traps and NaN propagation
				104 * 2) Short BFP finite number tests, incl. partial and final results
				105 * 3) Short BFP rounding, tests of quotient and remainder rounding
				106 * 4) Long BFP basic tests, including traps and NaN propagation
				107 * 5) Long BFP finite number tests, incl. partial and final results
				108 * 6) Short BFP rounding, tests of quotient and remainder rounding
				109 *
				110 * Three input test sets are provided each for short and long

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				111 *	BFP inputs. Test values are conceptually the same for each
				112 *	precision, but the values differ between precisions. Overflow,
				113 *	for example, is triggered by different values in short and long,
				114 *	but each test set includes overflow tests.
				115 *	
				116 *	Also tests the following floating point support instructions
				117 *	LOAD (Short)
				118 *	LOAD (Long)
				119 *	LFPC (Load Floating Point Control Register)
				120 *	SRNMB (Set BFP Rounding Mode 2-bit)
				121 *	SRNMB (Set BFP Rounding Mode 3-bit)
				122 *	STORE (Short)
				123 *	STORE (Long)
				124 *	STFPC (Store Floating Point Control Register)
				125 *	
				126 *	*****
				127 *	
				128 *	Note: for compatibility with the z/CMS test rig, do not change
				129 *	or use R11, R14, or R15. Everything else is fair game.
				130 *	
	00000000	00024FCB		131 BFPDV2NT	START 0
	00000000	00000001		132 STRTLABL	EQU *
	00000000	00000001		133 R0	EQU 0
					Work register for cc extraction
	00000001	00000001		134 R1	EQU 1
	00000002	00000001		135 R2	EQU 2
	00000003	00000001		136 R3	EQU 3
	00000004	00000001		137 R4	EQU 4
	00000005	00000001		138 R5	EQU 5
	00000006	00000001		139 R6	EQU 6
	00000007	00000001		140 R7	EQU 7
	00000008	00000001		141 R8	EQU 8
	00000009	00000001		142 R9	EQU 9
	0000000A	00000001		143 R10	EQU 10
	0000000B	00000001		144 R11	EQU 11
	0000000C	00000001		145 R12	EQU 12
	0000000D	00000001		146 R13	EQU 13
	0000000E	00000001		147 R14	EQU 14
	0000000F	00000001		148 R15	EQU 15
				149 *	
				150 *	Floating Point Register equates to keep the cross reference clean
				151 *	
	00000000	00000001		152 FPR0	EQU 0
	00000001	00000001		153 FPR1	EQU 1
	00000002	00000001		154 FPR2	EQU 2
	00000003	00000001		155 FPR3	EQU 3
	00000004	00000001		156 FPR4	EQU 4
	00000005	00000001		157 FPR5	EQU 5
	00000006	00000001		158 FPR6	EQU 6
	00000007	00000001		159 FPR7	EQU 7
	00000008	00000001		160 FPR8	EQU 8
	00000009	00000001		161 FPR9	EQU 9
	0000000A	00000001		162 FPR10	EQU 10
	0000000B	00000001		163 FPR11	EQU 11
	0000000C	00000001		164 FPR12	EQU 12
	0000000D	00000001		165 FPR13	EQU 13
	0000000E	00000001		166 FPR14	EQU 14

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
		0000000F	00000001	167	FPR15	EQU	15	
				168	*			
00000000		00000000		169		USING	*,R15	
00000000		00024C00		170		USING	HELPERS,R12	
				171	*			
				172	*	Above works on real iron (R15=0 after sysclear)		
				173	*	and in z/CMS (R15 points to start of load module)		
				174	*			
				176	*****			
				177	*			
				178	*	Low core definitions, Restart PSW, and Program Check Routine.		
				179	*			
				180	*****			
00000000		00000000	0000008E	182		ORG	STRTLABL+X'8E'	Program check interruption code
0000008E	0000			183	PCINTCD	DS	H	
				184	*			
		00000150	00000001	185	PCOLDPSW	EQU	STRTLABL+X'150'	z/Arch Program check old PSW
				186	*			
00000090		00000090	000001A0	187		ORG	STRTLABL+X'1A0'	z/Arch Restart PSW
000001A0	00000001 80000000			188		DC	X'0000000180000000',AD(START)	
				189	*			
000001B0		000001B0	000001D0	190		ORG	STRTLABL+X'1D0'	z/Arch Program check NEW PSW
000001D0	00000000 00000000			191		DC	X'0000000000000000',AD(PROGCHK)	
				192	*			
				193	*	Program check routine. If Data Exception, continue execution at		
				194	*	the instruction following the program check. Otherwise, hard wait.		
				195	*	No need to collect data. All interesting DXC stuff is captured		
				196	*	in the FPCR.		
				197	*			
000001E0		000001E0	00000200	198		ORG	STRTLABL+X'200'	
00000200				199	PROGCHK	DS	0H	Program check occurred...
00000200	9507 F08F		0000008F	200		CLI	PCINTCD+1,X'07'	Data Exception?
00000204	A774 0004		0000020C	201		JNE	PCNOTDTA	..no, hardwait
00000208	B2B2 F150		00000150	202		LPSWE	PCOLDPSW	..yes, resume program execution
0000020C	900F F23C		0000023C	204	PCNOTDTA	STM	R0,R15,SAVEREGS	Save registers
00000210	58C0 F27C		0000027C	205		L	R12,AHELPERS	Get address of helper subroutines
00000214	4DD0 C000		00024C00	206		BAS	R13,PGMCK	Report this unexpected program check
00000218	980F F23C		0000023C	207		LM	R0,R15,SAVEREGS	Restore registers
0000021C	12EE			209		LTR	R14,R14	Return address provided?
0000021E	077E			210		BNZR	R14	Yes, return to z/CMS test rig.
00000220	B2B2 F228		00000228	211		LPSWE	PROGPSW	Not data exception, enter disabled wait
00000228	00020000 00000000			212	PROGPSW	DC	0D'0',X'0002000000000000',XL6'00',X'DEAD'	Abnormal end
00000238	B2B2 F2E0		000002E0	213	FAIL	LPSWE	FAILPSW	Not data exception, enter disabled wait
0000023C	00000000 00000000			214	SAVEREGS	DC	16F'0'	Registers save area
0000027C	00024C00			215	AHELPERS	DC	A(HELPERS)	Address of helper subroutines

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				217 *****
				218 *
				219 * Main program. Enable Advanced Floating Point, process test cases.
				220 *
				221 *****
00000280				223 START DS 0H
00000280	B600 F2F0		000002F0	224 STCTL R0,R0,CTLR0 Store CR0 to enable AFP
00000284	9604 F2F1		000002F1	225 OI CTLR0+1,X'04' Turn on AFP bit
00000288	B700 F2F0		000002F0	226 LCTL R0,R0,CTLR0 Reload updated CR0
				227 *
0000028C	41A0 F2FC		000002FC	228 LA R10,SHORTNF Point to short BFP non-finite inputs
00000290	4DD0 F35C		0000035C	229 BAS R13,DIEBRNF Divide to Integer short BFP non-finite
00000294	41A0 F30C		0000030C	230 LA R10,SHORTF Point to short BFP finite inputs
00000298	4DD0 F3D2		000003D2	231 BAS R13,DIEBRF Divide to Integer short BFP finites
0000029C	41A0 F31C		0000031C	232 LA R10,RMSHORTS Point to short BFP rounding mode tests
000002A0	4DD0 F480		00000480	233 BAS R13,DIEBRM Convert using all rounding mode options
				234 *
000002A4	41A0 F32C		0000032C	235 LA R10,LONGNF Point to long BFP non-finite inputs
000002A8	4DD0 F524		00000524	236 BAS R13,DIDBRNF Divide to Integer long BFP basic
000002AC	41A0 F33C		0000033C	237 LA R10,LONGF Point to long BFP finite inputs
000002B0	4DD0 F59A		0000059A	238 BAS R13,DIDBRF Divide to Integer long BFP basic
000002B4	41A0 F34C		0000034C	239 LA R10,RMLONGS Point to long BFP rounding mode tests
000002B8	4DD0 F648		00000648	240 BAS R13,DIDBRM Convert using all rounding mode options
				241 *
				242 *****
				243 * Verify test results...
				244 *****
				245 *
000002BC	58C0 F27C		0000027C	246 L R12,AHELPERS Get address of helper subroutines
000002C0	4DD0 C0A0		00024CA0	247 BAS R13,VERISUB Go verify results
000002C4	12EE			248 LTR R14,R14 Was return address provided?
000002C6	077E			249 BNZR R14 Yes, return to z/CMS test rig.
000002C8	B2B2 F2D0		000002D0	250 LPSWE GOODPSW Load SUCCESS PSW

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000002D0				252	DS	0D	Ensure correct alignment for PSW
000002D0	00020000	00000000		253	GOODPSW	DC	X'0002000000000000',AD(0) Normal end - disabled wait
000002E0	00020000	00000000		254	FAILPSW	DC	X'0002000000000000',XL6'00',X'0BAD' Abnormal end
				255	*		
000002F0	00000000			256	CTLR0	DS	F
000002F4	00000000			257	FPCREGNT	DC	X'00000000' FPCR, trap all IEEE exceptions, zero flags
000002F8	F8000000			258	FPCREGTR	DC	X'F8000000' FPCR, trap no IEEE exceptions, zero flags
				259	*		
				260	* Input values parameter list, four fullwords for each test data set		
				261	* 1) Count,		
				262	* 2) Address of inputs,		
				263	* 3) Address to place results, and		
				264	* 4) Address to place DXC/Flags/cc values.		
				265	*		
000002FC				266	SHORTNF	DS	0F Input pairs for short BFP non-finite tests
000002FC	00000020			267		DC	A(SBFPNFCT)
00000300	00000738			268		DC	A(SBFPNFIN)
00000304	00001000			269		DC	A(SBFPNFOT)
00000308	00001200			270		DC	A(SBFPNFFL)
				271	*		
0000030C				272	SHORTF	DS	0F Input pairs for short BFP finite tests
0000030C	0000002B			273		DC	A(SBFPCT)
00000310	00000838			274		DC	A(SBFPIN)
00000314	0000A000			275		DC	A(SBFPOUT)
00000318	0000A800			276		DC	A(SBFPFLGS)
				277	*		
0000031C				278	RMSHORTS	DS	0F Input pairs for short BFP rounding testing
0000031C	0000000C			279		DC	A(SBFRMCT)
00000320	00000990			280		DC	A(SBFPINRM)
00000324	00002000			281		DC	A(SBFRMO)
00000328	00004000			282		DC	A(SBFRMOF)
				283	*		
0000032C				284	LONGNF	DS	0F Input pairs for long BFP non-finite testing
0000032C	00000020			285		DC	A(LBFPNFCT)
00000330	000009F0			286		DC	A(LBFPNFIN)
00000334	00001300			287		DC	A(LBFPNFOT)
00000338	00001700			288		DC	A(LBFPNFFL)
				289	*		
0000033C				290	LONGF	DS	0F Input pairs for long BFP finite testing
0000033C	00000026			291		DC	A(LBFPCT)
00000340	00000BF0			292		DC	A(LBFPIN)
00000344	0000B000			293		DC	A(LBFPOUT)
00000348	0000AC00			294		DC	A(LBFPFLGS)
				295	*		
0000034C				296	RMLONGS	DS	0F Input pairs for long BFP rounding testing
0000034C	0000000C			297		DC	A(LBFRMCT)
00000350	00000E50			298		DC	A(LBFPINRM)
00000354	00005000			299		DC	A(LBFRMO)
00000358	00009000			300		DC	A(LBFRMOF)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				302 *****
				303 *
				304 * Perform Divide to Integer using provided short BFP input pairs. This
				305 * set of tests checks NaN propagation and operations on values that are
				306 * not finite numbers.
				307 *
				308 * A pair of results is generated for each input: one with all
				309 * exceptions non-trappable, and the second with all exceptions
				310 * trappable. The FPCR and condition code is stored for each result.
				311 *
				312 *****
0000035C				314 DIEBRNF DS 0H BFP Short non-finite values tests
0000035C	9823 A000		00000000	315 LM R2,R3,0(R10) Get count and address of test input values
00000360	9878 A008		00000008	316 LM R7,R8,8(R10) Get address of result area and flag area.
00000364	1222			317 LTR R2,R2 Any test cases?
00000366	078D			318 BZR R13 ..No, return to caller
00000368	0DC0			319 BASR R12,0 Set top of loop
				320 *
0000036A	B29D F2F4		000002F4	321 LFPC FPCREGNT Set exceptions non-trappable
0000036E	7800 3000		00000000	322 LE FPR0,0(,R3) Get short BFP dividend
00000372	7810 3004		00000004	323 LE FPR1,4(,R3) Get short BFP divisor
00000376	B374 0020			324 LZER FPR2 Zero remainder register
0000037A	B353 2001			325 DIEBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				326 * Quotient in FPR2, remainder in FPR0
0000037E	7000 7000		00000000	327 STE FPR0,0(,R7) Store short BFP remainder
00000382	7020 7004		00000004	328 STE FPR2,4(,R7) Store short BFP quotient
00000386	B29C 8000		00000000	329 STFPC 0(R8) Store resulting FPCR flags and DXC
0000038A	B222 0000			330 IPM R0 Retrieve condition code
0000038E	8800 001C		0000001C	331 SRL R0,28 Move CC to low-order r0, dump prog mask
00000392	4200 8003		00000003	332 STC R0,3(0,R8) Store in last byte of FPCR
				333 *
00000396	B29D F2F8		000002F8	334 LFPC FPCREGTR Set exceptions trappable
0000039A	7800 3000		00000000	335 LE FPR0,0(,R3) Get short BFP dividend
0000039E	7810 3004		00000004	336 LE FPR1,4(,R3) Get short BFP divisor
000003A2	B374 0020			337 LZER FPR2 Zero remainder register
000003A6	B353 2001			338 DIEBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				339 * Quotient in FPR2, remainder in FPR0
000003AA	7000 7008		00000008	340 STE FPR0,8(,R7) Store short BFP remainder
000003AE	7020 700C		0000000C	341 STE FPR2,12(,R7) Store short BFP quotient
000003B2	B29C 8004		00000004	342 STFPC 4(R8) Store resulting FPCR flags and DXC
000003B6	B222 0000			343 IPM R0 Retrieve condition code
000003BA	8800 001C		0000001C	344 SRL R0,28 Move CC to low-order r0, dump prog mask
000003BE	4200 8007		00000007	345 STC R0,7(,R8) Store in last byte of FPCR
				346 *
000003C2	4130 3008		00000008	347 LA R3,8(,R3) Point to next input value pair
000003C6	4170 7010		00000010	348 LA R7,16(,R7) Point to next quo&rem result value pair
000003CA	4180 8008		00000008	349 LA R8,8(,R8) Point to next FPCR result area
000003CE	062C			350 BCTR R2,R12 Convert next input value.
000003D0	07FD			351 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				353 *****
				354 *
				355 * Perform Divide to Integer using provided short BFP input pairs. This
				356 * set of tests performs basic checks of Divide To Integer emulation
				357 * where both inputs are finite non-zero numbers.
				358 *
				359 * Four results (six values) are generated for each input:
				360 * 1) Divide to integer with all exceptions non-trappable (two values)
				361 * 2) Multiply integer quotient by divisor, add remainder (one value)
				362 * 3) Divide to integer with all exceptions trappable (two values)
				363 * 4) Multiply integer quotient by divisor, add remainder (one value)
				364 *
				365 * The FPCR and condition code is stored for each result. Note: the
				366 * Multiply and Add instruction does not set the condition code.
				367 *
				368 * Results two and four (multiply and add) validate the calculation
				369 * of the integer quotient and remainder.
				370 *
				371 *****
000003D2	9823 A000		00000000	373 DIEBRF LM R2,R3,0(R10) Get count and address of test input values
000003D6	9878 A008		00000008	374 LM R7,R8,8(R10) Get address of result area and flag area.
000003DA	1222			375 LTR R2,R2 Any test cases?
000003DC	078D			376 BZR R13 ..No, return to caller
000003DE	0DC0			377 BASR R12,0 Set top of loop
				378 *
000003E0	B29D F2F4		000002F4	379 LFPC FPCREGNT Set exceptions non-trappable
000003E4	7800 3000		00000000	380 LE FPR0,0(,R3) Get short BFP dividend
000003E8	7810 3004		00000004	381 LE FPR1,4(,R3) Get short BFP divisor
000003EC	B374 0020			382 LZER FPR2 Zero remainder register
000003F0	B353 2001			383 DIEBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				384 * Quotient in FPR2, remainder in FPR0
000003F4	7000 7000		00000000	385 STE FPR0,0(,R7) Store short BFP remainder
000003F8	7020 7004		00000004	386 STE FPR2,4(,R7) Store short BFP quotient
000003FC	B29C 8000		00000000	387 STFPC 0(R8) Store resulting FPCR flags and DXC
00000400	B222 0000			388 IPM R0 Retrieve condition code
00000404	8800 001C		0000001C	389 SRL R0,28 Move CC to low-order r0, dump prog mask
00000408	4200 8003		00000003	390 STC R0,3(,R8) Store in last byte of FPCR
				391 *
				392 * FPR1 still has divisor, FPR0 has remainder, FPR2 has integer quotient
				393 *
0000040C	B29D F2F4		000002F4	394 LFPC FPCREGNT Set exceptions non-trappable
00000410	B30E 0021			395 MAEBR FPR0,FPR2,FPR1 Multiply and add to recreate inputs
				396 * Sum of product and remainder in FPR0
00000414	7000 7008		00000008	397 STE FPR0,8(,R7) Store short BFP product-sum
00000418	B29C 8004		00000004	398 STFPC 4(R8) Store resulting FPCR flags and DXC
0000041C	B222 0000			399 IPM R0 Retrieve condition code
00000420	8800 001C		0000001C	400 SRL R0,28 Move CC to low-order r0, dump prog mask
00000424	4200 8007		00000007	401 STC R0,7(,R8) Store in last byte of FPCR
				402 *
00000428	B29D F2F8		000002F8	403 LFPC FPCREGTR Set exceptions trappable
0000042C	7800 3000		00000000	404 LE FPR0,0(,R3) Get short BFP dividend
00000430	7810 3004		00000004	405 LE FPR1,4(,R3) Get short BFP divisor
00000434	B374 0020			406 LZER FPR2 Zero remainder register
00000438	B353 2001			407 DIEBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				408 *	Quotient in FPR2, remainder in FPR0
0000043C	7000 7010		00000010	409	STE FPR0,16(,R7) Store short BFP remainder
00000440	7020 7014		00000014	410	STE FPR2,20(,R7) Store short BFP quotient
00000444	B29C 8008		00000008	411	STFPC 8(R8) Store resulting FPCR flags and DXC
00000448	B222 0000			412	IPM R0 Retrieve condition code
0000044C	8800 001C		0000001C	413	SRL R0,28 Move CC to low-order r0, dump prog mask
00000450	4200 800B		0000000B	414	STC R0,11(,R8) Store in last byte of FPCR
				415 *	
				416 *	FPR1 still has divisor, FPR0 has remainder, FPR2 has integer quotient
				417 *	
00000454	B29D F2F4		000002F4	418	LFPC FPCREGNT Set exceptions non-trappable
00000458	B30E 0021			419	MAEBR FPR0,FPR2,FPR1 Multiply and add to recreate inputs
				420 *	Sum of product and remainder in FPR0
0000045C	7000 7018		00000018	421	STE FPR0,24(,R7) Store short BFP remainder
00000460	B29C 800C		0000000C	422	STFPC 12(R8) Store resulting FPCR flags and DXC
00000464	B222 0000			423	IPM R0 Retrieve condition code
00000468	8800 001C		0000001C	424	SRL R0,28 Move CC to low-order r0, dump prog mask
0000046C	4200 800F		0000000F	425	STC R0,15(,R8) Store in last byte of FPCR
				426	
				427 *	
00000470	4130 3008		00000008	428	LA R3,8(,R3) Point to next input value pair
00000474	4170 7020		00000020	429	LA R7,32(,R7) Point to next quo&rem result value pair
00000478	4180 8010		00000010	430	LA R8,16(,R8) Point to next FPCR result area
0000047C	062C			431	BCTR R2,R12 Convert next input value.
0000047E	07FD			432	BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				434 *****
				435 *
				436 * The next tests operate on finite number input pairs and exhaustively
				437 * test rounding modes and partial and final results.
				438 *
				439 * Two rounding modes can be specified for each operation: one for the
				440 * quotient, specified in the M4 field, and the second for the
				441 * remainder, specified in the FPCR.
				442 *
				443 * Because six unique rounding modes can be specified in for the
				444 * quotient and four for the remainder, there are a lot of results that
				445 * need to be evaluated. Note: M4 rounding mode zero, use FPCR rounding
				446 * mode, is not tested because it duplicates one of the six explicit
				447 * M4 rounding modes. Which one depends on the current FPCR setting.
				448 *
				449 * The M4 rounding mode is assembled into the instruction. Back in the
				450 * day, this would be a perfect candidate for an Execute instructoin.
				451 * But the M4 field is not located such that it can be modified by
				452 * an Execute instruction. So we will still use Execute, but only to
				453 * select one of six DIEBR instructions for execution. That way we can
				454 * build an outer loop to iterate through the four FPCR modes, and an
				455 * inner loop to use each of the six M4-specified rounding modes.
				456 *
				457 *****
00000480	9823 A000		00000000	459 DIEBRM LM R2,R3,0(R10) Get count and address of test input values
00000484	9878 A008		00000008	460 LM R7,R8,8(R10) Get address of result area and flag area.
00000488	1222			461 LTR R2,R2 Any test cases?
0000048A	078D			462 BZR R13 ..No, return to caller
0000048C	1711			463 XR R1,R1 Zero register 1 for use in IC/STC/indexing
0000048E	0DC0			464 BASR R12,0 Set top of test case loop
				465
00000490	4150 0004		00000004	466 LA R5,FPCMCT Get count of FPC modes to be tested
00000494	0D90			467 BASR R9,0 Set top of rounding mode outer loop
				468 *
				469 * Update model FPC register settings with the BFP rounding mode for
				470 * this iteration of the loop.
				471 *
00000496	4315 F6F3		000006F3	472 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				473 *
0000049A	4140 0006		00000006	474 LA R4,D2IMCT Get count of M4 modes to be tested
0000049E	0D60			475 BASR R6,0 Set top of rounding mode inner loop
				476 *
				477 * Non-trap execution of the instruction.
				478 *
000004A0	B29D F2F4		000002F4	479 LFPC FPCREGNT Set exceptions non-trappable, clear flags
000004A4	4315 F6F3		000006F3	480 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
000004A8	B2B8 1000		00000000	481 SRNMB 0(R1) Set FPC Rounding Mode
000004AC	7800 3000		00000000	482 LE FPR0,0(,R3) Get short BFP dividend
000004B0	7810 3004		00000004	483 LE FPR1,4(,R3) Get short BFP divisor
000004B4	B374 0020			484 LZER FPR2 Zero remainder register
000004B8	4314 F6F7		000006F7	485 IC R1,D2IMODES-L'D2IMODES(R4) Get index DIEBR inst table
000004BC	4401 F700		00000700	486 EX 0,DIEBRTAB(R1) Execute Divide to Integer
000004C0	7000 7000		00000000	487 STE FPR0,0(,R7) Store short BFP remainder
000004C4	7020 7004		00000004	488 STE FPR2,4(,R7) Store short BFP quotient

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000004C8	B29C 8000		00000000	489 STFPC 0(R8) Store resulting FPCR flags and DXC
000004CC	B222 0000			490 IPM R0 Retrieve condition code
000004D0	8800 001C		0000001C	491 SRL R0,28 Move CC to low-order r0, dump prog mask
000004D4	4200 8003		00000003	492 STC R0,3(,R8) Store in last byte of FPCR
				493 *
				494 * Trap-enabled execution of the instruction.
				495 *
000004D8	B29D F2F8		000002F8	496 LFPC FPCREGTR Set exceptions trappable, clear flags
000004DC	4315 F6F3		000006F3	497 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
000004E0	B2B8 1000		00000000	498 SRNMB 0(R1) Set FPC Rounding Mode
000004E4	7800 3000		00000000	499 LE FPR0,0(,R3) Get short BFP dividend
000004E8	7810 3004		00000004	500 LE FPR1,4(,R3) Get short BFP divisor
000004EC	B374 0020			501 LZER FPR2 Zero remainder register
000004F0	4314 F6F7		000006F7	502 IC R1,D2IMODES-L'D2IMODES(R4) Get index DIEBR inst table
000004F4	4401 F700		00000700	503 EX 0,DIEBRTAB(R1) Execute Divide to Integer
000004F8	7000 7008		00000008	504 STE FPR0,8(,R7) Store short BFP remainder
000004FC	7020 700C		0000000C	505 STE FPR2,12(,R7) Store short BFP quotient
00000500	B29C 8004		00000004	506 STFPC 4(R8) Store resulting FPCR flags and DXC
00000504	B222 0000			507 IPM R0 Retrieve condition code
00000508	8800 001C		0000001C	508 SRL R0,28 Move CC to low-order r0, dump prog mask
0000050C	4200 8007		00000007	509 STC R0,7(,R8) Store in last byte of FPCR
				510 *
00000510	4170 7010		00000010	511 LA R7,16(,R7) Point to next quo&rem result value pair
00000514	4180 8008		00000008	512 LA R8,8(,R8) Point to next FPCR result area
				513 *
00000518	0646			514 BCTR R4,R6 Iterate inner loop
				515 *
				516 * End of M4 modes to be tested.
				517 *
0000051A	0659			518 BCTR R5,R9 Iterate outer loop
				519 *
				520 * End of FPC modes to be tested with each M4 mode. Advance to
				521 * next test case.
				522 *
0000051C	4130 3008		00000008	523 LA R3,8(,R3) Point to next input value pair
00000520	062C			524 BCTR R2,R12 Divide next input value lots of times
				525 *
00000522	07FD			526 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				528 *****
				529 *
				530 * Perform Divide to Integer using provided long BFP input pairs. This
				531 * set of tests checks NaN propagation and operations on values that are
				532 * not finite numbers.
				533 *
				534 * A pair of results is generated for each input: one with all
				535 * exceptions non-trappable, and the second with all exceptions
				536 * trappable. The FPCR and condition code is stored for each result.
				537 *
				538 *****
00000524	9823 A000		00000000	540 DIDBRNF LM R2,R3,0(R10) Get count and address of test input values
00000528	9878 A008		00000008	541 LM R7,R8,8(R10) Get address of result area and flag area.
0000052C	1222			542 LTR R2,R2 Any test cases?
0000052E	078D			543 BZR R13 ..No, return to caller
00000530	0DC0			544 BASR R12,0 Set top of loop
				545 *
00000532	B29D F2F4		000002F4	546 LFPC FPCREGNT Set exceptions non-trappable
00000536	6800 3000		00000000	547 LD FPR0,0(,R3) Get long BFP dividend
0000053A	6810 3008		00000008	548 LD FPR1,8(,R3) Get long BFP divisor
0000053E	B375 0020			549 LZDR FPR2 Zero remainder register
00000542	B35B 2001			550 DIDBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				551 * Quotient in FPR2, remainder in FPR0
00000546	6000 7000		00000000	552 STD FPR0,0(,R7) Store long BFP remainder
0000054A	6020 7008		00000008	553 STD FPR2,8(,R7) Store long BFP quotient
0000054E	B29C 8000		00000000	554 STFPC 0(R8) Store resulting FPCR flags and DXC
00000552	B222 0000			555 IPM R0 Retrieve condition code
00000556	8800 001C		0000001C	556 SRL R0,28 Move CC to low-order r0, dump prog mask
0000055A	4200 8003		00000003	557 STC R0,3(,R8) Store in last byte of FPCR
				558 *
0000055E	B29D F2F8		000002F8	559 LFPC FPCREGTR Set exceptions trappable
00000562	6800 3000		00000000	560 LD FPR0,0(,R3) Get long BFP dividend
00000566	6810 3008		00000008	561 LD FPR1,8(,R3) Get long BFP divisor
0000056A	B375 0020			562 LZDR FPR2 Zero remainder register
0000056E	B35B 2001			563 DIDBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				564 * Quotient in FPR2, remainder in FPR0
00000572	6000 7010		00000010	565 STD FPR0,16(,R7) Store long BFP remainder
00000576	6020 7018		00000018	566 STD FPR2,24(,R7) Store long BFP quotient
0000057A	B29C 8004		00000004	567 STFPC 4(R8) Store resulting FPCR flags and DXC
0000057E	B222 0000			568 IPM R0 Retrieve condition code
00000582	8800 001C		0000001C	569 SRL R0,28 Move CC to low-order r0, dump prog mask
00000586	4200 8007		00000007	570 STC R0,7(,R8) Store in last byte of FPCR
				571 *
0000058A	4130 3010		00000010	572 LA R3,16(,R3) Point to next input value pair
0000058E	4170 7020		00000020	573 LA R7,32(,R7) Point to next quo&rem result value pair
00000592	4180 8008		00000008	574 LA R8,8(,R8) Point to next FPCR result area
00000596	062C			575 BCTR R2,R12 Convert next input value.
00000598	07FD			576 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				578 *****
				579 *
				580 * Perform Divide to Integer using provided long BFP input pairs. This
				581 * set of tests performs basic checks of Divide To Integer emulation
				582 * where both inputs are finite non-zero numbers.
				583 *
				584 * Four results (six values) are generated for each input:
				585 * 1) Divide to integer with all exceptions non-trappable (two values)
				586 * 2) Multiply integer quotient by divisor, add remainder (one value)
				587 * 3) Divide to integer with all exceptions trappable (two values)
				588 * 4) Multiply integer quotient by divisor, add remainder (one value)
				589 *
				590 * The FPCR and condition code is stored for each result. Note: the
				591 * Multiply and Add instruction does not set the condition code.
				592 *
				593 * Results two and four (multiply and add) validate the calculation
				594 * of the integer quotient and remainder.
				595 *
				596 *****
0000059A	9823 A000		00000000	598 DIDBRF LM R2,R3,0(R10) Get count and address of test input values
0000059E	9878 A008		00000008	599 LM R7,R8,8(R10) Get address of result area and flag area.
000005A2	1222			600 LTR R2,R2 Any test cases?
000005A4	078D			601 BZR R13 ..No, return to caller
000005A6	0DC0			602 BASR R12,0 Set top of loop
				603 *
000005A8	B29D F2F4		000002F4	604 LFPC FPCREGNT Set exceptions non-trappable
000005AC	6800 3000		00000000	605 LD FPR0,0*32+0(,R3) Get long BFP dividend
000005B0	6810 3008		00000008	606 LD FPR1,0*32+8(,R3) Get long BFP divisor
000005B4	B375 0020			607 LZDR FPR2 Zero remainder register
000005B8	B35B 2001			608 DIDBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				609 * Quotient in FPR2, remainder in FPR0
000005BC	6000 7000		00000000	610 STD FPR0,0*32+0(,R7) Store long BFP remainder
000005C0	6020 7008		00000008	611 STD FPR2,0*32+8(,R7) Store long BFP quotient
000005C4	B29C 8000		00000000	612 STFPC 0*4(R8) Store resulting FPCR flags and DXC
000005C8	B222 0000			613 IPM R0 Retrieve condition code
000005CC	8800 001C		0000001C	614 SRL R0,28 Move CC to low-order r0, dump prog mask
000005D0	4200 8003		00000003	615 STC R0,0*4+3(,R8) Store in last byte of FPCR
				616 *
				617 * FPR1 still has divisor, FPR0 has remainder, FPR2 has integer quotient
				618 *
000005D4	B29D F2F4		000002F4	619 LFPC FPCREGNT Set exceptions non-trappable
000005D8	B31E 0021			620 MADBR FPR0,FPR2,FPR1 Multiply and add to recreate inputs
				621 * Sum of product and remainder in FPR0
000005DC	6000 7010		00000010	622 STD FPR0,0*32+16(,R7) Store short BFP product-sum
000005E0	B29C 8004		00000004	623 STFPC 1*4(R8) Store resulting FPCR flags and DXC
000005E4	B222 0000			624 IPM R0 Retrieve condition code
000005E8	8800 001C		0000001C	625 SRL R0,28 Move CC to low-order r0, dump prog mask
000005EC	4200 8007		00000007	626 STC R0,1*4+3(,R8) Store in last byte of FPCR
				627 *
000005F0	B29D F2F8		000002F8	628 LFPC FPCREGTR Set exceptions trappable
000005F4	6800 3000		00000000	629 LD FPR0,0(,R3) Get long BFP dividend
000005F8	6810 3008		00000008	630 LD FPR1,8(,R3) Get long BFP divisor
000005FC	B375 0020			631 LZDR FPR2 Zero remainder register
00000600	B35B 2001			632 DIDBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				633 * Quotient in FPR2, remainder in FPR0
00000604	6000 7020		00000020	634 STD FPR0,1*32+0(,R7) Store long BFP remainder
00000608	6020 7028		00000028	635 STD FPR2,1*32+8(,R7) Store long BFP quotient
0000060C	B29C 8008		00000008	636 STFPC 2*4(R8) Store resulting FPCR flags and DXC
00000610	B222 0000			637 IPM R0 Retrieve condition code
00000614	8800 001C		0000001C	638 SRL R0,28 Move CC to low-order r0, dump prog mask
00000618	4200 800B		0000000B	639 STC R0,2*4+3(,R8) Store in last byte of FPCR
				640 *
				641 * FPR1 still has divisor, FPR0 has remainder, FPR2 has integer quotient
				642 *
0000061C	B29D F2F4		000002F4	643 LFPC FPCREGNT Set exceptions non-trappable
00000620	B31E 0021			644 MADBR FPR0,FPR2,FPR1 Multiply and add to recreate inputs
				645 * Sum of product and remainder in FPR0
00000624	6000 7030		00000030	646 STD FPR0,1*32+16(,R7) Store short BFP product-sum
00000628	B29C 800C		0000000C	647 STFPC 3*4(R8) Store resulting FPCR flags and DXC
0000062C	B222 0000			648 IPM R0 Retrieve condition code
00000630	8800 001C		0000001C	649 SRL R0,28 Move CC to low-order r0, dump prog mask
00000634	4200 800F		0000000F	650 STC R0,3*4+3(,R8) Store in last byte of FPCR
				651 *
00000638	4130 3010		00000010	652 LA R3,16(,R3) Point to next input value pair
0000063C	4170 7040		00000040	653 LA R7,64(,R7) Point to next quo&rem result value pair
00000640	4180 8010		00000010	654 LA R8,16(,R8) Point to next FPCR result area
00000644	062C			655 BCTR R2,R12 Convert next input value.
00000646	07FD			656 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				658 *****
				659 *
				660 * The next tests operate on finite number input pairs and exhaustively
				661 * test rounding modes and partial and final results.
				662 *
				663 * Two rounding modes can be specified for each operation: one for the
				664 * quotient, specified in the M4 field, and the second for the
				665 * remainder, specified in the FPCR.
				666 *
				667 * Because six unique rounding modes can be specified in for the
				668 * quotient and four for the remainder, there are a lot of results that
				669 * need to be evaluated. Note: M4 rounding mode zero, use FPCR rounding
				670 * mode, is not tested because it duplicates one of the six explicit
				671 * M4 rounding modes. Which one depends on the current FPCR setting.
				672 *
				673 * The M4 rounding mode is assembled into the instruction. Back in the
				674 * day, this would be a perfect candidate for an Execute instructoin.
				675 * But the M4 field is not located such that it can be modified by
				676 * an Execute instruction. So we will still use Execute, but only to
				677 * select one of six DIEBR instructions for execution. That way we can
				678 * build an outer loop to iterate through the four FPCR modes, and an
				679 * inner loop to use each of the six M4-specified rounding modes.
				680 *
				681 *****
00000648	9823 A000		00000000	683 DIDBRM LM R2,R3,0(R10) Get count and address of test input values
0000064C	9878 A008		00000008	684 LM R7,R8,8(R10) Get address of result area and flag area.
00000650	1222			685 LTR R2,R2 Any test cases?
00000652	078D			686 BZR R13 ..No, return to caller
00000654	1711			687 XR R1,R1 Zero register 1 for use in IC/STC/indexing
00000656	0DC0			688 BASR R12,0 Set top of test case loop
				689
00000658	4150 0004		00000004	690 LA R5,FPCMCT Get count of FPC modes to be tested
0000065C	0D90			691 BASR R9,0 Set top of rounding mode outer loop
				692 *
				693 * Update model FPC register settings with the BFP rounding mode for
				694 * this iteration of the loop.
				695 *
0000065E	4315 F6F3		000006F3	696 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
00000662	4210 F2F7		000002F7	697 STC R1,FPCREGNT+3 Update non-trap register settings
00000666	4210 F2FB		000002FB	698 STC R1,FPCREGTR+3 Update trap-enabled register settings
				699 *
0000066A	4140 0006		00000006	700 LA R4,D2IMCT Get count of M4 modes to be tested
0000066E	0D60			701 BASR R6,0 Set top of rounding mode inner loop
				702 *
				703 * Non-trap execution of the instruction.
				704 *
00000670	B29D F2F4		000002F4	705 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000674	4315 F6F3		000006F3	706 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
00000678	B2B8 1000		00000000	707 SRNMB 0(R1) Set FPC Rounding Mode
0000067C	6800 3000		00000000	708 LD FPR0,0(,R3) Get short BFP dividend
00000680	6810 3008		00000008	709 LD FPR1,8(,R3) Get short BFP divisor
00000684	B375 0020			710 LZDR FPR2 Zero remainder register
00000688	4314 F6F7		000006F7	711 IC R1,D2IMODES-L'D2IMODES(R4) Get index DIEBR inst table
0000068C	4401 F71C		0000071C	712 EX 0,DIDBRTAB(R1) Execute Divide to Integer

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00000690	6000 7000		00000000	713	STD	FPR0,0(,R7)	Store short BFP remainder
00000694	6020 7008		00000008	714	STD	FPR2,8(,R7)	Store short BFP quotient
00000698	B29C 8000		00000000	715	STFPC	0(R8)	Store resulting FPCR flags and DXC
0000069C	B222 0000			716	IPM	R0	Retrieve condition code
000006A0	8800 001C		0000001C	717	SRL	R0,28	Move CC to low-order r0, dump prog mask
000006A4	4200 8003		00000003	718	STC	R0,3(,R8)	Store in last byte of FPCR
				719	*		
				720	*	Trap-enabled execution of the instruction.	
				721	*		
000006A8	B29D F2F8		000002F8	722	LFPC	FPCREGTR	Set exceptions trappable, clear flags
000006AC	4315 F6F3		000006F3	723	IC	R1,FPCMODES-L'	FPCMODES(R5) Get next FPC mode
000006B0	B2B8 1000		00000000	724	SRNMB	0(R1)	Set FPC Rounding Mode
000006B4	6800 3000		00000000	725	LD	FPR0,0(,R3)	Get short BFP dividend
000006B8	6810 3008		00000008	726	LD	FPR1,8(,R3)	Get short BFP divisor
000006BC	B374 0020			727	LZER	FPR2	Zero remainder register
000006C0	4314 F6F7		000006F7	728	IC	R1,D2IMODES-L'	D2IMODES(R4) Get index DIEBR inst table
000006C4	4401 F71C		0000071C	729	EX	0,DIDBRTAB(R1)	Execute Divide to Integer
000006C8	6000 7010		00000010	730	STD	FPR0,16(,R7)	Store short BFP remainder
000006CC	6020 7018		00000018	731	STD	FPR2,24(,R7)	Store short BFP quotient
000006D0	B29C 8004		00000004	732	STFPC	4(R8)	Store resulting FPCR flags and DXC
000006D4	B222 0000			733	IPM	R0	Retrieve condition code
000006D8	8800 001C		0000001C	734	SRL	R0,28	Move CC to low-order r0, dump prog mask
000006DC	4200 8007		00000007	735	STC	R0,7(,R8)	Store in last byte of FPCR
				736	*		
000006E0	4170 7020		00000020	737	LA	R7,32(,R7)	Point to next quo&rem result value pair
000006E4	4180 8008		00000008	738	LA	R8,8(,R8)	Point to next FPCR result area
				739	*		
000006E8	0646			740	BCTR	R4,R6	Iterate inner loop
				741	*		
				742	*	End of M4 modes to be tested.	
				743	*		
000006EA	0659			744	BCTR	R5,R9	Iterate outer loop
				745	*		
				746	*	End of FPC modes to be tested with each M4 mode. Advance to	
				747	*	next test case.	
				748	*		
000006EC	4130 3010		00000010	749	LA	R3,16(,R3)	Point to next input value pair
000006F0	062C			750	BCTR	R2,R12	Divide next input value lots of times
				751	*		
000006F2	07FD			752	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				754 *****
				755 *
				756 * Tables and indices used to exhaustively test remainder and quotient
				757 * rounding modes.
				758 *
				759 * The Execute instruction with an appropriate index * is used to
				760 * execute the correct DIEBR/DIDBR instruction. Because * the quotient
				761 * rounding mode is encoded in the DIXBR instruction in the wrong place
				762 * to use Execute to dynamically modify the rounding mode, we will just
				763 * use it to select the correct instruction.
				764 *
				765 * The Set BFP Rounding Mode does allow specification of the FPC
				766 * rounding mode as an address, so we shall index into a table of
				767 * BFP rounding modes without bothering with Execute.
				768 *
				769 *****
				771 *
				772 * Rounding modes that may be set in the FPCR. The FPCR controls
				773 * rounding of the quotient. The same table is used for both DIEBR
				774 * and DIDBR instruction testing.
				775 *
				776 * These are indexed directly by the loop counter, which counts down.
				777 * So the modes are listed in reverse order here.
				778 *
000006F4				779 FPCMODES DS 0C
000006F4	07			780 DC AL1(7) RFS, Round for shorter precision
000006F5	03			781 DC AL1(3) RM, Round to -infinity
000006F6	02			782 DC AL1(2) RP, Round to +infinity
000006F7	01			783 DC AL1(1) RZ, Round to zero
				784 *** DC AL1(0) RNTE, Round to Nearest, ties to even
		00000004	00000001	785 FPCMCT EQU *-FPCMODES Count of FPC Modes to be tested
				786 *
				787 * Table of indices into table of DIDBR/DIEBR instructions. The table
				788 * is used for both DIDBR and DIEBR, with the table value being used
				789 * as the index register of an Execute instruction that points to
				790 * either the DIDBR or DIEBR instruction list.
				791 *
				792 * These are indexed directly by the loop counter, which counts down.
				793 * So the instruction indices are listed in reverse order here.
				794 *
000006F8				795 D2IMODES DS 0C
000006F8	18			796 DC AL1(6*4) RM, Round to -infinity
000006F9	14			797 DC AL1(5*4) RP, Round to +infinity
000006FA	10			798 DC AL1(4*4) RZ, Round to zero
000006FB	0C			799 DC AL1(3*4) RNTE, Round to Nearest, ties to even
000006FC	08			800 DC AL1(2*4) RFS, Round for Shorter Precision
000006FD	04			801 DC AL1(1*4) RNTA, Round to Nearest, ties away
				802 *** DC AL1(0*4) Use FPCR rounding mode
		00000006	00000001	803 D2IMCT EQU *-D2IMODES Count of M4 Modes to be tested
				804 *
				805 * List of DIEBR instructions, each with a different rounding mode.
				806 * These are Execute'd by the rounding mode test routing using an index
				807 * obtained from the D2IMODES table above.
				808 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				809 * This table and the DIDBRTAB table below should remain in the same
				810 * sequence, or you'll be scratching your head keeping the result order
				811 * straight between short and long results.
				812 *
00000700				813 DIEBRTAB DS 0F Table of DIEBR instructions
00000700	B353	2001		814 DIEBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				815 * Above is not used
00000704	B353	2101		816 DIEBR FPR0,FPR2,FPR1,1 Div to Int FPR0/FPR1, M4=RNTA
00000708	B353	2301		817 DIEBR FPR0,FPR2,FPR1,3 Div to Int FPR0/FPR1, M4=RFS
0000070C	B353	2401		818 DIEBR FPR0,FPR2,FPR1,4 Div to Int FPR0/FPR1, M4=RNTE
00000710	B353	2501		819 DIEBR FPR0,FPR2,FPR1,5 Div to Int FPR0/FPR1, M4=RZ
00000714	B353	2601		820 DIEBR FPR0,FPR2,FPR1,6 Div to Int FPR0/FPR1, M4=RP
00000718	B353	2701		821 DIEBR FPR0,FPR2,FPR1,7 Div to Int FPR0/FPR1, M4=RM
				822 *
				823 * List of DIDBR instructions, each with a different rounding mode.
				824 * These are Execute'd by the rounding mode test routing using an index
				825 * obtained from the D2IMODES table above.
				826 *
				827 * This table and the DIEBRTAB table above should remain in the same
				828 * sequence, or you'll be scratching your head keeping the result order
				829 * straight between short and long results.
				830 *
0000071C				831 DIDBRTAB DS 0F Table of DIDBR instructions
0000071C	B35B	2001		832 DIDBR FPR0,FPR2,FPR1,0 Div to Int FPR0/FPR1, M4=use FPCR
				833 * Above is not used
00000720	B35B	2101		834 DIDBR FPR0,FPR2,FPR1,1 Div to Int FPR0/FPR1, M4=RNTA
00000724	B35B	2301		835 DIDBR FPR0,FPR2,FPR1,3 Div to Int FPR0/FPR1, M4=RFS
00000728	B35B	2401		836 DIDBR FPR0,FPR2,FPR1,4 Div to Int FPR0/FPR1, M4=RNTE
0000072C	B35B	2501		837 DIDBR FPR0,FPR2,FPR1,5 Div to Int FPR0/FPR1, M4=RZ
00000730	B35B	2601		838 DIDBR FPR0,FPR2,FPR1,6 Div to Int FPR0/FPR1, M4=RP
00000734	B35B	2701		839 DIDBR FPR0,FPR2,FPR1,7 Div to Int FPR0/FPR1, M4=RM
				840 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				842 *****
				843 *
				844 * Short integer test data sets for Divide to Integer testing.
				845 *
				846 * Each test data set member consists of two values, the dividend and
				847 * the divisor, in that order.
				848 *
				849 * The first test data set is used for tests of basic functionality,
				850 * NaN propagation, and results from operations involving other than
				851 * finite numbers.
				852 *
				853 * The secondd test data set is used for testing boundary conditions
				854 * using two finite non-zero values. Each possible condition code
				855 * and type of result (normal, scaled, etc) is created by members of
				856 * this test data set.
				857 *
				858 * The third test data set is used for exhaustive testing of final
				859 * results across the panoply of rounding mode combinations available
				860 * for Divide to Integer (five for the remainder, seven for the
				861 * quotient).
				862 *
				863 *****
				865 *
				866 * First input test data set, to test operations using non-finite or
				867 * zero inputs. Member values chosen to validate part 1 of Figure 19-21
				868 * on page 19-29 of SA22-7832-10.
				869 *
00000738				870 SBFPNFIN DS 0F Inputs for short BFP non-finite tests
				871 *
				872 * NaN propagation tests (Tests 1-4)
				873 *
00000738	7F8A0000			874 DC X'7F8A0000' SNaN
0000073C	7F8B0000			875 DC X'7F8B0000' SNaN
				876 *
00000740	7FCA0000			877 DC X'7FCA0000' QNaN
00000744	7FCB0000			878 DC X'7FCB0000' QNaN
				879 *
00000748	40000000			880 DC X'40000000' Finite number
0000074C	7FCB0000			881 DC X'7FCB0000' QNaN
				882 *
00000750	7FCA0000			883 DC X'7FCA0000' QNaN
00000754	7F8B0000			884 DC X'7F8B0000' SNaN
				885 *
				886 * Dividend is -inf (Tests 5-10)
				887 *
00000758	FF800000			888 DC X'FF800000' -inf
0000075C	FF800000			889 DC X'FF800000' -inf
				890 *
00000760	FF800000			891 DC X'FF800000' -inf
00000764	C0000000			892 DC X'C0000000' -2.0
				893 *
00000768	FF800000			894 DC X'FF800000' -inf
0000076C	80000000			895 DC X'80000000' -0
				896 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00000770	FF800000			897	DC	X'FF800000'	-inf
00000774	00000000			898	DC	X'00000000'	+0
				899 *			
00000778	FF800000			900	DC	X'FF800000'	-inf
0000077C	40000000			901	DC	X'40000000'	+2.0
				902 *			
00000780	FF800000			903	DC	X'FF800000'	-inf
00000784	7F800000			904	DC	X'7F800000'	+inf
				905 *			
				906 *	Dividend is +inf (Tests 11-16)		
				907 *			
00000788	7F800000			908	DC	X'7F800000'	+inf
0000078C	FF800000			909	DC	X'FF800000'	-inf
				910 *			
00000790	7F800000			911	DC	X'7F800000'	+inf
00000794	C0000000			912	DC	X'C0000000'	-2.0
				913 *			
00000798	7F800000			914	DC	X'7F800000'	+inf
0000079C	80000000			915	DC	X'80000000'	-0
				916 *			
000007A0	7F800000			917	DC	X'7F800000'	+inf
000007A4	00000000			918	DC	X'00000000'	+0
				919 *			
000007A8	7F800000			920	DC	X'7F800000'	+inf
000007AC	40000000			921	DC	X'40000000'	+2.0
				922 *			
000007B0	7F800000			923	DC	X'7F800000'	+inf
000007B4	7F800000			924	DC	X'7F800000'	+inf
				925 *			
				926 *	Divisor is -0. (+/-inf dividend tested above)		
				927 *	(Tests 17-20)		
				928 *			
000007B8	C0000000			929	DC	X'C0000000'	-2.0
000007BC	80000000			930	DC	X'80000000'	-0
				931 *			
000007C0	80000000			932	DC	X'80000000'	-0
000007C4	80000000			933	DC	X'80000000'	-0
				934 *			
000007C8	00000000			935	DC	X'00000000'	+0
000007CC	80000000			936	DC	X'80000000'	-0
				937 *			
000007D0	40000000			938	DC	X'40000000'	+2.0
000007D4	80000000			939	DC	X'80000000'	-0
				940 *			
				941 *	Divisor is +0. (+/-inf dividend tested above)		
				942 *	(Tests 21-24)		
				943 *			
000007D8	C0000000			944	DC	X'C0000000'	-2.0
000007DC	00000000			945	DC	X'00000000'	+0
				946 *			
000007E0	80000000			947	DC	X'80000000'	-0
000007E4	00000000			948	DC	X'00000000'	+0
				949 *			
000007E8	00000000			950	DC	X'00000000'	+0
000007EC	00000000			951	DC	X'00000000'	+0
				952 *			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000007F0	40000000			953 DC X'40000000' +2.0
000007F4	00000000			954 DC X'00000000' +0
				955 *
				956 * Divisor is -inf. (+/-inf dividend tested above)
				957 * (Tests 25-28)
				958 *
000007F8	C0000000			959 DC X'C0000000' -2.0
000007FC	FF800000			960 DC X'FF800000' -inf
				961 *
00000800	80000000			962 DC X'80000000' -0
00000804	FF800000			963 DC X'FF800000' -inf
				964 *
00000808	00000000			965 DC X'00000000' +0
0000080C	FF800000			966 DC X'FF800000' -inf
				967 *
00000810	40000000			968 DC X'40000000' +2.0
00000814	FF800000			969 DC X'FF800000' -inf
				970 *
				971 * Divisor is +inf. (+/-inf dividend tested above)
				972 * (Tests 29-32)
				973 *
00000818	C0000000			974 DC X'C0000000' -2.0
0000081C	7F800000			975 DC X'7F800000' +inf
				976 *
00000820	80000000			977 DC X'80000000' -0
00000824	7F800000			978 DC X'7F800000' +inf
				979 *
00000828	00000000			980 DC X'00000000' +0
0000082C	7F800000			981 DC X'7F800000' +inf
				982 *
00000830	40000000			983 DC X'40000000' +2.0
00000834	7F800000			984 DC X'7F800000' +inf
				985 *
	00000020	00000001		986 SBFPNFCT EQU (*-SBFPNFIN)/4/2 Count of short BFP in list
				988 *****
				989 *
				990 * Second input test data set. These are finite pairs intended to
				991 * test all combinations of finite values and results (final
				992 * results due to remainder zero, final results due to quotient
				993 * within range, and partial results.
				994 *
				995 *****
00000838				997 SBFPIN DS 0F Inputs for short BFP finite tests
				998 *
				999 * Dividend and Divisor are both finite numbers.
				1000 *
				1001 * Remainder tests from SA22-7832-10, Figure 19-7 on page 19-6
				1002 * (Finite tests 1-16; negative divisor)
				1003 *
00000838	C1000000			1004 DC X'C1000000' -8
0000083C	C0800000			1005 DC X'C0800000' -4

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				1006 *				
00000840	C0E00000			1007	DC	X'C0E00000'	-7	
00000844	C0800000			1008	DC	X'C0800000'	-4	
				1009 *				
00000848	C0C00000			1010	DC	X'C0C00000'	-6	
0000084C	C0800000			1011	DC	X'C0800000'	-4	
				1012 *				
00000850	C0A00000			1013	DC	X'C0A00000'	-5	
00000854	C0800000			1014	DC	X'C0800000'	-4	
				1015 *				
00000858	C0800000			1016	DC	X'C0800000'	-4	
0000085C	C0800000			1017	DC	X'C0800000'	-4	
				1018 *				
00000860	C0400000			1019	DC	X'C0400000'	-3	
00000864	C0800000			1020	DC	X'C0800000'	-4	
				1021 *				
00000868	C0000000			1022	DC	X'C0000000'	-2	
0000086C	C0800000			1023	DC	X'C0800000'	-4	
				1024 *				
00000870	BF800000			1025	DC	X'BF800000'	-1	
00000874	C0800000			1026	DC	X'C0800000'	-4	
				1027 *				
				1028 *	The +/- zero - +/- zero cases are handled above and skipped here			
				1029 *				
00000878	3F800000			1030	DC	X'3F800000'	+1	
0000087C	C0800000			1031	DC	X'C0800000'	-4	
				1032 *				
00000880	40000000			1033	DC	X'40000000'	+2	
00000884	C0800000			1034	DC	X'C0800000'	-4	
				1035 *				
00000888	40400000			1036	DC	X'40400000'	+3	
0000088C	C0800000			1037	DC	X'C0800000'	-4	
				1038 *				
00000890	40800000			1039	DC	X'40800000'	+4	
00000894	C0800000			1040	DC	X'C0800000'	-4	
				1041 *				
00000898	40A00000			1042	DC	X'40A00000'	+5	
0000089C	C0800000			1043	DC	X'C0800000'	-4	
				1044 *				
000008A0	40C00000			1045	DC	X'40C00000'	+6	
000008A4	C0800000			1046	DC	X'C0800000'	-4	
				1047 *				
000008A8	40E00000			1048	DC	X'40E00000'	+7	
000008AC	C0800000			1049	DC	X'C0800000'	-4	
				1050 *				
000008B0	41000000			1051	DC	X'41000000'	+8	
000008B4	C0800000			1052	DC	X'C0800000'	-4	
				1053 *				
				1054 *	Finite tests 17-32; positive divisor			
				1055 *				
000008B8	C1000000			1056	DC	X'C1000000'	-8	
000008BC	40800000			1057	DC	X'40800000'	+4	
				1058 *				
000008C0	C0E00000			1059	DC	X'C0E00000'	-7	
000008C4	40800000			1060	DC	X'40800000'	+4	
				1061 *				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000008C8	C0C00000			1062	DC	X'C0C00000'	-6
000008CC	40800000			1063	DC	X'40800000'	+4
				1064 *			
000008D0	C0A00000			1065	DC	X'C0A00000'	-5
000008D4	40800000			1066	DC	X'40800000'	+4
				1067 *			
000008D8	C0800000			1068	DC	X'C0800000'	-4
000008DC	40800000			1069	DC	X'40800000'	+4
				1070 *			
000008E0	C0400000			1071	DC	X'C0400000'	-3
000008E4	40800000			1072	DC	X'40800000'	+4
				1073 *			
000008E8	C0000000			1074	DC	X'C0000000'	-2
000008EC	40800000			1075	DC	X'40800000'	+4
				1076 *			
000008F0	3F800000			1077	DC	X'3F800000'	-1
000008F4	40800000			1078	DC	X'40800000'	+4
				1079 *			
000008F8	3F800000			1080	DC	X'3F800000'	+1
000008FC	40800000			1081	DC	X'40800000'	+4
				1082 *			
00000900	40000000			1083	DC	X'40000000'	+2
00000904	40800000			1084	DC	X'40800000'	+4
				1085 *			
00000908	40400000			1086	DC	X'40400000'	+3
0000090C	40800000			1087	DC	X'40800000'	+4
				1088 *			
00000910	40800000			1089	DC	X'40800000'	+4
00000914	40800000			1090	DC	X'40800000'	+4
				1091 *			
00000918	40A00000			1092	DC	X'40A00000'	+5
0000091C	40800000			1093	DC	X'40800000'	+4
				1094 *			
00000920	40C00000			1095	DC	X'40C00000'	+6
00000924	40800000			1096	DC	X'40800000'	+4
				1097 *			
00000928	40E00000			1098	DC	X'40E00000'	+7
0000092C	40800000			1099	DC	X'40800000'	+4
				1100 *			
00000930	41000000			1101	DC	X'41000000'	+8
00000934	40800000			1102	DC	X'40800000'	+4
				1103 *			
				1104 * Finite value boundary condition tests			
				1105 * Tests 17-22			
				1106 *			
00000938	42200000			1107	DC	X'42200000'	+40.0
0000093C	C1100000			1108	DC	X'C1100000'	-9.0
				1109 *			
				1110 * Following forces quotient overflow, remainder zero.			
				1111 * Final result, scaled quotient, cc1			
00000940	7F7FFFFFFF			1112	DC	X'7F7FFFFFFF'	+maxvalue
00000944	00000001			1113	DC	X'00000001'	+minvalue (tiny)
				1114 *			
00000948	00FFFFFFF			1115	DC	X'00FFFFFFF'	near +minvalue normal
0000094C	00FFFFFFE			1116	DC	X'00FFFFFFE'	almost above
				1117 *			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1118 * Following forces partial results without quotient overflow
				1119 * Partial result, scaled quotient, normal remainder, cc2
00000950	4C800000			1120 DC X'4C800000' +2^26th
00000954	40400000			1121 DC X'40400000' +3.0
				1122 * Expected results from above case: remainder < 3, quotient mismatch
				1123 * z12 actual results: remainder 4, quotient match.
				1124 *
				1125 * Following forces zero quotient, remainder = divisor.
				1126 *
00000958	40100000			1127 DC X'40100000' +2.25
0000095C	41200000			1128 DC X'41200000' +10
				1129 *
				1130 * Following five tests force quotient overflow. Four have non-zero
				1131 * Remainder. All five return partial results.
				1132 *
				1133 * Note: +minvalue+11 is the smallest divisor that generates a
				1134 * remainder.
				1135 *
00000960	7F7FFFFFF			1136 DC X'7F7FFFFFF' +maxvalue
00000964	0000000B			1137 DC X'0000000B' +minvalue + 11 (tiny)
				1138 *
00000968	7F7FFFFE			1139 DC X'7F7FFFFE' +maxvalue
0000096C	0000000A			1140 DC X'0000000A' +minvalue + 11 (tiny)
				1141 *
00000970	7F7FFFFFF			1142 DC X'7F7FFFFFF' +maxvalue
00000974	0000000C			1143 DC X'0000000C' +minvalue + 11 (tiny)
				1144 *
00000978	7F7FFFFFF			1145 DC X'7F7FFFFFF' +maxvalue
0000097C	00000013			1146 DC X'00000013' +minvalue + 11 (tiny)
				1147 *
00000980	7F7FFFFFF			1148 DC X'7F7FFFFFF' +maxvalue
00000984	3F000000			1149 DC X'3F000000' +0.5
				1150 *
00000988	40400000			1151 DC X'40400000' +3
0000098C	40000000			1152 DC X'40000000' +2
				1153 *
		0000002B	00000001	1154 SBFPCT EQU (*-SBFPIN)/4/2 Count of short BFP in list
				1156 *****
				1157 *
				1158 * Third input test data set. These are finite pairs intended to
				1159 * test all combinations of rounding mode for the quotient and the
				1160 * remainder.
				1161 *
				1162 * The quotient/remainder pairs are for Round to Nearest, Ties to Even.
				1163 * Other rounding modes have different results.
				1164 *
				1165 *****
00000990				1167 SBFPINRM DS 0F Inputs for short BFP rounding testing
00000990	C1980000			1168 DC X'C1980000' -19 / 0.5 = -9.5, -9 rem +1
00000994	40000000			1169 DC X'40000000' ...+2.0
00000998	C1300000			1170 DC X'C1300000' -11 / 0.5 = -5.5, -5 rem +1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000099C	40000000			1171	DC	X'40000000'	...+2.0
000009A0	C0A00000			1172	DC	X'C0A00000'	-5 / 0.5 = -2.5
000009A4	40000000			1173	DC	X'40000000'	...+2.0
000009A8	C0400000			1174	DC	X'C0400000'	-3 / 0.5 = -1.5
000009AC	40000000			1175	DC	X'40000000'	...+2.0
000009B0	BF800000			1176	DC	X'BF800000'	-1 / 0.5 = -0.5
000009B4	40000000			1177	DC	X'40000000'	...+2.0
000009B8	3F800000			1178	DC	X'3F800000'	+1 / 0.5 = +0.5
000009BC	40000000			1179	DC	X'40000000'	...+2.0
000009C0	40400000			1180	DC	X'40400000'	+3 / 0.5 = +1.5
000009C4	40000000			1181	DC	X'40000000'	...+2.0
000009C8	40A00000			1182	DC	X'40A00000'	+5 / 0.5 = +2.5
000009CC	40000000			1183	DC	X'40000000'	...+2.0
000009D0	41300000			1184	DC	X'41300000'	+11 / 0.5 = +5.5
000009D4	40000000			1185	DC	X'40000000'	...+2.0
000009D8	41980000			1186	DC	X'41980000'	+19 / 0.5 = +9.5
000009DC	40000000			1187	DC	X'40000000'	...+2.0
000009E0	40000000			1188	DC	X'40000000'	2 / 2 = 1
000009E4	40000000			1189	DC	X'40000000'	...+2.0
000009E8	40400000			1190	DC	X'40400000'	+3 / 5 = +0.6, 0 rem 3
000009EC	40A00000			1191	DC	X'40A00000'	...+5.0
		0000000C	00000001	1192	SBFPRMCT EQU	(*-SBFPINRM)/4/2	Count of short BFP rounding tests

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1194 *****
				1195 *
				1196 * Long integer test data sets for Divide to Integer testing.
				1197 *
				1198 * Each test data set member consists of two values, the dividend and
				1199 * the divisor, in that order.
				1200 *
				1201 * The first test data set is used for tests of basic functionality,
				1202 * NaN propagation, and results from operations involving other than
				1203 * finite numbers.
				1204 *
				1205 * The secondd test data set is used for testing boundary conditions
				1206 * using two finite non-zero values. Each possible condition code
				1207 * and type of result (normal, scaled, etc) is created by members of
				1208 * this test data set.
				1209 *
				1210 * The third test data set is used for exhaustive testing of final
				1211 * results across the panoply of rounding mode combinations available
				1212 * for Divide to Integer (five for the remainder, seven for the
				1213 * quotient).
				1214 *
				1215 *****
				1216 *
000009F0				1217 LBFPNFIN DS 0F Inputs for long BFP testing
				1218 *
				1219 * NaN propagation tests
				1220 *
000009F0	7FF0A000	00000000		1221 DC X'7FF0A00000000000' SNaN
000009F8	7FF0B000	00000000		1222 DC X'7FF0B00000000000' SNaN
				1223 *
00000A00	7FF8A000	00000000		1224 DC X'7FF8A00000000000' QNaN
00000A08	7FF8B000	00000000		1225 DC X'7FF8B00000000000' QNaN
				1226 *
00000A10	40000000	00000000		1227 DC X'4000000000000000' Finite number
00000A18	7FF8B000	00000000		1228 DC X'7FF8B00000000000' QNaN
				1229 *
00000A20	7FF8A000	00000000		1230 DC X'7FF8A00000000000' QNaN
00000A28	7FF0B000	00000000		1231 DC X'7FF0B00000000000' SNaN
				1232 *
				1233 * Dividend is -inf
				1234 *
00000A30	FFF00000	00000000		1235 DC X'FFF0000000000000' -inf
00000A38	FFF00000	00000000		1236 DC X'FFF0000000000000' -inf
				1237 *
00000A40	FFF00000	00000000		1238 DC X'FFF0000000000000' -inf
00000A48	C0000000	00000000		1239 DC X'C000000000000000' -2.0
				1240 *
00000A50	FFF00000	00000000		1241 DC X'FFF0000000000000' -inf
00000A58	80000000	00000000		1242 DC X'8000000000000000' -0
				1243 *
00000A60	FFF00000	00000000		1244 DC X'FFF0000000000000' -inf
00000A68	00000000	00000000		1245 DC X'0000000000000000' +0
				1246 *
00000A70	FFF00000	00000000		1247 DC X'FFF0000000000000' -inf
00000A78	40000000	00000000		1248 DC X'4000000000000000' +2.0
				1249 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00000A80	FFF00000	00000000		1250	DC	X'FFF0000000000000'	-inf
00000A88	7FF00000	00000000		1251	DC	X'7FF0000000000000'	+inf
				1252 *			
				1253 * Dividend is +inf			
				1254 *			
00000A90	7FF00000	00000000		1255	DC	X'7FF0000000000000'	+inf
00000A98	FFF00000	00000000		1256	DC	X'FFF0000000000000'	-inf
				1257 *			
00000AA0	7FF00000	00000000		1258	DC	X'7FF0000000000000'	+inf
00000AA8	C0000000	00000000		1259	DC	X'C000000000000000'	-2.0
				1260 *			
00000AB0	7FF00000	00000000		1261	DC	X'7FF0000000000000'	+inf
00000AB8	80000000	00000000		1262	DC	X'8000000000000000'	-0
				1263 *			
00000AC0	7FF00000	00000000		1264	DC	X'7FF0000000000000'	+inf
00000AC8	00000000	00000000		1265	DC	X'0000000000000000'	+0
				1266 *			
00000AD0	7FF00000	00000000		1267	DC	X'7FF0000000000000'	+inf
00000AD8	40000000	00000000		1268	DC	X'4000000000000000'	+2.0
				1269 *			
00000AE0	7FF00000	00000000		1270	DC	X'7FF0000000000000'	+inf
00000AE8	7FF00000	00000000		1271	DC	X'7FF0000000000000'	+inf
				1272 *			
				1273 * Divisor is -0. (+/-inf dividend tested above)			
				1274 *			
00000AF0	C0000000	00000000		1275	DC	X'C000000000000000'	-2.0
00000AF8	80000000	00000000		1276	DC	X'8000000000000000'	-0
				1277 *			
00000B00	80000000	00000000		1278	DC	X'8000000000000000'	-0
00000B08	80000000	00000000		1279	DC	X'8000000000000000'	-0
				1280 *			
00000B10	00000000	00000000		1281	DC	X'0000000000000000'	+0
00000B18	80000000	00000000		1282	DC	X'8000000000000000'	-0
				1283 *			
00000B20	40000000	00000000		1284	DC	X'4000000000000000'	+2.0
00000B28	80000000	00000000		1285	DC	X'8000000000000000'	-0
				1286 *			
				1287 * Divisor is +0. (+/-inf dividend tested above)			
				1288 *			
00000B30	C0000000	00000000		1289	DC	X'C000000000000000'	-2.0
00000B38	00000000	00000000		1290	DC	X'0000000000000000'	+0
				1291 *			
00000B40	80000000	00000000		1292	DC	X'8000000000000000'	-0
00000B48	00000000	00000000		1293	DC	X'0000000000000000'	+0
				1294 *			
00000B50	00000000	00000000		1295	DC	X'0000000000000000'	+0
00000B58	00000000	00000000		1296	DC	X'0000000000000000'	+0
				1297 *			
00000B60	40000000	00000000		1298	DC	X'4000000000000000'	+2.0
00000B68	00000000	00000000		1299	DC	X'0000000000000000'	+0
				1300 *			
				1301 * Divisor is -inf. (+/-inf dividend tested above)			
				1302 *			
00000B70	C0000000	00000000		1303	DC	X'C000000000000000'	-2.0
00000B78	FFF00000	00000000		1304	DC	X'FFF0000000000000'	-inf
				1305 *			

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT				
00000B80	80000000	00000000			1306	DC	X'8000000000000000'	-0	
00000B88	FFF00000	00000000			1307	DC	X'FFF0000000000000'	-inf	
					1308	*			
00000B90	00000000	00000000			1309	DC	X'0000000000000000'	+0	
00000B98	FFF00000	00000000			1310	DC	X'FFF0000000000000'	-inf	
					1311	*			
00000BA0	40000000	00000000			1312	DC	X'4000000000000000'	+2.0	
00000BA8	FFF00000	00000000			1313	DC	X'FFF0000000000000'	-inf	
					1314	*			
					1315	* Divisor is +inf. (+/-inf dividend tested above)			
					1316	*			
00000BB0	C0000000	00000000			1317	DC	X'C000000000000000'	-2.0	
00000BB8	7FF00000	00000000			1318	DC	X'7FF0000000000000'	+inf	
					1319	*			
00000BC0	80000000	00000000			1320	DC	X'8000000000000000'	-0	
00000BC8	7FF00000	00000000			1321	DC	X'7FF0000000000000'	+inf	
					1322	*			
00000BD0	00000000	00000000			1323	DC	X'0000000000000000'	+0	
00000BD8	7FF00000	00000000			1324	DC	X'7FF0000000000000'	+inf	
					1325	*			
00000BE0	40000000	00000000			1326	DC	X'4000000000000000'	+2.0	
00000BE8	7FF00000	00000000			1327	DC	X'7FF0000000000000'	+inf	
			00000020	00000001	1328	LBFPNFCT EQU	(*-LBFPNFIN)/8/2 Count of long BFP in list		
					1330	*****			
					1331	*			
					1332	* Second set of test inputs. These are finite pairs intended to			
					1333	* test all combinations of finite values and results (final			
					1334	* results due to remainder zero, final results due to quotient			
					1335	* within range, and partial results.			
					1336	*			
					1337	*****			
00000BF0					1339	LBFPIN DS 0F	Inputs for long BFP finite tests		
					1340	*			
					1341	* Dividend and Divisor are both finite numbers.			
					1342	*			
					1343	* Remainder tests from SA22-7832-10, Figure 19-7 on page 19-6			
					1344	* (Finite tests 1-32)			
					1345	*			
00000BF0	C0200000	00000000			1346	DC	X'C020000000000000'	-8	
00000BF8	C0100000	00000000			1347	DC	X'C010000000000000'	-4	
					1348	*			
00000C00	C01C0000	00000000			1349	DC	X'C01C000000000000'	-7	
00000C08	C0100000	00000000			1350	DC	X'C010000000000000'	-4	
					1351	*			
00000C10	C0180000	00000000			1352	DC	X'C018000000000000'	-6	
00000C18	C0100000	00000000			1353	DC	X'C010000000000000'	-4	
					1354	*			
00000C20	C0140000	00000000			1355	DC	X'C014000000000000'	-5	
00000C28	C0100000	00000000			1356	DC	X'C010000000000000'	-4	
					1357	*			
00000C30	C0100000	00000000			1358	DC	X'C010000000000000'	-4	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00000C38	C0100000	00000000		1359	DC	X'C010000000000000'	-4
				1360 *			
00000C40	C0080000	00000000		1361	DC	X'C008000000000000'	-3
00000C48	C0100000	00000000		1362	DC	X'C010000000000000'	-4
				1363 *			
00000C50	C0000000	00000000		1364	DC	X'C000000000000000'	-2
00000C58	C0100000	00000000		1365	DC	X'C010000000000000'	-4
				1366 *			
00000C60	BFF00000	00000000		1367	DC	X'BFF0000000000000'	-1
00000C68	C0100000	00000000		1368	DC	X'C010000000000000'	-4
				1369 *			
				1370 *	The +/- zero - +/- zero cases are handled above and skipped here		
				1371 *			
00000C70	3FF00000	00000000		1372	DC	X'3FF0000000000000'	+1
00000C78	C0100000	00000000		1373	DC	X'C010000000000000'	-4
				1374 *			
00000C80	40000000	00000000		1375	DC	X'4000000000000000'	+2
00000C88	C0100000	00000000		1376	DC	X'C010000000000000'	-4
				1377 *			
00000C90	40080000	00000000		1378	DC	X'4008000000000000'	+3
00000C98	C0100000	00000000		1379	DC	X'C010000000000000'	-4
				1380 *			
00000CA0	40100000	00000000		1381	DC	X'4010000000000000'	+4
00000CA8	C0100000	00000000		1382	DC	X'C010000000000000'	-4
				1383 *			
00000CB0	40140000	00000000		1384	DC	X'4014000000000000'	+5
00000CB8	C0100000	00000000		1385	DC	X'C010000000000000'	-4
				1386 *			
00000CC0	40180000	00000000		1387	DC	X'4018000000000000'	+6
00000CC8	C0100000	00000000		1388	DC	X'C010000000000000'	-4
				1389 *			
00000CD0	401C0000	00000000		1390	DC	X'401C000000000000'	+7
00000CD8	C0100000	00000000		1391	DC	X'C010000000000000'	-4
				1392 *			
00000CE0	40200000	00000000		1393	DC	X'4020000000000000'	+8
00000CE8	C0100000	00000000		1394	DC	X'C010000000000000'	-4
				1395 *			
00000CF0	C0200000	00000000		1396	DC	X'C020000000000000'	-8
00000CF8	40100000	00000000		1397	DC	X'4010000000000000'	+4
				1398 *			
00000D00	C01C0000	00000000		1399	DC	X'C01C000000000000'	-7
00000D08	40100000	00000000		1400	DC	X'4010000000000000'	+4
				1401 *			
00000D10	C0180000	00000000		1402	DC	X'C018000000000000'	-6
00000D18	40100000	00000000		1403	DC	X'4010000000000000'	+4
				1404 *			
00000D20	C0140000	00000000		1405	DC	X'C014000000000000'	-5
00000D28	40100000	00000000		1406	DC	X'4010000000000000'	+4
				1407 *			
00000D30	C0100000	00000000		1408	DC	X'C010000000000000'	-4
00000D38	40100000	00000000		1409	DC	X'4010000000000000'	+4
				1410 *			
00000D40	C0080000	00000000		1411	DC	X'C008000000000000'	-3
00000D48	40100000	00000000		1412	DC	X'4010000000000000'	+4
				1413 *			
00000D50	C0000000	00000000		1414	DC	X'C000000000000000'	-2

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00000D58	40100000	00000000		1415	DC	X'4010000000000000'	+4
				1416 *			
00000D60	3FF00000	00000000		1417	DC	X'3FF0000000000000'	-1
00000D68	40100000	00000000		1418	DC	X'4010000000000000'	+4
				1419 *			
00000D70	3FF00000	00000000		1420	DC	X'3FF0000000000000'	+1
00000D78	40100000	00000000		1421	DC	X'4010000000000000'	+4
				1422 *			
00000D80	40000000	00000000		1423	DC	X'4000000000000000'	+2
00000D88	40100000	00000000		1424	DC	X'4010000000000000'	+4
				1425 *			
00000D90	40080000	00000000		1426	DC	X'4008000000000000'	+3
00000D98	40100000	00000000		1427	DC	X'4010000000000000'	+4
				1428 *			
00000DA0	40100000	00000000		1429	DC	X'4010000000000000'	+4
00000DA8	40100000	00000000		1430	DC	X'4010000000000000'	+4
				1431 *			
00000DB0	40140000	00000000		1432	DC	X'4014000000000000'	+5
00000DB8	40100000	00000000		1433	DC	X'4010000000000000'	+4
				1434 *			
00000DC0	40180000	00000000		1435	DC	X'4018000000000000'	+6
00000DC8	40100000	00000000		1436	DC	X'4010000000000000'	+4
				1437 *			
00000DD0	401C0000	00000000		1438	DC	X'401C000000000000'	+7
00000DD8	40100000	00000000		1439	DC	X'4010000000000000'	+4
				1440 *			
00000DE0	40200000	00000000		1441	DC	X'4020000000000000'	+8
00000DE8	40100000	00000000		1442	DC	X'4010000000000000'	+4
				1443 **			
				1444 *	Dividend and Divisor are both finite numbers.		
				1445 *	(Tests 33-38)		
				1446 *			
00000DF0	40440000	00000000		1447	DC	X'4044000000000000'	+40.0
00000DF8	C0220000	00000000		1448	DC	X'C022000000000000'	-9.0
				1449 *			
				1450 *	Following forces quotient overflow, remainder zero.		
				1451 *	Final result, scaled quotient, cc1		
00000E00	7FEFFFFFFF	FFFFFFFF		1452	DC	X'7FEFFFFFFF'	+maxvalue
00000E08	00000000	00000001		1453	DC	X'0000000000000001'	+minvalue (tiny)
				1454 *			
				1455 *	Following forces quotient overflow, remainder non-zero.		
				1456 *	Partial result, scaled quotient, tiny remainder, cc3		
				1457 *	Note: +minvalue+2 is the smallest divisor that		
				1458 *	generates a non-zero remainder.		
00000E10	7FEFFFFFFF	FFFFFFFF		1459	DC	X'7FEFFFFFFF'	+maxvalue
00000E18	00000000	00000003		1460	DC	X'0000000000000003'	+minvalue (tiny)
				1461 *			
00000E20	000FFFFFFF	FFFFFFFF		1462	DC	X'000FFFFFFF'	near +minvalue normal
00000E28	000FFFFFFF	FFFFFFFE		1463	DC	X'000FFFFFFF'	almost above
				1464 *			
				1465 *	Following forces partial results without quotient overflow		
				1466 *	Partial result, scaled quotient, normal remainder, cc2		
00000E30	43700000	00000000		1467	DC	X'4370000000000000'	+2^56th
00000E38	40080000	00000000		1468	DC	X'4008000000000000'	+3.0
				1469 *	Expected results from above case: remainder < 3, quotient mismatch		
				1470 *	z12 actual results: remainder 4, quotient match.		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1471 *
				1472 * Following forces zero quotient, remainder = divisor.
				1473 *
00000E40	40020000	00000000		1474 DC X'4002000000000000' +2.25
00000E48	40240000	00000000		1475 DC X'4024000000000000' +10
				1476 *
		00000026	00000001	1477 LBFPCT EQU (*-LBFPIN)/8/2 Count of long BFP in list
				1479 *****
				1480 *
				1481 * Third input test data set. These are finite pairs intended to
				1482 * test all combinations of rounding mode for the quotient and the
				1483 * remainder.
				1484 *
				1485 * The quotient/remainder pairs are for Round to Nearest, Ties to Even.
				1486 * Other rounding modes have different results.
				1487 *
				1488 *****
00000E50				1490 LBFPINRM DS 0F
00000E50	C0230000	00000000		1491 DC X'C023000000000000' -9.5, -9 rem 1
00000E58	40000000	00000000		1492 DC X'4000000000000000' +2
				1493 *
00000E60	C0160000	00000000		1494 DC X'C016000000000000' -5.5
00000E68	40000000	00000000		1495 DC X'4000000000000000' +2
				1496 *
00000E70	C0040000	00000000		1497 DC X'C004000000000000' -2.5
00000E78	40000000	00000000		1498 DC X'4000000000000000' +2
				1499 *
00000E80	BFF80000	00000000		1500 DC X'BFF8000000000000' -1.5
00000E88	40000000	00000000		1501 DC X'4000000000000000' +2
				1502 *
00000E90	BFE00000	00000000		1503 DC X'BFE0000000000000' -0.5
00000E98	40000000	00000000		1504 DC X'4000000000000000' +2
				1505 *
00000EA0	3FE00000	00000000		1506 DC X'3FE0000000000000' +0.5
00000EA8	40000000	00000000		1507 DC X'4000000000000000' +2
				1508 *
00000EB0	3FF80000	00000000		1509 DC X'3FF8000000000000' +1.5
00000EB8	40000000	00000000		1510 DC X'4000000000000000' +2
				1511 *
00000EC0	40040000	00000000		1512 DC X'4004000000000000' +2.5
00000EC8	40000000	00000000		1513 DC X'4000000000000000' +2
				1514 *
00000ED0	40160000	00000000		1515 DC X'4016000000000000' +5.5
00000ED8	40000000	00000000		1516 DC X'4000000000000000' +2
				1517 *
00000EE0	40230000	00000000		1518 DC X'4023000000000000' +9.5
00000EE8	40000000	00000000		1519 DC X'4000000000000000' +2
				1520 *
00000EF0	40000000	00000000		1521 DC X'4000000000000000' +2
00000EF8	40000000	00000000		1522 DC X'4000000000000000' +2
				1523 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1566 *****
				1567 * EXPECTED results
				1568 *****
				1569 *
00000F10		00000F10	0000C000	1570 ORG STRTLABL+X'C000' (past end of actual results)
				1571 *
		0000C000	00000001	1572 SBFPNFOT_GOOD EQU *
0000C000	C4C9C5C2	D940A385		1573 DC CL48'DIEBR test 1 NaN'
0000C030	7FCA0000	7FCA0000		1574 DC XL16'7FCA00007FCA00007F8A000000000000'
0000C040	C4C9C5C2	D940A385		1575 DC CL48'DIEBR test 2 NaN'
0000C070	7FCA0000	7FCA0000		1576 DC XL16'7FCA00007FCA00007FCA00007FCA0000'
0000C080	C4C9C5C2	D940A385		1577 DC CL48'DIEBR test 3 NaN'
0000C0B0	7FCB0000	7FCB0000		1578 DC XL16'7FCB00007FCB00007FCB00007FCB0000'
0000C0C0	C4C9C5C2	D940A385		1579 DC CL48'DIEBR test 4 NaN'
0000C0F0	7FCB0000	7FCB0000		1580 DC XL16'7FCB00007FCB00007FCA000000000000'
0000C100	C4C9C5C2	D940A385		1581 DC CL48'DIEBR test 5 -inf dividend'
0000C130	7FC00000	7FC00000		1582 DC XL16'7FC000007FC00000FF80000000000000'
0000C140	C4C9C5C2	D940A385		1583 DC CL48'DIEBR test 6 -inf dividend'
0000C170	7FC00000	7FC00000		1584 DC XL16'7FC000007FC00000FF80000000000000'
0000C180	C4C9C5C2	D940A385		1585 DC CL48'DIEBR test 7 -inf dividend'
0000C1B0	7FC00000	7FC00000		1586 DC XL16'7FC000007FC00000FF80000000000000'
0000C1C0	C4C9C5C2	D940A385		1587 DC CL48'DIEBR test 8 -inf dividend'
0000C1F0	7FC00000	7FC00000		1588 DC XL16'7FC000007FC00000FF80000000000000'
0000C200	C4C9C5C2	D940A385		1589 DC CL48'DIEBR test 9 -inf dividend'
0000C230	7FC00000	7FC00000		1590 DC XL16'7FC000007FC00000FF80000000000000'
0000C240	C4C9C5C2	D940A385		1591 DC CL48'DIEBR test 10 -inf dividend'
0000C270	7FC00000	7FC00000		1592 DC XL16'7FC000007FC00000FF80000000000000'
0000C280	C4C9C5C2	D940A385		1593 DC CL48'DIEBR test 11 +inf dividend'
0000C2B0	7FC00000	7FC00000		1594 DC XL16'7FC000007FC000007F8000000000000'
0000C2C0	C4C9C5C2	D940A385		1595 DC CL48'DIEBR test 12 +inf dividend'
0000C2F0	7FC00000	7FC00000		1596 DC XL16'7FC000007FC000007F8000000000000'
0000C300	C4C9C5C2	D940A385		1597 DC CL48'DIEBR test 13 +inf dividend'
0000C330	7FC00000	7FC00000		1598 DC XL16'7FC000007FC000007F8000000000000'
0000C340	C4C9C5C2	D940A385		1599 DC CL48'DIEBR test 14 +inf dividend'
0000C370	7FC00000	7FC00000		1600 DC XL16'7FC000007FC000007F8000000000000'
0000C380	C4C9C5C2	D940A385		1601 DC CL48'DIEBR test 15 +inf dividend'
0000C3B0	7FC00000	7FC00000		1602 DC XL16'7FC000007FC000007F8000000000000'
0000C3C0	C4C9C5C2	D940A385		1603 DC CL48'DIEBR test 16 +inf dividend'
0000C3F0	7FC00000	7FC00000		1604 DC XL16'7FC000007FC000007F8000000000000'
0000C400	C4C9C5C2	D940A385		1605 DC CL48'DIEBR test 17 -0 divisor'
0000C430	7FC00000	7FC00000		1606 DC XL16'7FC000007FC00000C00000000000000'
0000C440	C4C9C5C2	D940A385		1607 DC CL48'DIEBR test 18 -0 divisor'
0000C470	7FC00000	7FC00000		1608 DC XL16'7FC000007FC00000800000000000000'
0000C480	C4C9C5C2	D940A385		1609 DC CL48'DIEBR test 19 -0 divisor'
0000C4B0	7FC00000	7FC00000		1610 DC XL16'7FC000007FC00000000000000000000'
0000C4C0	C4C9C5C2	D940A385		1611 DC CL48'DIEBR test 20 -0 divisor'
0000C4F0	7FC00000	7FC00000		1612 DC XL16'7FC000007FC00000400000000000000'
0000C500	C4C9C5C2	D940A385		1613 DC CL48'DIEBR test 21 +0 divisor'
0000C530	7FC00000	7FC00000		1614 DC XL16'7FC000007FC00000C00000000000000'
0000C540	C4C9C5C2	D940A385		1615 DC CL48'DIEBR test 22 +0 divisor'
0000C570	7FC00000	7FC00000		1616 DC XL16'7FC000007FC00000800000000000000'
0000C580	C4C9C5C2	D940A385		1617 DC CL48'DIEBR test 23 +0 divisor'
0000C5B0	7FC00000	7FC00000		1618 DC XL16'7FC000007FC00000000000000000000'
0000C5C0	C4C9C5C2	D940A385		1619 DC CL48'DIEBR test 24 +0 divisor'
0000C5F0	7FC00000	7FC00000		1620 DC XL16'7FC000007FC00000400000000000000'
0000C600	C4C9C5C2	D940A385		1621 DC CL48'DIEBR test 25 -inf divisor'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000C630	C0000000 00000000			1622 DC XL16'C000000000000000C000000000000000'
0000C640	C4C9C5C2 D940A385			1623 DC CL48'DIEBR test 26 -inf divisor'
0000C670	80000000 00000000			1624 DC XL16'80000000000000008000000000000000'
0000C680	C4C9C5C2 D940A385			1625 DC CL48'DIEBR test 27 -inf divisor'
0000C6B0	00000000 80000000			1626 DC XL16'00000000800000000000000080000000'
0000C6C0	C4C9C5C2 D940A385			1627 DC CL48'DIEBR test 28 -inf divisor'
0000C6F0	40000000 80000000			1628 DC XL16'40000000800000004000000080000000'
0000C700	C4C9C5C2 D940A385			1629 DC CL48'DIEBR test 29 +inf divisor'
0000C730	C0000000 80000000			1630 DC XL16'C000000080000000C000000080000000'
0000C740	C4C9C5C2 D940A385			1631 DC CL48'DIEBR test 30 +inf divisor'
0000C770	80000000 80000000			1632 DC XL16'80000000800000008000000080000000'
0000C780	C4C9C5C2 D940A385			1633 DC CL48'DIEBR test 31 +inf divisor'
0000C7B0	00000000 00000000			1634 DC XL16'00000000000000000000000000000000'
0000C7C0	C4C9C5C2 D940A385			1635 DC CL48'DIEBR test 32 +inf divisor'
0000C7F0	40000000 00000000			1636 DC XL16'40000000000000004000000000000000'
		00000020	00000001	1637 SBFPNFOT_NUM EQU (*-SBFPNFOT_GOOD)/64
				1638 *
				1639 *
		0000C800	00000001	1640 SBFPNFFL_GOOD EQU *
0000C800	C4C9C5C2 D940C6D7			1641 DC CL48'DIEBR FPCR pair NaN 1-2'
0000C830	00800001 F8008001			1642 DC XL16'00800001F800800100000001F8000001'
0000C840	C4C9C5C2 D940C6D7			1643 DC CL48'DIEBR FPCR pair NaN 4-4'
0000C870	00000001 F8000001			1644 DC XL16'00000001F800000100800001F8008001'
0000C880	C4C9C5C2 D940C6D7			1645 DC CL48'DIEBR FPCR pair -inf 5-6'
0000C8B0	00800001 F8008001			1646 DC XL16'00800001F800800100800001F8008001'
0000C8C0	C4C9C5C2 D940C6D7			1647 DC CL48'DIEBR FPCR pair -inf 7-8'
0000C8F0	00800001 F8008001			1648 DC XL16'00800001F800800100800001F8008001'
0000C900	C4C9C5C2 D940C6D7			1649 DC CL48'DIEBR FPCR pair -inf 9-10'
0000C930	00800001 F8008001			1650 DC XL16'00800001F800800100800001F8008001'
0000C940	C4C9C5C2 D940C6D7			1651 DC CL48'DIEBR FPCR pair +inf 11-12'
0000C970	00800001 F8008001			1652 DC XL16'00800001F800800100800001F8008001'
0000C980	C4C9C5C2 D940C6D7			1653 DC CL48'DIEBR FPCR pair +inf 13-14'
0000C9B0	00800001 F8008001			1654 DC XL16'00800001F800800100800001F8008001'
0000C9C0	C4C9C5C2 D940C6D7			1655 DC CL48'DIEBR FPCR pair +inf 15-16'
0000C9F0	00800001 F8008001			1656 DC XL16'00800001F800800100800001F8008001'
0000CA00	C4C9C5C2 D940C6D7			1657 DC CL48'DIEBR FPCR pair -0 17-18'
0000CA30	00800001 F8008001			1658 DC XL16'00800001F800800100800001F8008001'
0000CA40	C4C9C5C2 D940C6D7			1659 DC CL48'DIEBR FPCR pair -0 19-20'
0000CA70	00800001 F8008001			1660 DC XL16'00800001F800800100800001F8008001'
0000CA80	C4C9C5C2 D940C6D7			1661 DC CL48'DIEBR FPCR pair +0 21-22'
0000CAB0	00800001 F8008001			1662 DC XL16'00800001F800800100800001F8008001'
0000CAC0	C4C9C5C2 D940C6D7			1663 DC CL48'DIEBR FPCR pair +0 23-24'
0000CAF0	00800001 F8008001			1664 DC XL16'00800001F800800100800001F8008001'
0000CB00	C4C9C5C2 D940C6D7			1665 DC CL48'DIEBR FPCR pair div -inf 25-26'
0000CB30	00000000 F8000000			1666 DC XL16'00000000F800000000000000F8000000'
0000CB40	C4C9C5C2 D940C6D7			1667 DC CL48'DIEBR FPCR pair div -inf 27-28'
0000CB70	00000000 F8000000			1668 DC XL16'00000000F800000000000000F8000000'
0000CB80	C4C9C5C2 D940C6D7			1669 DC CL48'DIEBR FPCR pair div +inf 29-30'
0000CBB0	00000000 F8000000			1670 DC XL16'00000000F800000000000000F8000000'
0000CBC0	C4C9C5C2 D940C6D7			1671 DC CL48'DIEBR FPCR pair div +inf 31-32'
0000CBF0	00000000 F8000000			1672 DC XL16'00000000F800000000000000F8000000'
		00000010	00000001	1673 SBFPNFFL_NUM EQU (*-SBFPNFFL_GOOD)/64
				1674 *
				1675 *
		0000CC00	00000001	1676 LBFPNFOT_GOOD EQU *
0000CC00	C4C9C4C2 D940A385			1677 DC CL48'DIDBR test 1a NaN'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000CC30	7FF8A000 00000000			1678 DC XL16'7FF8A00000000000007FF8A00000000000'
0000CC40	C4C9C4C2 D940A385			1679 DC CL48'DIDBR test 1b NaN'
0000CC70	7FF0A000 00000000			1680 DC XL16'7FF0A000000000000000000000000000'
0000CC80	C4C9C4C2 D940A385			1681 DC CL48'DIDBR test 2a NaN'
0000CCB0	7FF8A000 00000000			1682 DC XL16'7FF8A00000000000007FF8A00000000000'
0000CCC0	C4C9C4C2 D940A385			1683 DC CL48'DIDBR test 2b NaN'
0000CCF0	7FF8A000 00000000			1684 DC XL16'7FF8A00000000000007FF8A00000000000'
0000CD00	C4C9C4C2 D940A385			1685 DC CL48'DIDBR test 3a NaN'
0000CD30	7FF8B000 00000000			1686 DC XL16'7FF8B00000000000007FF8B00000000000'
0000CD40	C4C9C4C2 D940A385			1687 DC CL48'DIDBR test 3b NaN'
0000CD70	7FF8B000 00000000			1688 DC XL16'7FF8B00000000000007FF8B00000000000'
0000CD80	C4C9C4C2 D940A385			1689 DC CL48'DIDBR test 4a NaN'
0000CDB0	7FF8B000 00000000			1690 DC XL16'7FF8B00000000000007FF8B00000000000'
0000CDC0	C4C9C4C2 D940A385			1691 DC CL48'DIDBR test 4b NaN'
0000CDF0	7FF8A000 00000000			1692 DC XL16'7FF8A000000000000000000000000000'
0000CE00	C4C9C4C2 D940A385			1693 DC CL48'DIDBR test 5a -inf dividend'
0000CE30	7FF80000 00000000			1694 DC XL16'7FF8000000000000007FF8000000000000'
0000CE40	C4C9C4C2 D940A385			1695 DC CL48'DIDBR test 5b -inf dividend'
0000CE70	FFF00000 00000000			1696 DC XL16'FFF00000000000000000000000000000'
0000CE80	C4C9C4C2 D940A385			1697 DC CL48'DIDBR test 6a -inf dividend'
0000CEB0	7FF80000 00000000			1698 DC XL16'7FF8000000000000007FF8000000000000'
0000CEC0	C4C9C4C2 D940A385			1699 DC CL48'DIDBR test 6b -inf dividend'
0000CEF0	FFF00000 00000000			1700 DC XL16'FFF00000000000000000000000000000'
0000CF00	C4C9C4C2 D940A385			1701 DC CL48'DIDBR test 7a -inf dividend'
0000CF30	7FF80000 00000000			1702 DC XL16'7FF8000000000000007FF8000000000000'
0000CF40	C4C9C4C2 D940A385			1703 DC CL48'DIDBR test 7b -inf dividend'
0000CF70	FFF00000 00000000			1704 DC XL16'FFF00000000000000000000000000000'
0000CF80	C4C9C4C2 D940A385			1705 DC CL48'DIDBR test 8a -inf dividend'
0000CFB0	7FF80000 00000000			1706 DC XL16'7FF8000000000000007FF8000000000000'
0000CFC0	C4C9C4C2 D940A385			1707 DC CL48'DIDBR test 8b -inf dividend'
0000CFF0	FFF00000 00000000			1708 DC XL16'FFF00000000000000000000000000000'
0000D000	C4C9C4C2 D940A385			1709 DC CL48'DIDBR test 9a -inf dividend'
0000D030	7FF80000 00000000			1710 DC XL16'7FF8000000000000007FF8000000000000'
0000D040	C4C9C4C2 D940A385			1711 DC CL48'DIDBR test 9b -inf dividend'
0000D070	FFF00000 00000000			1712 DC XL16'FFF00000000000000000000000000000'
0000D080	C4C9C4C2 D940A385			1713 DC CL48'DIDBR test 10a -inf dividend'
0000D0B0	7FF80000 00000000			1714 DC XL16'7FF8000000000000007FF8000000000000'
0000D0C0	C4C9C4C2 D940A385			1715 DC CL48'DIDBR test 10b -inf dividend'
0000D0F0	FFF00000 00000000			1716 DC XL16'FFF00000000000000000000000000000'
0000D100	C4C9C4C2 D940A385			1717 DC CL48'DIDBR test 11a +inf dividend'
0000D130	7FF80000 00000000			1718 DC XL16'7FF8000000000000007FF8000000000000'
0000D140	C4C9C4C2 D940A385			1719 DC CL48'DIDBR test 11b +inf dividend'
0000D170	7FF00000 00000000			1720 DC XL16'7FF00000000000000000000000000000'
0000D180	C4C9C4C2 D940A385			1721 DC CL48'DIDBR test 12a +inf dividend'
0000D1B0	7FF80000 00000000			1722 DC XL16'7FF8000000000000007FF8000000000000'
0000D1C0	C4C9C4C2 D940A385			1723 DC CL48'DIDBR test 12b +inf dividend'
0000D1F0	7FF00000 00000000			1724 DC XL16'7FF00000000000000000000000000000'
0000D200	C4C9C4C2 D940A385			1725 DC CL48'DIDBR test 13a +inf dividend'
0000D230	7FF80000 00000000			1726 DC XL16'7FF8000000000000007FF8000000000000'
0000D240	C4C9C4C2 D940A385			1727 DC CL48'DIDBR test 13b +inf dividend'
0000D270	7FF00000 00000000			1728 DC XL16'7FF00000000000000000000000000000'
0000D280	C4C9C4C2 D940A385			1729 DC CL48'DIDBR test 14a +inf dividend'
0000D2B0	7FF80000 00000000			1730 DC XL16'7FF8000000000000007FF8000000000000'
0000D2C0	C4C9C4C2 D940A385			1731 DC CL48'DIDBR test 14b +inf dividend'
0000D2F0	7FF00000 00000000			1732 DC XL16'7FF00000000000000000000000000000'
0000D300	C4C9C4C2 D940A385			1733 DC CL48'DIDBR test 15a +inf dividend'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000D330	7FF80000 00000000			1734 DC XL16'7FF80000000000007FF8000000000000'
0000D340	C4C9C4C2 D940A385			1735 DC CL48'DIDBR test 15b +inf dividend'
0000D370	7FF00000 00000000			1736 DC XL16'7FF00000000000000000000000000000'
0000D380	C4C9C4C2 D940A385			1737 DC CL48'DIDBR test 16a +inf dividend'
0000D3B0	7FF80000 00000000			1738 DC XL16'7FF80000000000007FF8000000000000'
0000D3C0	C4C9C4C2 D940A385			1739 DC CL48'DIDBR test 16b +inf dividend'
0000D3F0	7FF00000 00000000			1740 DC XL16'7FF00000000000000000000000000000'
0000D400	C4C9C4C2 D940A385			1741 DC CL48'DIDBR test 17a -0 divisor'
0000D430	7FF80000 00000000			1742 DC XL16'7FF80000000000007FF8000000000000'
0000D440	C4C9C4C2 D940A385			1743 DC CL48'DIDBR test 17b -0 divisor'
0000D470	C0000000 00000000			1744 DC XL16'C0000000000000000000000000000000'
0000D480	C4C9C4C2 D940A385			1745 DC CL48'DIDBR test 18a -0 divisor'
0000D4B0	7FF80000 00000000			1746 DC XL16'7FF80000000000007FF8000000000000'
0000D4C0	C4C9C4C2 D940A385			1747 DC CL48'DIDBR test 18b -0 divisor'
0000D4F0	80000000 00000000			1748 DC XL16'80000000000000000000000000000000'
0000D500	C4C9C4C2 D940A385			1749 DC CL48'DIDBR test 19a -0 divisor'
0000D530	7FF80000 00000000			1750 DC XL16'7FF80000000000007FF8000000000000'
0000D540	C4C9C4C2 D940A385			1751 DC CL48'DIDBR test 19b -0 divisor'
0000D570	00000000 00000000			1752 DC XL16'00000000000000000000000000000000'
0000D580	C4C9C4C2 D940A385			1753 DC CL48'DIDBR test 20a -0 divisor'
0000D5B0	7FF80000 00000000			1754 DC XL16'7FF80000000000007FF8000000000000'
0000D5C0	C4C9C4C2 D940A385			1755 DC CL48'DIDBR test 20b -0 divisor'
0000D5F0	40000000 00000000			1756 DC XL16'40000000000000000000000000000000'
0000D600	C4C9C4C2 D940A385			1757 DC CL48'DIDBR test 21a +0 divisor'
0000D630	7FF80000 00000000			1758 DC XL16'7FF80000000000007FF8000000000000'
0000D640	C4C9C4C2 D940A385			1759 DC CL48'DIDBR test 21b +0 divisor'
0000D670	C0000000 00000000			1760 DC XL16'C0000000000000000000000000000000'
0000D680	C4C9C4C2 D940A385			1761 DC CL48'DIDBR test 22a +0 divisor'
0000D6B0	7FF80000 00000000			1762 DC XL16'7FF80000000000007FF8000000000000'
0000D6C0	C4C9C4C2 D940A385			1763 DC CL48'DIDBR test 22b +0 divisor'
0000D6F0	80000000 00000000			1764 DC XL16'80000000000000000000000000000000'
0000D700	C4C9C4C2 D940A385			1765 DC CL48'DIDBR test 23a +0 divisor'
0000D730	7FF80000 00000000			1766 DC XL16'7FF80000000000007FF8000000000000'
0000D740	C4C9C4C2 D940A385			1767 DC CL48'DIDBR test 23b +0 divisor'
0000D770	00000000 00000000			1768 DC XL16'00000000000000000000000000000000'
0000D780	C4C9C4C2 D940A385			1769 DC CL48'DIDBR test 24a +0 divisor'
0000D7B0	7FF80000 00000000			1770 DC XL16'7FF80000000000007FF8000000000000'
0000D7C0	C4C9C4C2 D940A385			1771 DC CL48'DIDBR test 24b +0 divisor'
0000D7F0	40000000 00000000			1772 DC XL16'40000000000000000000000000000000'
0000D800	C4C9C4C2 D940A385			1773 DC CL48'DIDBR test 25a -inf divisor'
0000D830	C0000000 00000000			1774 DC XL16'C0000000000000000000000000000000'
0000D840	C4C9C4C2 D940A385			1775 DC CL48'DIDBR test 25b -inf divisor'
0000D870	C0000000 00000000			1776 DC XL16'C0000000000000000000000000000000'
0000D880	C4C9C4C2 D940A385			1777 DC CL48'DIDBR test 26a -inf divisor'
0000D8B0	80000000 00000000			1778 DC XL16'80000000000000000000000000000000'
0000D8C0	C4C9C4C2 D940A385			1779 DC CL48'DIDBR test 26b -inf divisor'
0000D8F0	80000000 00000000			1780 DC XL16'80000000000000000000000000000000'
0000D900	C4C9C4C2 D940A385			1781 DC CL48'DIDBR test 27a -inf divisor'
0000D930	00000000 00000000			1782 DC XL16'00000000000000008000000000000000'
0000D940	C4C9C4C2 D940A385			1783 DC CL48'DIDBR test 27b -inf divisor'
0000D970	00000000 00000000			1784 DC XL16'00000000000000008000000000000000'
0000D980	C4C9C4C2 D940A385			1785 DC CL48'DIDBR test 28a -inf divisor'
0000D9B0	40000000 00000000			1786 DC XL16'40000000000000008000000000000000'
0000D9C0	C4C9C4C2 D940A385			1787 DC CL48'DIDBR test 28b -inf divisor'
0000D9F0	40000000 00000000			1788 DC XL16'40000000000000008000000000000000'
0000DA00	C4C9C4C2 D940A385			1789 DC CL48'DIDBR test 29a +inf divisor'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000DA30	C0000000 00000000			1790 DC XL16'C000000000000000800000000000000'
0000DA40	C4C9C4C2 D940A385			1791 DC CL48'DIDBR test 29b +inf divisor'
0000DA70	C0000000 00000000			1792 DC XL16'C000000000000000800000000000000'
0000DA80	C4C9C4C2 D940A385			1793 DC CL48'DIDBR test 30a +inf divisor'
0000DAB0	80000000 00000000			1794 DC XL16'8000000000000000800000000000000'
0000DAC0	C4C9C4C2 D940A385			1795 DC CL48'DIDBR test 30b +inf divisor'
0000DAF0	80000000 00000000			1796 DC XL16'8000000000000000800000000000000'
0000DB00	C4C9C4C2 D940A385			1797 DC CL48'DIDBR test 31a +inf divisor'
0000DB30	00000000 00000000			1798 DC XL16'0000000000000000000000000000000'
0000DB40	C4C9C4C2 D940A385			1799 DC CL48'DIDBR test 31b +inf divisor'
0000DB70	00000000 00000000			1800 DC XL16'0000000000000000000000000000000'
0000DB80	C4C9C4C2 D940A385			1801 DC CL48'DIDBR test 32a +inf divisor'
0000DBB0	40000000 00000000			1802 DC XL16'4000000000000000000000000000000'
0000DBC0	C4C9C4C2 D940A385			1803 DC CL48'DIDBR test 32b +inf divisor'
0000DBF0	40000000 00000000			1804 DC XL16'4000000000000000000000000000000'
		00000040	00000001	1805 LBFPNFOT_NUM EQU (*-LBFPNFOT_GOOD)/64
				1806 *
				1807 *
		0000DC00	00000001	1808 LBFPNFFL_GOOD EQU *
0000DC00	C4C9C4C2 D940C6D7			1809 DC CL48'DIDBR FPCR pair 1-2'
0000DC30	00800001 F8008001			1810 DC XL16'00800001F800800100000001F8000001'
0000DC40	C4C9C4C2 D940C6D7			1811 DC CL48'DIDBR FPCR pair 3-4'
0000DC70	00000001 F8000001			1812 DC XL16'00000001F800000100800001F8008001'
0000DC80	C4C9C4C2 D940C6D7			1813 DC CL48'DIDBR FPCR pair 5-6'
0000DCB0	00800001 F8008001			1814 DC XL16'00800001F800800100800001F8008001'
0000DCC0	C4C9C4C2 D940C6D7			1815 DC CL48'DIDBR FPCR pair 7-8'
0000DCF0	00800001 F8008001			1816 DC XL16'00800001F800800100800001F8008001'
0000DD00	C4C9C4C2 D940C6D7			1817 DC CL48'DIDBR FPCR pair 9-10'
0000DD30	00800001 F8008001			1818 DC XL16'00800001F800800100800001F8008001'
0000DD40	C4C9C4C2 D940C6D7			1819 DC CL48'DIDBR FPCR pair 11-12'
0000DD70	00800001 F8008001			1820 DC XL16'00800001F800800100800001F8008001'
0000DD80	C4C9C4C2 D940C6D7			1821 DC CL48'DIDBR FPCR pair 13-14'
0000ddb0	00800001 F8008001			1822 DC XL16'00800001F800800100800001F8008001'
0000DDC0	C4C9C4C2 D940C6D7			1823 DC CL48'DIDBR FPCR pair 15-16'
0000DDF0	00800001 F8008001			1824 DC XL16'00800001F800800100800001F8008001'
0000DE00	C4C9C4C2 D940C6D7			1825 DC CL48'DIDBR FPCR pair 17-18'
0000DE30	00800001 F8008001			1826 DC XL16'00800001F800800100800001F8008001'
0000DE40	C4C9C4C2 D940C6D7			1827 DC CL48'DIDBR FPCR pair 19-20'
0000DE70	00800001 F8008001			1828 DC XL16'00800001F800800100800001F8008001'
0000DE80	C4C9C4C2 D940C6D7			1829 DC CL48'DIDBR FPCR pair 21-22'
0000DEB0	00800001 F8008001			1830 DC XL16'00800001F800800100800001F8008001'
0000DEC0	C4C9C4C2 D940C6D7			1831 DC CL48'DIDBR FPCR pair 23-24'
0000DEF0	00800001 F8008001			1832 DC XL16'00800001F800800100800001F8008001'
0000DF00	C4C9C4C2 D940C6D7			1833 DC CL48'DIDBR FPCR pair 25-26'
0000DF30	00000000 F8000000			1834 DC XL16'00000000F800000000000000F8000000'
0000DF40	C4C9C4C2 D940C6D7			1835 DC CL48'DIDBR FPCR pair 27-28'
0000DF70	00000000 F8000000			1836 DC XL16'00000000F800000000000000F8000000'
0000DF80	C4C9C4C2 D940C6D7			1837 DC CL48'DIDBR FPCR pair 29-30'
0000DFB0	00000000 F8000000			1838 DC XL16'00000000F800000000000000F8000000'
0000DFC0	C4C9C4C2 D940C6D7			1839 DC CL48'DIDBR FPCR pair 31-32'
0000DFf0	00000000 F8000000			1840 DC XL16'00000000F800000000000000F8000000'
		00000010	00000001	1841 LBFPNFFL_NUM EQU (*-LBFPNFFL_GOOD)/64
				1842 *
				1843 *
		0000E000	00000001	1844 SBFPRMO_GOOD EQU *
0000E000	C4C9C5C2 D940D996			1845 DC CL48'DIEBR Rounding case 1a'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000E030	3F800000 C1200000			1846	DC XL16'3F800000C12000003F800000C1200000'
0000E040	C4C9C5C2 D940D996			1847	DC CL48'DIEBR Rounding case 1b'
0000E070	BF800000 C1100000			1848	DC XL16'BF800000C1100000BF800000C1100000'
0000E080	C4C9C5C2 D940D996			1849	DC CL48'DIEBR Rounding case 1c'
0000E0B0	3F800000 C1200000			1850	DC XL16'3F800000C12000003F800000C1200000'
0000E0C0	C4C9C5C2 D940D996			1851	DC CL48'DIEBR Rounding case 1d'
0000E0F0	BF800000 C1100000			1852	DC XL16'BF800000C1100000BF800000C1100000'
0000E100	C4C9C5C2 D940D996			1853	DC CL48'DIEBR Rounding case 1e'
0000E130	BF800000 C1100000			1854	DC XL16'BF800000C1100000BF800000C1100000'
0000E140	C4C9C5C2 D940D996			1855	DC CL48'DIEBR Rounding case 1f'
0000E170	3F800000 C1200000			1856	DC XL16'3F800000C12000003F800000C1200000'
0000E180	C4C9C5C2 D940D996			1857	DC CL48'DIEBR Rounding case 1g'
0000E1B0	3F800000 C1200000			1858	DC XL16'3F800000C12000003F800000C1200000'
0000E1C0	C4C9C5C2 D940D996			1859	DC CL48'DIEBR Rounding case 1h'
0000E1F0	BF800000 C1100000			1860	DC XL16'BF800000C1100000BF800000C1100000'
0000E200	C4C9C5C2 D940D996			1861	DC CL48'DIEBR Rounding case 1i'
0000E230	3F800000 C1200000			1862	DC XL16'3F800000C12000003F800000C1200000'
0000E240	C4C9C5C2 D940D996			1863	DC CL48'DIEBR Rounding case 1j'
0000E270	BF800000 C1100000			1864	DC XL16'BF800000C1100000BF800000C1100000'
0000E280	C4C9C5C2 D940D996			1865	DC CL48'DIEBR Rounding case 1k'
0000E2B0	BF800000 C1100000			1866	DC XL16'BF800000C1100000BF800000C1100000'
0000E2C0	C4C9C5C2 D940D996			1867	DC CL48'DIEBR Rounding case 1l'
0000E2F0	3F800000 C1200000			1868	DC XL16'3F800000C12000003F800000C1200000'
0000E300	C4C9C5C2 D940D996			1869	DC CL48'DIEBR Rounding case 1m'
0000E330	3F800000 C1200000			1870	DC XL16'3F800000C12000003F800000C1200000'
0000E340	C4C9C5C2 D940D996			1871	DC CL48'DIEBR Rounding case 1n'
0000E370	BF800000 C1100000			1872	DC XL16'BF800000C1100000BF800000C1100000'
0000E380	C4C9C5C2 D940D996			1873	DC CL48'DIEBR Rounding case 1o'
0000E3B0	3F800000 C1200000			1874	DC XL16'3F800000C12000003F800000C1200000'
0000E3C0	C4C9C5C2 D940D996			1875	DC CL48'DIEBR Rounding case 1p'
0000E3F0	BF800000 C1100000			1876	DC XL16'BF800000C1100000BF800000C1100000'
0000E400	C4C9C5C2 D940D996			1877	DC CL48'DIEBR Rounding case 1q'
0000E430	BF800000 C1100000			1878	DC XL16'BF800000C1100000BF800000C1100000'
0000E440	C4C9C5C2 D940D996			1879	DC CL48'DIEBR Rounding case 1r'
0000E470	3F800000 C1200000			1880	DC XL16'3F800000C12000003F800000C1200000'
0000E480	C4C9C5C2 D940D996			1881	DC CL48'DIEBR Rounding case 1s'
0000E4B0	3F800000 C1200000			1882	DC XL16'3F800000C12000003F800000C1200000'
0000E4C0	C4C9C5C2 D940D996			1883	DC CL48'DIEBR Rounding case 1t'
0000E4F0	BF800000 C1100000			1884	DC XL16'BF800000C1100000BF800000C1100000'
0000E500	C4C9C5C2 D940D996			1885	DC CL48'DIEBR Rounding case 1u'
0000E530	3F800000 C1200000			1886	DC XL16'3F800000C12000003F800000C1200000'
0000E540	C4C9C5C2 D940D996			1887	DC CL48'DIEBR Rounding case 1v'
0000E570	BF800000 C1100000			1888	DC XL16'BF800000C1100000BF800000C1100000'
0000E580	C4C9C5C2 D940D996			1889	DC CL48'DIEBR Rounding case 1w'
0000E5B0	BF800000 C1100000			1890	DC XL16'BF800000C1100000BF800000C1100000'
0000E5C0	C4C9C5C2 D940D996			1891	DC CL48'DIEBR Rounding case 1x'
0000E5F0	3F800000 C1200000			1892	DC XL16'3F800000C12000003F800000C1200000'
0000E600	C4C9C5C2 D940D996			1893	DC CL48'DIEBR Rounding case 2a'
0000E630	3F800000 C0C00000			1894	DC XL16'3F800000C0C000003F800000C0C00000'
0000E640	C4C9C5C2 D940D996			1895	DC CL48'DIEBR Rounding case 2b'
0000E670	BF800000 C0A00000			1896	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E680	C4C9C5C2 D940D996			1897	DC CL48'DIEBR Rounding case 2c'
0000E6B0	3F800000 C0C00000			1898	DC XL16'3F800000C0C000003F800000C0C00000'
0000E6C0	C4C9C5C2 D940D996			1899	DC CL48'DIEBR Rounding case 2d'
0000E6F0	BF800000 C0A00000			1900	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E700	C4C9C5C2 D940D996			1901	DC CL48'DIEBR Rounding case 2e'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000E730	BF800000 C0A00000			1902	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E740	C4C9C5C2 D940D996			1903	DC CL48'DIEBR Rounding case 2f'
0000E770	3F800000 C0C00000			1904	DC XL16'3F800000C0C000003F800000C0C00000'
0000E780	C4C9C5C2 D940D996			1905	DC CL48'DIEBR Rounding case 2g'
0000E7B0	3F800000 C0C00000			1906	DC XL16'3F800000C0C000003F800000C0C00000'
0000E7C0	C4C9C5C2 D940D996			1907	DC CL48'DIEBR Rounding case 2h'
0000E7F0	BF800000 C0A00000			1908	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E800	C4C9C5C2 D940D996			1909	DC CL48'DIEBR Rounding case 2i'
0000E830	3F800000 C0C00000			1910	DC XL16'3F800000C0C000003F800000C0C00000'
0000E840	C4C9C5C2 D940D996			1911	DC CL48'DIEBR Rounding case 2j'
0000E870	BF800000 C0A00000			1912	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E880	C4C9C5C2 D940D996			1913	DC CL48'DIEBR Rounding case 2k'
0000E8B0	BF800000 C0A00000			1914	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E8C0	C4C9C5C2 D940D996			1915	DC CL48'DIEBR Rounding case 2l'
0000E8F0	3F800000 C0C00000			1916	DC XL16'3F800000C0C000003F800000C0C00000'
0000E900	C4C9C5C2 D940D996			1917	DC CL48'DIEBR Rounding case 2m'
0000E930	3F800000 C0C00000			1918	DC XL16'3F800000C0C000003F800000C0C00000'
0000E940	C4C9C5C2 D940D996			1919	DC CL48'DIEBR Rounding case 2n'
0000E970	BF800000 C0A00000			1920	DC XL16'BF800000C0A00000BF800000C0A00000'
0000E980	C4C9C5C2 D940D996			1921	DC CL48'DIEBR Rounding case 2o'
0000E9B0	3F800000 C0C00000			1922	DC XL16'3F800000C0C000003F800000C0C00000'
0000E9C0	C4C9C5C2 D940D996			1923	DC CL48'DIEBR Rounding case 2p'
0000E9F0	BF800000 C0A00000			1924	DC XL16'BF800000C0A00000BF800000C0A00000'
0000EA00	C4C9C5C2 D940D996			1925	DC CL48'DIEBR Rounding case 2q'
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0000EA40	C4C9C5C2 D940D996			1927	DC CL48'DIEBR Rounding case 2r'
0000EA70	3F800000 C0C00000			1928	DC XL16'3F800000C0C000003F800000C0C00000'
0000EA80	C4C9C5C2 D940D996			1929	DC CL48'DIEBR Rounding case 2s'
0000EAB0	3F800000 C0C00000			1930	DC XL16'3F800000C0C000003F800000C0C00000'
0000EAC0	C4C9C5C2 D940D996			1931	DC CL48'DIEBR Rounding case 2t'
0000EAF0	BF800000 C0A00000			1932	DC XL16'BF800000C0A00000BF800000C0A00000'
0000EB00	C4C9C5C2 D940D996			1933	DC CL48'DIEBR Rounding case 2u'
0000EB30	3F800000 C0C00000			1934	DC XL16'3F800000C0C000003F800000C0C00000'
0000EB40	C4C9C5C2 D940D996			1935	DC CL48'DIEBR Rounding case 2v'
0000EB70	BF800000 C0A00000			1936	DC XL16'BF800000C0A00000BF800000C0A00000'
0000EB80	C4C9C5C2 D940D996			1937	DC CL48'DIEBR Rounding case 2w'
0000EBB0	BF800000 C0A00000			1938	DC XL16'BF800000C0A00000BF800000C0A00000'
0000EBC0	C4C9C5C2 D940D996			1939	DC CL48'DIEBR Rounding case 2x'
0000EBF0	3F800000 C0C00000			1940	DC XL16'3F800000C0C000003F800000C0C00000'
0000EC00	C4C9C5C2 D940D996			1941	DC CL48'DIEBR Rounding case 3a'
0000EC30	3F800000 C0400000			1942	DC XL16'3F800000C04000003F800000C0400000'
0000EC40	C4C9C5C2 D940D996			1943	DC CL48'DIEBR Rounding case 3b'
0000EC70	3F800000 C0400000			1944	DC XL16'3F800000C04000003F800000C0400000'
0000EC80	C4C9C5C2 D940D996			1945	DC CL48'DIEBR Rounding case 3c'
0000ECB0	BF800000 C0000000			1946	DC XL16'BF800000C0000000BF800000C0000000'
0000ECC0	C4C9C5C2 D940D996			1947	DC CL48'DIEBR Rounding case 3d'
0000ECF0	BF800000 C0000000			1948	DC XL16'BF800000C0000000BF800000C0000000'
0000ED00	C4C9C5C2 D940D996			1949	DC CL48'DIEBR Rounding case 3e'
0000ED30	BF800000 C0000000			1950	DC XL16'BF800000C0000000BF800000C0000000'
0000ED40	C4C9C5C2 D940D996			1951	DC CL48'DIEBR Rounding case 3f'
0000ED70	3F800000 C0400000			1952	DC XL16'3F800000C04000003F800000C0400000'
0000ED80	C4C9C5C2 D940D996			1953	DC CL48'DIEBR Rounding case 3g'
0000EDB0	3F800000 C0400000			1954	DC XL16'3F800000C04000003F800000C0400000'
0000EDC0	C4C9C5C2 D940D996			1955	DC CL48'DIEBR Rounding case 3h'
0000EDF0	3F800000 C0400000			1956	DC XL16'3F800000C04000003F800000C0400000'
0000EE00	C4C9C5C2 D940D996			1957	DC CL48'DIEBR Rounding case 3i'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000EE30	BF800000 C0000000			1958 DC XL16'BF800000C0000000BF800000C0000000'
0000EE40	C4C9C5C2 D940D996			1959 DC CL48'DIEBR Rounding case 3j'
0000EE70	BF800000 C0000000			1960 DC XL16'BF800000C0000000BF800000C0000000'
0000EE80	C4C9C5C2 D940D996			1961 DC CL48'DIEBR Rounding case 3k'
0000EEB0	BF800000 C0000000			1962 DC XL16'BF800000C0000000BF800000C0000000'
0000EEC0	C4C9C5C2 D940D996			1963 DC CL48'DIEBR Rounding case 3l'
0000EEF0	3F800000 C0400000			1964 DC XL16'3F800000C04000003F800000C0400000'
0000EF00	C4C9C5C2 D940D996			1965 DC CL48'DIEBR Rounding case 3m'
0000EF30	3F800000 C0400000			1966 DC XL16'3F800000C04000003F800000C0400000'
0000EF40	C4C9C5C2 D940D996			1967 DC CL48'DIEBR Rounding case 3n'
0000EF70	3F800000 C0400000			1968 DC XL16'3F800000C04000003F800000C0400000'
0000EF80	C4C9C5C2 D940D996			1969 DC CL48'DIEBR Rounding case 3o'
0000EFB0	BF800000 C0000000			1970 DC XL16'BF800000C0000000BF800000C0000000'
0000EFC0	C4C9C5C2 D940D996			1971 DC CL48'DIEBR Rounding case 3p'
0000EFF0	BF800000 C0000000			1972 DC XL16'BF800000C0000000BF800000C0000000'
0000F000	C4C9C5C2 D940D996			1973 DC CL48'DIEBR Rounding case 3q'
0000F030	BF800000 C0000000			1974 DC XL16'BF800000C0000000BF800000C0000000'
0000F040	C4C9C5C2 D940D996			1975 DC CL48'DIEBR Rounding case 3r'
0000F070	3F800000 C0400000			1976 DC XL16'3F800000C04000003F800000C0400000'
0000F080	C4C9C5C2 D940D996			1977 DC CL48'DIEBR Rounding case 3s'
0000F0B0	3F800000 C0400000			1978 DC XL16'3F800000C04000003F800000C0400000'
0000F0C0	C4C9C5C2 D940D996			1979 DC CL48'DIEBR Rounding case 3t'
0000F0F0	3F800000 C0400000			1980 DC XL16'3F800000C04000003F800000C0400000'
0000F100	C4C9C5C2 D940D996			1981 DC CL48'DIEBR Rounding case 3u'
0000F130	BF800000 C0000000			1982 DC XL16'BF800000C0000000BF800000C0000000'
0000F140	C4C9C5C2 D940D996			1983 DC CL48'DIEBR Rounding case 3v'
0000F170	BF800000 C0000000			1984 DC XL16'BF800000C0000000BF800000C0000000'
0000F180	C4C9C5C2 D940D996			1985 DC CL48'DIEBR Rounding case 3w'
0000F1B0	BF800000 C0000000			1986 DC XL16'BF800000C0000000BF800000C0000000'
0000F1C0	C4C9C5C2 D940D996			1987 DC CL48'DIEBR Rounding case 3x'
0000F1F0	3F800000 C0400000			1988 DC XL16'3F800000C04000003F800000C0400000'
0000F200	C4C9C5C2 D940D996			1989 DC CL48'DIEBR Rounding case 4a'
0000F230	3F800000 C0000000			1990 DC XL16'3F800000C00000003F800000C0000000'
0000F240	C4C9C5C2 D940D996			1991 DC CL48'DIEBR Rounding case 4b'
0000F270	BF800000 BF800000			1992 DC XL16'BF800000BF800000BF800000BF800000'
0000F280	C4C9C5C2 D940D996			1993 DC CL48'DIEBR Rounding case 4c'
0000F2B0	3F800000 C0000000			1994 DC XL16'3F800000C00000003F800000C0000000'
0000F2C0	C4C9C5C2 D940D996			1995 DC CL48'DIEBR Rounding case 4d'
0000F2F0	BF800000 BF800000			1996 DC XL16'BF800000BF800000BF800000BF800000'
0000F300	C4C9C5C2 D940D996			1997 DC CL48'DIEBR Rounding case 4e'
0000F330	BF800000 BF800000			1998 DC XL16'BF800000BF800000BF800000BF800000'
0000F340	C4C9C5C2 D940D996			1999 DC CL48'DIEBR Rounding case 4f'
0000F370	3F800000 C0000000			2000 DC XL16'3F800000C00000003F800000C0000000'
0000F380	C4C9C5C2 D940D996			2001 DC CL48'DIEBR Rounding case 4g'
0000F3B0	3F800000 C0000000			2002 DC XL16'3F800000C00000003F800000C0000000'
0000F3C0	C4C9C5C2 D940D996			2003 DC CL48'DIEBR Rounding case 4h'
0000F3F0	BF800000 BF800000			2004 DC XL16'BF800000BF800000BF800000BF800000'
0000F400	C4C9C5C2 D940D996			2005 DC CL48'DIEBR Rounding case 4i'
0000F430	3F800000 C0000000			2006 DC XL16'3F800000C00000003F800000C0000000'
0000F440	C4C9C5C2 D940D996			2007 DC CL48'DIEBR Rounding case 4j'
0000F470	BF800000 BF800000			2008 DC XL16'BF800000BF800000BF800000BF800000'
0000F480	C4C9C5C2 D940D996			2009 DC CL48'DIEBR Rounding case 4k'
0000F4B0	BF800000 BF800000			2010 DC XL16'BF800000BF800000BF800000BF800000'
0000F4C0	C4C9C5C2 D940D996			2011 DC CL48'DIEBR Rounding case 4l'
0000F4F0	3F800000 C0000000			2012 DC XL16'3F800000C00000003F800000C0000000'
0000F500	C4C9C5C2 D940D996			2013 DC CL48'DIEBR Rounding case 4m'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000F530	3F800000 C0000000			2014	DC XL16'3F800000C00000003F800000C0000000'
0000F540	C4C9C5C2 D940D996			2015	DC CL48'DIEBR Rounding case 4n'
0000F570	BF800000 BF800000			2016	DC XL16'BF800000BF800000BF800000BF800000'
0000F580	C4C9C5C2 D940D996			2017	DC CL48'DIEBR Rounding case 4o'
0000F5B0	3F800000 C0000000			2018	DC XL16'3F800000C00000003F800000C0000000'
0000F5C0	C4C9C5C2 D940D996			2019	DC CL48'DIEBR Rounding case 4p'
0000F5F0	BF800000 BF800000			2020	DC XL16'BF800000BF800000BF800000BF800000'
0000F600	C4C9C5C2 D940D996			2021	DC CL48'DIEBR Rounding case 4q'
0000F630	BF800000 BF800000			2022	DC XL16'BF800000BF800000BF800000BF800000'
0000F640	C4C9C5C2 D940D996			2023	DC CL48'DIEBR Rounding case 4r'
0000F670	3F800000 C0000000			2024	DC XL16'3F800000C00000003F800000C0000000'
0000F680	C4C9C5C2 D940D996			2025	DC CL48'DIEBR Rounding case 4s'
0000F6B0	3F800000 C0000000			2026	DC XL16'3F800000C00000003F800000C0000000'
0000F6C0	C4C9C5C2 D940D996			2027	DC CL48'DIEBR Rounding case 4t'
0000F6F0	BF800000 BF800000			2028	DC XL16'BF800000BF800000BF800000BF800000'
0000F700	C4C9C5C2 D940D996			2029	DC CL48'DIEBR Rounding case 4u'
0000F730	3F800000 C0000000			2030	DC XL16'3F800000C00000003F800000C0000000'
0000F740	C4C9C5C2 D940D996			2031	DC CL48'DIEBR Rounding case 4v'
0000F770	BF800000 BF800000			2032	DC XL16'BF800000BF800000BF800000BF800000'
0000F780	C4C9C5C2 D940D996			2033	DC CL48'DIEBR Rounding case 4w'
0000F7B0	BF800000 BF800000			2034	DC XL16'BF800000BF800000BF800000BF800000'
0000F7C0	C4C9C5C2 D940D996			2035	DC CL48'DIEBR Rounding case 4x'
0000F7F0	3F800000 C0000000			2036	DC XL16'3F800000C00000003F800000C0000000'
0000F800	C4C9C5C2 D940D996			2037	DC CL48'DIEBR Rounding case 5a'
0000F830	3F800000 BF800000			2038	DC XL16'3F800000BF8000003F800000BF800000'
0000F840	C4C9C5C2 D940D996			2039	DC CL48'DIEBR Rounding case 5b'
0000F870	3F800000 BF800000			2040	DC XL16'3F800000BF8000003F800000BF800000'
0000F880	C4C9C5C2 D940D996			2041	DC CL48'DIEBR Rounding case 5c'
0000F8B0	BF800000 80000000			2042	DC XL16'BF80000080000000BF80000080000000'
0000F8C0	C4C9C5C2 D940D996			2043	DC CL48'DIEBR Rounding case 5d'
0000F8F0	BF800000 80000000			2044	DC XL16'BF80000080000000BF80000080000000'
0000F900	C4C9C5C2 D940D996			2045	DC CL48'DIEBR Rounding case 5e'
0000F930	BF800000 80000000			2046	DC XL16'BF80000080000000BF80000080000000'
0000F940	C4C9C5C2 D940D996			2047	DC CL48'DIEBR Rounding case 5f'
0000F970	3F800000 BF800000			2048	DC XL16'3F800000BF8000003F800000BF800000'
0000F980	C4C9C5C2 D940D996			2049	DC CL48'DIEBR Rounding case 5g'
0000F9B0	3F800000 BF800000			2050	DC XL16'3F800000BF8000003F800000BF800000'
0000F9C0	C4C9C5C2 D940D996			2051	DC CL48'DIEBR Rounding case 5h'
0000F9F0	3F800000 BF800000			2052	DC XL16'3F800000BF8000003F800000BF800000'
0000FA00	C4C9C5C2 D940D996			2053	DC CL48'DIEBR Rounding case 5i'
0000FA30	BF800000 80000000			2054	DC XL16'BF80000080000000BF80000080000000'
0000FA40	C4C9C5C2 D940D996			2055	DC CL48'DIEBR Rounding case 5j'
0000FA70	BF800000 80000000			2056	DC XL16'BF80000080000000BF80000080000000'
0000FA80	C4C9C5C2 D940D996			2057	DC CL48'DIEBR Rounding case 5k'
0000FAB0	BF800000 80000000			2058	DC XL16'BF80000080000000BF80000080000000'
0000FAC0	C4C9C5C2 D940D996			2059	DC CL48'DIEBR Rounding case 5l'
0000FAF0	3F800000 BF800000			2060	DC XL16'3F800000BF8000003F800000BF800000'
0000FB00	C4C9C5C2 D940D996			2061	DC CL48'DIEBR Rounding case 5m'
0000FB30	3F800000 BF800000			2062	DC XL16'3F800000BF8000003F800000BF800000'
0000FB40	C4C9C5C2 D940D996			2063	DC CL48'DIEBR Rounding case 5n'
0000FB70	3F800000 BF800000			2064	DC XL16'3F800000BF8000003F800000BF800000'
0000FB80	C4C9C5C2 D940D996			2065	DC CL48'DIEBR Rounding case 5o'
0000FBB0	BF800000 80000000			2066	DC XL16'BF80000080000000BF80000080000000'
0000FBC0	C4C9C5C2 D940D996			2067	DC CL48'DIEBR Rounding case 5p'
0000FBF0	BF800000 80000000			2068	DC XL16'BF80000080000000BF80000080000000'
0000FC00	C4C9C5C2 D940D996			2069	DC CL48'DIEBR Rounding case 5q'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000FC30	BF800000 80000000			2070	DC XL16'BF80000080000000BF80000080000000'
0000FC40	C4C9C5C2 D940D996			2071	DC CL48'DIEBR Rounding case 5r'
0000FC70	3F800000 BF800000			2072	DC XL16'3F800000BF8000003F800000BF800000'
0000FC80	C4C9C5C2 D940D996			2073	DC CL48'DIEBR Rounding case 5s'
0000FCB0	3F800000 BF800000			2074	DC XL16'3F800000BF8000003F800000BF800000'
0000FCC0	C4C9C5C2 D940D996			2075	DC CL48'DIEBR Rounding case 5t'
0000FCF0	3F800000 BF800000			2076	DC XL16'3F800000BF8000003F800000BF800000'
0000FD00	C4C9C5C2 D940D996			2077	DC CL48'DIEBR Rounding case 5u'
0000FD30	BF800000 80000000			2078	DC XL16'BF80000080000000BF80000080000000'
0000FD40	C4C9C5C2 D940D996			2079	DC CL48'DIEBR Rounding case 5v'
0000FD70	BF800000 80000000			2080	DC XL16'BF80000080000000BF80000080000000'
0000FD80	C4C9C5C2 D940D996			2081	DC CL48'DIEBR Rounding case 5w'
0000FDB0	BF800000 80000000			2082	DC XL16'BF80000080000000BF80000080000000'
0000FDC0	C4C9C5C2 D940D996			2083	DC CL48'DIEBR Rounding case 5x'
0000FDF0	3F800000 BF800000			2084	DC XL16'3F800000BF8000003F800000BF800000'
0000FE00	C4C9C5C2 D940D996			2085	DC CL48'DIEBR Rounding case 6a'
0000FE30	BF800000 3F800000			2086	DC XL16'BF8000003F800000BF8000003F800000'
0000FE40	C4C9C5C2 D940D996			2087	DC CL48'DIEBR Rounding case 6b'
0000FE70	BF800000 3F800000			2088	DC XL16'BF8000003F800000BF8000003F800000'
0000FE80	C4C9C5C2 D940D996			2089	DC CL48'DIEBR Rounding case 6c'
0000FEB0	3F800000 00000000			2090	DC XL16'3F800000000000003F80000000000000'
0000FEC0	C4C9C5C2 D940D996			2091	DC CL48'DIEBR Rounding case 6d'
0000FEF0	3F800000 00000000			2092	DC XL16'3F800000000000003F80000000000000'
0000FF00	C4C9C5C2 D940D996			2093	DC CL48'DIEBR Rounding case 6e'
0000FF30	BF800000 3F800000			2094	DC XL16'BF8000003F800000BF8000003F800000'
0000FF40	C4C9C5C2 D940D996			2095	DC CL48'DIEBR Rounding case 6f'
0000FF70	3F800000 00000000			2096	DC XL16'3F800000000000003F80000000000000'
0000FF80	C4C9C5C2 D940D996			2097	DC CL48'DIEBR Rounding case 6g'
0000FFB0	BF800000 3F800000			2098	DC XL16'BF8000003F800000BF8000003F800000'
0000FFC0	C4C9C5C2 D940D996			2099	DC CL48'DIEBR Rounding case 6h'
0000FFF0	BF800000 3F800000			2100	DC XL16'BF8000003F800000BF8000003F800000'
00010000	C4C9C5C2 D940D996			2101	DC CL48'DIEBR Rounding case 6i'
00010030	3F800000 00000000			2102	DC XL16'3F800000000000003F80000000000000'
00010040	C4C9C5C2 D940D996			2103	DC CL48'DIEBR Rounding case 6j'
00010070	3F800000 00000000			2104	DC XL16'3F800000000000003F80000000000000'
00010080	C4C9C5C2 D940D996			2105	DC CL48'DIEBR Rounding case 6k'
000100B0	BF800000 3F800000			2106	DC XL16'BF8000003F800000BF8000003F800000'
000100C0	C4C9C5C2 D940D996			2107	DC CL48'DIEBR Rounding case 6l'
000100F0	3F800000 00000000			2108	DC XL16'3F800000000000003F80000000000000'
00010100	C4C9C5C2 D940D996			2109	DC CL48'DIEBR Rounding case 6m'
00010130	BF800000 3F800000			2110	DC XL16'BF8000003F800000BF8000003F800000'
00010140	C4C9C5C2 D940D996			2111	DC CL48'DIEBR Rounding case 6n'
00010170	BF800000 3F800000			2112	DC XL16'BF8000003F800000BF8000003F800000'
00010180	C4C9C5C2 D940D996			2113	DC CL48'DIEBR Rounding case 6o'
000101B0	3F800000 00000000			2114	DC XL16'3F800000000000003F80000000000000'
000101C0	C4C9C5C2 D940D996			2115	DC CL48'DIEBR Rounding case 6p'
000101F0	3F800000 00000000			2116	DC XL16'3F800000000000003F80000000000000'
00010200	C4C9C5C2 D940D996			2117	DC CL48'DIEBR Rounding case 6q'
00010230	BF800000 3F800000			2118	DC XL16'BF8000003F800000BF8000003F800000'
00010240	C4C9C5C2 D940D996			2119	DC CL48'DIEBR Rounding case 6r'
00010270	3F800000 00000000			2120	DC XL16'3F800000000000003F80000000000000'
00010280	C4C9C5C2 D940D996			2121	DC CL48'DIEBR Rounding case 6s'
000102B0	BF800000 3F800000			2122	DC XL16'BF8000003F800000BF8000003F800000'
000102C0	C4C9C5C2 D940D996			2123	DC CL48'DIEBR Rounding case 6t'
000102F0	BF800000 3F800000			2124	DC XL16'BF8000003F800000BF8000003F800000'
00010300	C4C9C5C2 D940D996			2125	DC CL48'DIEBR Rounding case 6u'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00010330	3F800000 00000000			2126 DC XL16'3F800000000000003F80000000000000'
00010340	C4C9C5C2 D940D996			2127 DC CL48'DIEBR Rounding case 6v'
00010370	3F800000 00000000			2128 DC XL16'3F800000000000003F80000000000000'
00010380	C4C9C5C2 D940D996			2129 DC CL48'DIEBR Rounding case 6w'
000103B0	BF800000 3F800000			2130 DC XL16'BF8000003F800000BF8000003F800000'
000103C0	C4C9C5C2 D940D996			2131 DC CL48'DIEBR Rounding case 6x'
000103F0	3F800000 00000000			2132 DC XL16'3F800000000000003F80000000000000'
00010400	C4C9C5C2 D940D996			2133 DC CL48'DIEBR Rounding case 7a'
00010430	BF800000 40000000			2134 DC XL16'BF80000040000000BF80000040000000'
00010440	C4C9C5C2 D940D996			2135 DC CL48'DIEBR Rounding case 7b'
00010470	3F800000 3F800000			2136 DC XL16'3F8000003F8000003F8000003F800000'
00010480	C4C9C5C2 D940D996			2137 DC CL48'DIEBR Rounding case 7c'
000104B0	BF800000 40000000			2138 DC XL16'BF80000040000000BF80000040000000'
000104C0	C4C9C5C2 D940D996			2139 DC CL48'DIEBR Rounding case 7d'
000104F0	3F800000 3F800000			2140 DC XL16'3F8000003F8000003F8000003F800000'
00010500	C4C9C5C2 D940D996			2141 DC CL48'DIEBR Rounding case 7e'
00010530	BF800000 40000000			2142 DC XL16'BF80000040000000BF80000040000000'
00010540	C4C9C5C2 D940D996			2143 DC CL48'DIEBR Rounding case 7f'
00010570	3F800000 3F800000			2144 DC XL16'3F8000003F8000003F8000003F800000'
00010580	C4C9C5C2 D940D996			2145 DC CL48'DIEBR Rounding case 7g'
000105B0	BF800000 40000000			2146 DC XL16'BF80000040000000BF80000040000000'
000105C0	C4C9C5C2 D940D996			2147 DC CL48'DIEBR Rounding case 7h'
000105F0	3F800000 3F800000			2148 DC XL16'3F8000003F8000003F8000003F800000'
00010600	C4C9C5C2 D940D996			2149 DC CL48'DIEBR Rounding case 7i'
00010630	BF800000 40000000			2150 DC XL16'BF80000040000000BF80000040000000'
00010640	C4C9C5C2 D940D996			2151 DC CL48'DIEBR Rounding case 7j'
00010670	3F800000 3F800000			2152 DC XL16'3F8000003F8000003F8000003F800000'
00010680	C4C9C5C2 D940D996			2153 DC CL48'DIEBR Rounding case 7k'
000106B0	BF800000 40000000			2154 DC XL16'BF80000040000000BF80000040000000'
000106C0	C4C9C5C2 D940D996			2155 DC CL48'DIEBR Rounding case 7l'
000106F0	3F800000 3F800000			2156 DC XL16'3F8000003F8000003F8000003F800000'
00010700	C4C9C5C2 D940D996			2157 DC CL48'DIEBR Rounding case 7m'
00010730	BF800000 40000000			2158 DC XL16'BF80000040000000BF80000040000000'
00010740	C4C9C5C2 D940D996			2159 DC CL48'DIEBR Rounding case 7n'
00010770	3F800000 3F800000			2160 DC XL16'3F8000003F8000003F8000003F800000'
00010780	C4C9C5C2 D940D996			2161 DC CL48'DIEBR Rounding case 7o'
000107B0	BF800000 40000000			2162 DC XL16'BF80000040000000BF80000040000000'
000107C0	C4C9C5C2 D940D996			2163 DC CL48'DIEBR Rounding case 7p'
000107F0	3F800000 3F800000			2164 DC XL16'3F8000003F8000003F8000003F800000'
00010800	C4C9C5C2 D940D996			2165 DC CL48'DIEBR Rounding case 7q'
00010830	BF800000 40000000			2166 DC XL16'BF80000040000000BF80000040000000'
00010840	C4C9C5C2 D940D996			2167 DC CL48'DIEBR Rounding case 7r'
00010870	3F800000 3F800000			2168 DC XL16'3F8000003F8000003F8000003F800000'
00010880	C4C9C5C2 D940D996			2169 DC CL48'DIEBR Rounding case 7s'
000108B0	BF800000 40000000			2170 DC XL16'BF80000040000000BF80000040000000'
000108C0	C4C9C5C2 D940D996			2171 DC CL48'DIEBR Rounding case 7t'
000108F0	3F800000 3F800000			2172 DC XL16'3F8000003F8000003F8000003F800000'
00010900	C4C9C5C2 D940D996			2173 DC CL48'DIEBR Rounding case 7u'
00010930	BF800000 40000000			2174 DC XL16'BF80000040000000BF80000040000000'
00010940	C4C9C5C2 D940D996			2175 DC CL48'DIEBR Rounding case 7v'
00010970	3F800000 3F800000			2176 DC XL16'3F8000003F8000003F8000003F800000'
00010980	C4C9C5C2 D940D996			2177 DC CL48'DIEBR Rounding case 7w'
000109B0	BF800000 40000000			2178 DC XL16'BF80000040000000BF80000040000000'
000109C0	C4C9C5C2 D940D996			2179 DC CL48'DIEBR Rounding case 7x'
000109F0	3F800000 3F800000			2180 DC XL16'3F8000003F8000003F8000003F800000'
00010A00	C4C9C5C2 D940D996			2181 DC CL48'DIEBR Rounding case 8a'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00010A30	BF800000 40400000			2182	DC XL16'BF80000040400000BF80000040400000'
00010A40	C4C9C5C2 D940D996			2183	DC CL48'DIEBR Rounding case 8b'
00010A70	BF800000 40400000			2184	DC XL16'BF80000040400000BF80000040400000'
00010A80	C4C9C5C2 D940D996			2185	DC CL48'DIEBR Rounding case 8c'
00010AB0	3F800000 40000000			2186	DC XL16'3F800000400000003F80000040000000'
00010AC0	C4C9C5C2 D940D996			2187	DC CL48'DIEBR Rounding case 8d'
00010AF0	3F800000 40000000			2188	DC XL16'3F800000400000003F80000040000000'
00010B00	C4C9C5C2 D940D996			2189	DC CL48'DIEBR Rounding case 8e'
00010B30	BF800000 40400000			2190	DC XL16'BF80000040400000BF80000040400000'
00010B40	C4C9C5C2 D940D996			2191	DC CL48'DIEBR Rounding case 8f'
00010B70	3F800000 40000000			2192	DC XL16'3F800000400000003F80000040000000'
00010B80	C4C9C5C2 D940D996			2193	DC CL48'DIEBR Rounding case 8g'
00010BB0	BF800000 40400000			2194	DC XL16'BF80000040400000BF80000040400000'
00010BC0	C4C9C5C2 D940D996			2195	DC CL48'DIEBR Rounding case 8h'
00010BF0	BF800000 40400000			2196	DC XL16'BF80000040400000BF80000040400000'
00010C00	C4C9C5C2 D940D996			2197	DC CL48'DIEBR Rounding case 8i'
00010C30	3F800000 40000000			2198	DC XL16'3F800000400000003F80000040000000'
00010C40	C4C9C5C2 D940D996			2199	DC CL48'DIEBR Rounding case 8j'
00010C70	3F800000 40000000			2200	DC XL16'3F800000400000003F80000040000000'
00010C80	C4C9C5C2 D940D996			2201	DC CL48'DIEBR Rounding case 8k'
00010CB0	BF800000 40400000			2202	DC XL16'BF80000040400000BF80000040400000'
00010CC0	C4C9C5C2 D940D996			2203	DC CL48'DIEBR Rounding case 8l'
00010CF0	3F800000 40000000			2204	DC XL16'3F800000400000003F80000040000000'
00010D00	C4C9C5C2 D940D996			2205	DC CL48'DIEBR Rounding case 8m'
00010D30	BF800000 40400000			2206	DC XL16'BF80000040400000BF80000040400000'
00010D40	C4C9C5C2 D940D996			2207	DC CL48'DIEBR Rounding case 8n'
00010D70	BF800000 40400000			2208	DC XL16'BF80000040400000BF80000040400000'
00010D80	C4C9C5C2 D940D996			2209	DC CL48'DIEBR Rounding case 8o'
00010DB0	3F800000 40000000			2210	DC XL16'3F800000400000003F80000040000000'
00010DC0	C4C9C5C2 D940D996			2211	DC CL48'DIEBR Rounding case 8p'
00010DF0	3F800000 40000000			2212	DC XL16'3F800000400000003F80000040000000'
00010E00	C4C9C5C2 D940D996			2213	DC CL48'DIEBR Rounding case 8q'
00010E30	BF800000 40400000			2214	DC XL16'BF80000040400000BF80000040400000'
00010E40	C4C9C5C2 D940D996			2215	DC CL48'DIEBR Rounding case 8r'
00010E70	3F800000 40000000			2216	DC XL16'3F800000400000003F80000040000000'
00010E80	C4C9C5C2 D940D996			2217	DC CL48'DIEBR Rounding case 8s'
00010EB0	BF800000 40400000			2218	DC XL16'BF80000040400000BF80000040400000'
00010EC0	C4C9C5C2 D940D996			2219	DC CL48'DIEBR Rounding case 8t'
00010EF0	BF800000 40400000			2220	DC XL16'BF80000040400000BF80000040400000'
00010F00	C4C9C5C2 D940D996			2221	DC CL48'DIEBR Rounding case 8u'
00010F30	3F800000 40000000			2222	DC XL16'3F800000400000003F80000040000000'
00010F40	C4C9C5C2 D940D996			2223	DC CL48'DIEBR Rounding case 8v'
00010F70	3F800000 40000000			2224	DC XL16'3F800000400000003F80000040000000'
00010F80	C4C9C5C2 D940D996			2225	DC CL48'DIEBR Rounding case 8w'
00010FB0	BF800000 40400000			2226	DC XL16'BF80000040400000BF80000040400000'
00010FC0	C4C9C5C2 D940D996			2227	DC CL48'DIEBR Rounding case 8x'
00010FF0	3F800000 40000000			2228	DC XL16'3F800000400000003F80000040000000'
00011000	C4C9C5C2 D940D996			2229	DC CL48'DIEBR Rounding case 9a'
00011030	BF800000 40C00000			2230	DC XL16'BF80000040C00000BF80000040C00000'
00011040	C4C9C5C2 D940D996			2231	DC CL48'DIEBR Rounding case 9b'
00011070	3F800000 40A00000			2232	DC XL16'3F80000040A000003F80000040A00000'
00011080	C4C9C5C2 D940D996			2233	DC CL48'DIEBR Rounding case 9c'
000110B0	BF800000 40C00000			2234	DC XL16'BF80000040C00000BF80000040C00000'
000110C0	C4C9C5C2 D940D996			2235	DC CL48'DIEBR Rounding case 9d'
000110F0	3F800000 40A00000			2236	DC XL16'3F80000040A000003F80000040A00000'
00011100	C4C9C5C2 D940D996			2237	DC CL48'DIEBR Rounding case 9e'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00011130	BF800000 40C00000			2238	DC XL16'BF80000040C00000BF80000040C00000'
00011140	C4C9C5C2 D940D996			2239	DC CL48'DIEBR Rounding case 9f'
00011170	3F800000 40A00000			2240	DC XL16'3F80000040A000003F80000040A00000'
00011180	C4C9C5C2 D940D996			2241	DC CL48'DIEBR Rounding case 9g'
000111B0	BF800000 40C00000			2242	DC XL16'BF80000040C00000BF80000040C00000'
000111C0	C4C9C5C2 D940D996			2243	DC CL48'DIEBR Rounding case 9h'
000111F0	3F800000 40A00000			2244	DC XL16'3F80000040A000003F80000040A00000'
00011200	C4C9C5C2 D940D996			2245	DC CL48'DIEBR Rounding case 9i'
00011230	BF800000 40C00000			2246	DC XL16'BF80000040C00000BF80000040C00000'
00011240	C4C9C5C2 D940D996			2247	DC CL48'DIEBR Rounding case 9j'
00011270	3F800000 40A00000			2248	DC XL16'3F80000040A000003F80000040A00000'
00011280	C4C9C5C2 D940D996			2249	DC CL48'DIEBR Rounding case 9k'
000112B0	BF800000 40C00000			2250	DC XL16'BF80000040C00000BF80000040C00000'
000112C0	C4C9C5C2 D940D996			2251	DC CL48'DIEBR Rounding case 9l'
000112F0	3F800000 40A00000			2252	DC XL16'3F80000040A000003F80000040A00000'
00011300	C4C9C5C2 D940D996			2253	DC CL48'DIEBR Rounding case 9m'
00011330	BF800000 40C00000			2254	DC XL16'BF80000040C00000BF80000040C00000'
00011340	C4C9C5C2 D940D996			2255	DC CL48'DIEBR Rounding case 9n'
00011370	3F800000 40A00000			2256	DC XL16'3F80000040A000003F80000040A00000'
00011380	C4C9C5C2 D940D996			2257	DC CL48'DIEBR Rounding case 9o'
000113B0	BF800000 40C00000			2258	DC XL16'BF80000040C00000BF80000040C00000'
000113C0	C4C9C5C2 D940D996			2259	DC CL48'DIEBR Rounding case 9p'
000113F0	3F800000 40A00000			2260	DC XL16'3F80000040A000003F80000040A00000'
00011400	C4C9C5C2 D940D996			2261	DC CL48'DIEBR Rounding case 9q'
00011430	BF800000 40C00000			2262	DC XL16'BF80000040C00000BF80000040C00000'
00011440	C4C9C5C2 D940D996			2263	DC CL48'DIEBR Rounding case 9r'
00011470	3F800000 40A00000			2264	DC XL16'3F80000040A000003F80000040A00000'
00011480	C4C9C5C2 D940D996			2265	DC CL48'DIEBR Rounding case 9s'
000114B0	BF800000 40C00000			2266	DC XL16'BF80000040C00000BF80000040C00000'
000114C0	C4C9C5C2 D940D996			2267	DC CL48'DIEBR Rounding case 9t'
000114F0	3F800000 40A00000			2268	DC XL16'3F80000040A000003F80000040A00000'
00011500	C4C9C5C2 D940D996			2269	DC CL48'DIEBR Rounding case 9u'
00011530	BF800000 40C00000			2270	DC XL16'BF80000040C00000BF80000040C00000'
00011540	C4C9C5C2 D940D996			2271	DC CL48'DIEBR Rounding case 9v'
00011570	3F800000 40A00000			2272	DC XL16'3F80000040A000003F80000040A00000'
00011580	C4C9C5C2 D940D996			2273	DC CL48'DIEBR Rounding case 9w'
000115B0	BF800000 40C00000			2274	DC XL16'BF80000040C00000BF80000040C00000'
000115C0	C4C9C5C2 D940D996			2275	DC CL48'DIEBR Rounding case 9x'
000115F0	3F800000 40A00000			2276	DC XL16'3F80000040A000003F80000040A00000'
00011600	C4C9C5C2 D940D996			2277	DC CL48'DIEBR Rounding case 10a'
00011630	BF800000 41200000			2278	DC XL16'BF80000041200000BF80000041200000'
00011640	C4C9C5C2 D940D996			2279	DC CL48'DIEBR Rounding case 10b'
00011670	3F800000 41100000			2280	DC XL16'3F800000411000003F80000041100000'
00011680	C4C9C5C2 D940D996			2281	DC CL48'DIEBR Rounding case 10c'
000116B0	BF800000 41200000			2282	DC XL16'BF80000041200000BF80000041200000'
000116C0	C4C9C5C2 D940D996			2283	DC CL48'DIEBR Rounding case 10d'
000116F0	3F800000 41100000			2284	DC XL16'3F800000411000003F80000041100000'
00011700	C4C9C5C2 D940D996			2285	DC CL48'DIEBR Rounding case 10e'
00011730	BF800000 41200000			2286	DC XL16'BF80000041200000BF80000041200000'
00011740	C4C9C5C2 D940D996			2287	DC CL48'DIEBR Rounding case 10f'
00011770	3F800000 41100000			2288	DC XL16'3F800000411000003F80000041100000'
00011780	C4C9C5C2 D940D996			2289	DC CL48'DIEBR Rounding case 10g'
000117B0	BF800000 41200000			2290	DC XL16'BF80000041200000BF80000041200000'
000117C0	C4C9C5C2 D940D996			2291	DC CL48'DIEBR Rounding case 10h'
000117F0	3F800000 41100000			2292	DC XL16'3F800000411000003F80000041100000'
00011800	C4C9C5C2 D940D996			2293	DC CL48'DIEBR Rounding case 10i'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00011830	BF800000 41200000			2294 DC XL16'BF80000041200000BF80000041200000'
00011840	C4C9C5C2 D940D996			2295 DC CL48'DIEBR Rounding case 10j'
00011870	3F800000 41100000			2296 DC XL16'3F800000411000003F80000041100000'
00011880	C4C9C5C2 D940D996			2297 DC CL48'DIEBR Rounding case 10k'
000118B0	BF800000 41200000			2298 DC XL16'BF80000041200000BF80000041200000'
000118C0	C4C9C5C2 D940D996			2299 DC CL48'DIEBR Rounding case 10l'
000118F0	3F800000 41100000			2300 DC XL16'3F800000411000003F80000041100000'
00011900	C4C9C5C2 D940D996			2301 DC CL48'DIEBR Rounding case 10m'
00011930	BF800000 41200000			2302 DC XL16'BF80000041200000BF80000041200000'
00011940	C4C9C5C2 D940D996			2303 DC CL48'DIEBR Rounding case 10n'
00011970	3F800000 41100000			2304 DC XL16'3F800000411000003F80000041100000'
00011980	C4C9C5C2 D940D996			2305 DC CL48'DIEBR Rounding case 10o'
000119B0	BF800000 41200000			2306 DC XL16'BF80000041200000BF80000041200000'
000119C0	C4C9C5C2 D940D996			2307 DC CL48'DIEBR Rounding case 10p'
000119F0	3F800000 41100000			2308 DC XL16'3F800000411000003F80000041100000'
00011A00	C4C9C5C2 D940D996			2309 DC CL48'DIEBR Rounding case 10q'
00011A30	BF800000 41200000			2310 DC XL16'BF80000041200000BF80000041200000'
00011A40	C4C9C5C2 D940D996			2311 DC CL48'DIEBR Rounding case 10r'
00011A70	3F800000 41100000			2312 DC XL16'3F800000411000003F80000041100000'
00011A80	C4C9C5C2 D940D996			2313 DC CL48'DIEBR Rounding case 10s'
00011AB0	BF800000 41200000			2314 DC XL16'BF80000041200000BF80000041200000'
00011AC0	C4C9C5C2 D940D996			2315 DC CL48'DIEBR Rounding case 10t'
00011AF0	3F800000 41100000			2316 DC XL16'3F800000411000003F80000041100000'
00011B00	C4C9C5C2 D940D996			2317 DC CL48'DIEBR Rounding case 10u'
00011B30	BF800000 41200000			2318 DC XL16'BF80000041200000BF80000041200000'
00011B40	C4C9C5C2 D940D996			2319 DC CL48'DIEBR Rounding case 10v'
00011B70	3F800000 41100000			2320 DC XL16'3F800000411000003F80000041100000'
00011B80	C4C9C5C2 D940D996			2321 DC CL48'DIEBR Rounding case 10w'
00011BB0	BF800000 41200000			2322 DC XL16'BF80000041200000BF80000041200000'
00011BC0	C4C9C5C2 D940D996			2323 DC CL48'DIEBR Rounding case 10x'
00011BF0	3F800000 41100000			2324 DC XL16'3F800000411000003F80000041100000'
00011C00	C4C9C5C2 D940D996			2325 DC CL48'DIEBR Rounding case 11'
00011C30	00000000 3F800000			2326 DC XL16'000000003F800000000000003F800000'
00011C40	C4C9C5C2 D940D996			2327 DC CL48'DIEBR Rounding case 11'
00011C70	00000000 3F800000			2328 DC XL16'000000003F800000000000003F800000'
00011C80	C4C9C5C2 D940D996			2329 DC CL48'DIEBR Rounding case 11'
00011CB0	00000000 3F800000			2330 DC XL16'000000003F800000000000003F800000'
00011CC0	C4C9C5C2 D940D996			2331 DC CL48'DIEBR Rounding case 11'
00011CF0	00000000 3F800000			2332 DC XL16'000000003F800000000000003F800000'
00011D00	C4C9C5C2 D940D996			2333 DC CL48'DIEBR Rounding case 11'
00011D30	00000000 3F800000			2334 DC XL16'000000003F800000000000003F800000'
00011D40	C4C9C5C2 D940D996			2335 DC CL48'DIEBR Rounding case 11'
00011D70	00000000 3F800000			2336 DC XL16'000000003F800000000000003F800000'
00011D80	C4C9C5C2 D940D996			2337 DC CL48'DIEBR Rounding case 11'
00011DB0	00000000 3F800000			2338 DC XL16'000000003F800000000000003F800000'
00011DC0	C4C9C5C2 D940D996			2339 DC CL48'DIEBR Rounding case 11'
00011DF0	00000000 3F800000			2340 DC XL16'000000003F800000000000003F800000'
00011E00	C4C9C5C2 D940D996			2341 DC CL48'DIEBR Rounding case 11'
00011E30	00000000 3F800000			2342 DC XL16'000000003F800000000000003F800000'
00011E40	C4C9C5C2 D940D996			2343 DC CL48'DIEBR Rounding case 11'
00011E70	00000000 3F800000			2344 DC XL16'000000003F800000000000003F800000'
00011E80	C4C9C5C2 D940D996			2345 DC CL48'DIEBR Rounding case 11'
00011EB0	00000000 3F800000			2346 DC XL16'000000003F800000000000003F800000'
00011EC0	C4C9C5C2 D940D996			2347 DC CL48'DIEBR Rounding case 11'
00011EF0	00000000 3F800000			2348 DC XL16'000000003F800000000000003F800000'
00011F00	C4C9C5C2 D940D996			2349 DC CL48'DIEBR Rounding case 11'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00011F30	00000000 3F800000			2350	DC XL16'000000003F800000000000003F800000'
00011F40	C4C9C5C2 D940D996			2351	DC CL48'DIEBR Rounding case 11'
00011F70	00000000 3F800000			2352	DC XL16'000000003F800000000000003F800000'
00011F80	C4C9C5C2 D940D996			2353	DC CL48'DIEBR Rounding case 11'
00011FB0	00000000 3F800000			2354	DC XL16'000000003F800000000000003F800000'
00011FC0	C4C9C5C2 D940D996			2355	DC CL48'DIEBR Rounding case 11'
00011FF0	00000000 3F800000			2356	DC XL16'000000003F800000000000003F800000'
00012000	C4C9C5C2 D940D996			2357	DC CL48'DIEBR Rounding case 11'
00012030	00000000 3F800000			2358	DC XL16'000000003F800000000000003F800000'
00012040	C4C9C5C2 D940D996			2359	DC CL48'DIEBR Rounding case 11'
00012070	00000000 3F800000			2360	DC XL16'000000003F800000000000003F800000'
00012080	C4C9C5C2 D940D996			2361	DC CL48'DIEBR Rounding case 11'
000120B0	00000000 3F800000			2362	DC XL16'000000003F800000000000003F800000'
000120C0	C4C9C5C2 D940D996			2363	DC CL48'DIEBR Rounding case 11'
000120F0	00000000 3F800000			2364	DC XL16'000000003F800000000000003F800000'
00012100	C4C9C5C2 D940D996			2365	DC CL48'DIEBR Rounding case 11'
00012130	00000000 3F800000			2366	DC XL16'000000003F800000000000003F800000'
00012140	C4C9C5C2 D940D996			2367	DC CL48'DIEBR Rounding case 11'
00012170	00000000 3F800000			2368	DC XL16'000000003F800000000000003F800000'
00012180	C4C9C5C2 D940D996			2369	DC CL48'DIEBR Rounding case 11'
000121B0	00000000 3F800000			2370	DC XL16'000000003F800000000000003F800000'
000121C0	C4C9C5C2 D940D996			2371	DC CL48'DIEBR Rounding case 11'
000121F0	00000000 3F800000			2372	DC XL16'000000003F800000000000003F800000'
00012200	C4C9C5C2 D940D996			2373	DC CL48'DIEBR Rounding case 12'
00012230	C0000000 3F800000			2374	DC XL16'C00000003F800000C00000003F800000'
00012240	C4C9C5C2 D940D996			2375	DC CL48'DIEBR Rounding case 12'
00012270	C0000000 3F800000			2376	DC XL16'C00000003F800000C00000003F800000'
00012280	C4C9C5C2 D940D996			2377	DC CL48'DIEBR Rounding case 12'
000122B0	C0000000 3F800000			2378	DC XL16'C00000003F800000C00000003F800000'
000122C0	C4C9C5C2 D940D996			2379	DC CL48'DIEBR Rounding case 12'
000122F0	40400000 00000000			2380	DC XL16'40400000000000004040000000000000'
00012300	C4C9C5C2 D940D996			2381	DC CL48'DIEBR Rounding case 12'
00012330	C0000000 3F800000			2382	DC XL16'C00000003F800000C00000003F800000'
00012340	C4C9C5C2 D940D996			2383	DC CL48'DIEBR Rounding case 12'
00012370	40400000 00000000			2384	DC XL16'40400000000000004040000000000000'
00012380	C4C9C5C2 D940D996			2385	DC CL48'DIEBR Rounding case 12'
000123B0	C0000000 3F800000			2386	DC XL16'C00000003F800000C00000003F800000'
000123C0	C4C9C5C2 D940D996			2387	DC CL48'DIEBR Rounding case 12'
000123F0	C0000000 3F800000			2388	DC XL16'C00000003F800000C00000003F800000'
00012400	C4C9C5C2 D940D996			2389	DC CL48'DIEBR Rounding case 12'
00012430	C0000000 3F800000			2390	DC XL16'C00000003F800000C00000003F800000'
00012440	C4C9C5C2 D940D996			2391	DC CL48'DIEBR Rounding case 12'
00012470	40400000 00000000			2392	DC XL16'40400000000000004040000000000000'
00012480	C4C9C5C2 D940D996			2393	DC CL48'DIEBR Rounding case 12'
000124B0	C0000000 3F800000			2394	DC XL16'C00000003F800000C00000003F800000'
000124C0	C4C9C5C2 D940D996			2395	DC CL48'DIEBR Rounding case 12'
000124F0	40400000 00000000			2396	DC XL16'40400000000000004040000000000000'
00012500	C4C9C5C2 D940D996			2397	DC CL48'DIEBR Rounding case 12'
00012530	C0000000 3F800000			2398	DC XL16'C00000003F800000C00000003F800000'
00012540	C4C9C5C2 D940D996			2399	DC CL48'DIEBR Rounding case 12'
00012570	C0000000 3F800000			2400	DC XL16'C00000003F800000C00000003F800000'
00012580	C4C9C5C2 D940D996			2401	DC CL48'DIEBR Rounding case 12'
000125B0	C0000000 3F800000			2402	DC XL16'C00000003F800000C00000003F800000'
000125C0	C4C9C5C2 D940D996			2403	DC CL48'DIEBR Rounding case 12'
000125F0	40400000 00000000			2404	DC XL16'40400000000000004040000000000000'
00012600	C4C9C5C2 D940D996			2405	DC CL48'DIEBR Rounding case 12'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00012630	C0000000 3F800000			2406 DC XL16'C00000003F800000C00000003F800000'
00012640	C4C9C5C2 D940D996			2407 DC CL48'DIEBR Rounding case 12'
00012670	40400000 00000000			2408 DC XL16'40400000000000004040000000000000'
00012680	C4C9C5C2 D940D996			2409 DC CL48'DIEBR Rounding case 12'
000126B0	C0000000 3F800000			2410 DC XL16'C00000003F800000C00000003F800000'
000126C0	C4C9C5C2 D940D996			2411 DC CL48'DIEBR Rounding case 12'
000126F0	C0000000 3F800000			2412 DC XL16'C00000003F800000C00000003F800000'
00012700	C4C9C5C2 D940D996			2413 DC CL48'DIEBR Rounding case 12'
00012730	C0000000 3F800000			2414 DC XL16'C00000003F800000C00000003F800000'
00012740	C4C9C5C2 D940D996			2415 DC CL48'DIEBR Rounding case 12'
00012770	40400000 00000000			2416 DC XL16'40400000000000004040000000000000'
00012780	C4C9C5C2 D940D996			2417 DC CL48'DIEBR Rounding case 12'
000127B0	C0000000 3F800000			2418 DC XL16'C00000003F800000C00000003F800000'
000127C0	C4C9C5C2 D940D996			2419 DC CL48'DIEBR Rounding case 12'
000127F0	40400000 00000000			2420 DC XL16'40400000000000004040000000000000'
		00000120	00000001	2421 SBFPRMO_NUM EQU (*-SBFPRMO_GOOD)/64
				2422 *
				2423 *
		00012800	00000001	2424 SBFPRMOF_GOOD EQU *
00012800	C4C9C5C2 D940D996			2425 DC CL48'DIEBR Rounding FPCR 1ab'
00012830	00000000 F8000000			2426 DC XL16'00000000F800000000000000F8000000'
00012840	C4C9C5C2 D940D996			2427 DC CL48'DIEBR Rounding FPCR 1cd'
00012870	00000000 F8000000			2428 DC XL16'00000000F800000000000000F8000000'
00012880	C4C9C5C2 D940D996			2429 DC CL48'DIEBR Rounding FPCR 1ef'
000128B0	00000000 F8000000			2430 DC XL16'00000000F800000000000000F8000000'
000128C0	C4C9C5C2 D940D996			2431 DC CL48'DIEBR Rounding FPCR 1gh'
000128F0	00000000 F8000000			2432 DC XL16'00000000F800000000000000F8000000'
00012900	C4C9C5C2 D940D996			2433 DC CL48'DIEBR Rounding FPCR 1ij'
00012930	00000000 F8000000			2434 DC XL16'00000000F800000000000000F8000000'
00012940	C4C9C5C2 D940D996			2435 DC CL48'DIEBR Rounding FPCR 1kl'
00012970	00000000 F8000000			2436 DC XL16'00000000F800000000000000F8000000'
00012980	C4C9C5C2 D940D996			2437 DC CL48'DIEBR Rounding FPCR 1mn'
000129B0	00000000 F8000000			2438 DC XL16'00000000F800000000000000F8000000'
000129C0	C4C9C5C2 D940D996			2439 DC CL48'DIEBR Rounding FPCR 1op'
000129F0	00000000 F8000000			2440 DC XL16'00000000F800000000000000F8000000'
00012A00	C4C9C5C2 D940D996			2441 DC CL48'DIEBR Rounding FPCR 1qr'
00012A30	00000000 F8000000			2442 DC XL16'00000000F800000000000000F8000000'
00012A40	C4C9C5C2 D940D996			2443 DC CL48'DIEBR Rounding FPCR 1st'
00012A70	00000000 F8000000			2444 DC XL16'00000000F800000000000000F8000000'
00012A80	C4C9C5C2 D940D996			2445 DC CL48'DIEBR Rounding FPCR 1uv'
00012AB0	00000000 F8000000			2446 DC XL16'00000000F800000000000000F8000000'
00012AC0	C4C9C5C2 D940D996			2447 DC CL48'DIEBR Rounding FPCR 1wx'
00012AF0	00000000 F8000000			2448 DC XL16'00000000F800000000000000F8000000'
00012B00	C4C9C5C2 D940D996			2449 DC CL48'DIEBR Rounding FPCR 2ab'
00012B30	00000000 F8000000			2450 DC XL16'00000000F800000000000000F8000000'
00012B40	C4C9C5C2 D940D996			2451 DC CL48'DIEBR Rounding FPCR 2cd'
00012B70	00000000 F8000000			2452 DC XL16'00000000F800000000000000F8000000'
00012B80	C4C9C5C2 D940D996			2453 DC CL48'DIEBR Rounding FPCR 2ef'
00012BB0	00000000 F8000000			2454 DC XL16'00000000F800000000000000F8000000'
00012BC0	C4C9C5C2 D940D996			2455 DC CL48'DIEBR Rounding FPCR 2gh'
00012BF0	00000000 F8000000			2456 DC XL16'00000000F800000000000000F8000000'
00012C00	C4C9C5C2 D940D996			2457 DC CL48'DIEBR Rounding FPCR 2ij'
00012C30	00000000 F8000000			2458 DC XL16'00000000F800000000000000F8000000'
00012C40	C4C9C5C2 D940D996			2459 DC CL48'DIEBR Rounding FPCR 2kl'
00012C70	00000000 F8000000			2460 DC XL16'00000000F800000000000000F8000000'
00012C80	C4C9C5C2 D940D996			2461 DC CL48'DIEBR Rounding FPCR 2mn'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00012CB0	00000000 F8000000			2462 DC XL16'00000000F800000000000000F8000000'
00012CC0	C4C9C5C2 D940D996			2463 DC CL48'DIEBR Rounding FPCR 2op'
00012CF0	00000000 F8000000			2464 DC XL16'00000000F800000000000000F8000000'
00012D00	C4C9C5C2 D940D996			2465 DC CL48'DIEBR Rounding FPCR 2qr'
00012D30	00000000 F8000000			2466 DC XL16'00000000F800000000000000F8000000'
00012D40	C4C9C5C2 D940D996			2467 DC CL48'DIEBR Rounding FPCR 2st'
00012D70	00000000 F8000000			2468 DC XL16'00000000F800000000000000F8000000'
00012D80	C4C9C5C2 D940D996			2469 DC CL48'DIEBR Rounding FPCR 2uv'
00012DB0	00000000 F8000000			2470 DC XL16'00000000F800000000000000F8000000'
00012DC0	C4C9C5C2 D940D996			2471 DC CL48'DIEBR Rounding FPCR 2wx'
00012DF0	00000000 F8000000			2472 DC XL16'00000000F800000000000000F8000000'
00012E00	C4C9C5C2 D940D996			2473 DC CL48'DIEBR Rounding FPCR 3ab'
00012E30	00000000 F8000000			2474 DC XL16'00000000F800000000000000F8000000'
00012E40	C4C9C5C2 D940D996			2475 DC CL48'DIEBR Rounding FPCR 3cd'
00012E70	00000000 F8000000			2476 DC XL16'00000000F800000000000000F8000000'
00012E80	C4C9C5C2 D940D996			2477 DC CL48'DIEBR Rounding FPCR 3ef'
00012EB0	00000000 F8000000			2478 DC XL16'00000000F800000000000000F8000000'
00012EC0	C4C9C5C2 D940D996			2479 DC CL48'DIEBR Rounding FPCR 3gh'
00012EF0	00000000 F8000000			2480 DC XL16'00000000F800000000000000F8000000'
00012F00	C4C9C5C2 D940D996			2481 DC CL48'DIEBR Rounding FPCR 3ij'
00012F30	00000000 F8000000			2482 DC XL16'00000000F800000000000000F8000000'
00012F40	C4C9C5C2 D940D996			2483 DC CL48'DIEBR Rounding FPCR 3kl'
00012F70	00000000 F8000000			2484 DC XL16'00000000F800000000000000F8000000'
00012F80	C4C9C5C2 D940D996			2485 DC CL48'DIEBR Rounding FPCR 3mn'
00012FB0	00000000 F8000000			2486 DC XL16'00000000F800000000000000F8000000'
00012FC0	C4C9C5C2 D940D996			2487 DC CL48'DIEBR Rounding FPCR 3op'
00012FF0	00000000 F8000000			2488 DC XL16'00000000F800000000000000F8000000'
00013000	C4C9C5C2 D940D996			2489 DC CL48'DIEBR Rounding FPCR 3qr'
00013030	00000000 F8000000			2490 DC XL16'00000000F800000000000000F8000000'
00013040	C4C9C5C2 D940D996			2491 DC CL48'DIEBR Rounding FPCR 3st'
00013070	00000000 F8000000			2492 DC XL16'00000000F800000000000000F8000000'
00013080	C4C9C5C2 D940D996			2493 DC CL48'DIEBR Rounding FPCR 3uv'
000130B0	00000000 F8000000			2494 DC XL16'00000000F800000000000000F8000000'
000130C0	C4C9C5C2 D940D996			2495 DC CL48'DIEBR Rounding FPCR 3wx'
000130F0	00000000 F8000000			2496 DC XL16'00000000F800000000000000F8000000'
00013100	C4C9C5C2 D940D996			2497 DC CL48'DIEBR Rounding FPCR 4ab'
00013130	00000000 F8000000			2498 DC XL16'00000000F800000000000000F8000000'
00013140	C4C9C5C2 D940D996			2499 DC CL48'DIEBR Rounding FPCR 4cd'
00013170	00000000 F8000000			2500 DC XL16'00000000F800000000000000F8000000'
00013180	C4C9C5C2 D940D996			2501 DC CL48'DIEBR Rounding FPCR 4ef'
000131B0	00000000 F8000000			2502 DC XL16'00000000F800000000000000F8000000'
000131C0	C4C9C5C2 D940D996			2503 DC CL48'DIEBR Rounding FPCR 4gh'
000131F0	00000000 F8000000			2504 DC XL16'00000000F800000000000000F8000000'
00013200	C4C9C5C2 D940D996			2505 DC CL48'DIEBR Rounding FPCR 4ij'
00013230	00000000 F8000000			2506 DC XL16'00000000F800000000000000F8000000'
00013240	C4C9C5C2 D940D996			2507 DC CL48'DIEBR Rounding FPCR 4kl'
00013270	00000000 F8000000			2508 DC XL16'00000000F800000000000000F8000000'
00013280	C4C9C5C2 D940D996			2509 DC CL48'DIEBR Rounding FPCR 4mn'
000132B0	00000000 F8000000			2510 DC XL16'00000000F800000000000000F8000000'
000132C0	C4C9C5C2 D940D996			2511 DC CL48'DIEBR Rounding FPCR 4op'
000132F0	00000000 F8000000			2512 DC XL16'00000000F800000000000000F8000000'
00013300	C4C9C5C2 D940D996			2513 DC CL48'DIEBR Rounding FPCR 4qr'
00013330	00000000 F8000000			2514 DC XL16'00000000F800000000000000F8000000'
00013340	C4C9C5C2 D940D996			2515 DC CL48'DIEBR Rounding FPCR 4st'
00013370	00000000 F8000000			2516 DC XL16'00000000F800000000000000F8000000'
00013380	C4C9C5C2 D940D996			2517 DC CL48'DIEBR Rounding FPCR 4uv'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
000133B0	00000000 F8000000			2518	DC XL16'00000000F800000000000000F8000000'
000133C0	C4C9C5C2 D940D996			2519	DC CL48'DIEBR Rounding FPCR 4wx'
000133F0	00000000 F8000000			2520	DC XL16'00000000F800000000000000F8000000'
00013400	C4C9C5C2 D940D996			2521	DC CL48'DIEBR Rounding FPCR 5ab'
00013430	00000000 F8000000			2522	DC XL16'00000000F800000000000000F8000000'
00013440	C4C9C5C2 D940D996			2523	DC CL48'DIEBR Rounding FPCR 5cd'
00013470	00000000 F8000000			2524	DC XL16'00000000F800000000000000F8000000'
00013480	C4C9C5C2 D940D996			2525	DC CL48'DIEBR Rounding FPCR 5ef'
000134B0	00000000 F8000000			2526	DC XL16'00000000F800000000000000F8000000'
000134C0	C4C9C5C2 D940D996			2527	DC CL48'DIEBR Rounding FPCR 5gh'
000134F0	00000000 F8000000			2528	DC XL16'00000000F800000000000000F8000000'
00013500	C4C9C5C2 D940D996			2529	DC CL48'DIEBR Rounding FPCR 5ij'
00013530	00000000 F8000000			2530	DC XL16'00000000F800000000000000F8000000'
00013540	C4C9C5C2 D940D996			2531	DC CL48'DIEBR Rounding FPCR 5kl'
00013570	00000000 F8000000			2532	DC XL16'00000000F800000000000000F8000000'
00013580	C4C9C5C2 D940D996			2533	DC CL48'DIEBR Rounding FPCR 5mn'
000135B0	00000000 F8000000			2534	DC XL16'00000000F800000000000000F8000000'
000135C0	C4C9C5C2 D940D996			2535	DC CL48'DIEBR Rounding FPCR 5op'
000135F0	00000000 F8000000			2536	DC XL16'00000000F800000000000000F8000000'
00013600	C4C9C5C2 D940D996			2537	DC CL48'DIEBR Rounding FPCR 5qr'
00013630	00000000 F8000000			2538	DC XL16'00000000F800000000000000F8000000'
00013640	C4C9C5C2 D940D996			2539	DC CL48'DIEBR Rounding FPCR 5st'
00013670	00000000 F8000000			2540	DC XL16'00000000F800000000000000F8000000'
00013680	C4C9C5C2 D940D996			2541	DC CL48'DIEBR Rounding FPCR 5uv'
000136B0	00000000 F8000000			2542	DC XL16'00000000F800000000000000F8000000'
000136C0	C4C9C5C2 D940D996			2543	DC CL48'DIEBR Rounding FPCR 5wx'
000136F0	00000000 F8000000			2544	DC XL16'00000000F800000000000000F8000000'
00013700	C4C9C5C2 D940D996			2545	DC CL48'DIEBR Rounding FPCR 6ab'
00013730	00000000 F8000000			2546	DC XL16'00000000F800000000000000F8000000'
00013740	C4C9C5C2 D940D996			2547	DC CL48'DIEBR Rounding FPCR 6cd'
00013770	00000000 F8000000			2548	DC XL16'00000000F800000000000000F8000000'
00013780	C4C9C5C2 D940D996			2549	DC CL48'DIEBR Rounding FPCR 6ef'
000137B0	00000000 F8000000			2550	DC XL16'00000000F800000000000000F8000000'
000137C0	C4C9C5C2 D940D996			2551	DC CL48'DIEBR Rounding FPCR 6gh'
000137F0	00000000 F8000000			2552	DC XL16'00000000F800000000000000F8000000'
00013800	C4C9C5C2 D940D996			2553	DC CL48'DIEBR Rounding FPCR 6ij'
00013830	00000000 F8000000			2554	DC XL16'00000000F800000000000000F8000000'
00013840	C4C9C5C2 D940D996			2555	DC CL48'DIEBR Rounding FPCR 6kl'
00013870	00000000 F8000000			2556	DC XL16'00000000F800000000000000F8000000'
00013880	C4C9C5C2 D940D996			2557	DC CL48'DIEBR Rounding FPCR 6mn'
000138B0	00000000 F8000000			2558	DC XL16'00000000F800000000000000F8000000'
000138C0	C4C9C5C2 D940D996			2559	DC CL48'DIEBR Rounding FPCR 6op'
000138F0	00000000 F8000000			2560	DC XL16'00000000F800000000000000F8000000'
00013900	C4C9C5C2 D940D996			2561	DC CL48'DIEBR Rounding FPCR 6qr'
00013930	00000000 F8000000			2562	DC XL16'00000000F800000000000000F8000000'
00013940	C4C9C5C2 D940D996			2563	DC CL48'DIEBR Rounding FPCR 6st'
00013970	00000000 F8000000			2564	DC XL16'00000000F800000000000000F8000000'
00013980	C4C9C5C2 D940D996			2565	DC CL48'DIEBR Rounding FPCR 6uv'
000139B0	00000000 F8000000			2566	DC XL16'00000000F800000000000000F8000000'
000139C0	C4C9C5C2 D940D996			2567	DC CL48'DIEBR Rounding FPCR 6wx'
000139F0	00000000 F8000000			2568	DC XL16'00000000F800000000000000F8000000'
00013A00	C4C9C5C2 D940D996			2569	DC CL48'DIEBR Rounding FPCR 7ab'
00013A30	00000000 F8000000			2570	DC XL16'00000000F800000000000000F8000000'
00013A40	C4C9C5C2 D940D996			2571	DC CL48'DIEBR Rounding FPCR 7cd'
00013A70	00000000 F8000000			2572	DC XL16'00000000F800000000000000F8000000'
00013A80	C4C9C5C2 D940D996			2573	DC CL48'DIEBR Rounding FPCR 7ef'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00013AB0	00000000 F8000000			2574	DC XL16'00000000F800000000000000F8000000'
00013AC0	C4C9C5C2 D940D996			2575	DC CL48'DIEBR Rounding FPCR 7gh'
00013AF0	00000000 F8000000			2576	DC XL16'00000000F800000000000000F8000000'
00013B00	C4C9C5C2 D940D996			2577	DC CL48'DIEBR Rounding FPCR 7ij'
00013B30	00000000 F8000000			2578	DC XL16'00000000F800000000000000F8000000'
00013B40	C4C9C5C2 D940D996			2579	DC CL48'DIEBR Rounding FPCR 7kl'
00013B70	00000000 F8000000			2580	DC XL16'00000000F800000000000000F8000000'
00013B80	C4C9C5C2 D940D996			2581	DC CL48'DIEBR Rounding FPCR 7mn'
00013BB0	00000000 F8000000			2582	DC XL16'00000000F800000000000000F8000000'
00013BC0	C4C9C5C2 D940D996			2583	DC CL48'DIEBR Rounding FPCR 7op'
00013BF0	00000000 F8000000			2584	DC XL16'00000000F800000000000000F8000000'
00013C00	C4C9C5C2 D940D996			2585	DC CL48'DIEBR Rounding FPCR 7qr'
00013C30	00000000 F8000000			2586	DC XL16'00000000F800000000000000F8000000'
00013C40	C4C9C5C2 D940D996			2587	DC CL48'DIEBR Rounding FPCR 7st'
00013C70	00000000 F8000000			2588	DC XL16'00000000F800000000000000F8000000'
00013C80	C4C9C5C2 D940D996			2589	DC CL48'DIEBR Rounding FPCR 7uv'
00013CB0	00000000 F8000000			2590	DC XL16'00000000F800000000000000F8000000'
00013CC0	C4C9C5C2 D940D996			2591	DC CL48'DIEBR Rounding FPCR 7wx'
00013CF0	00000000 F8000000			2592	DC XL16'00000000F800000000000000F8000000'
00013D00	C4C9C5C2 D940D996			2593	DC CL48'DIEBR Rounding FPCR 8ab'
00013D30	00000000 F8000000			2594	DC XL16'00000000F800000000000000F8000000'
00013D40	C4C9C5C2 D940D996			2595	DC CL48'DIEBR Rounding FPCR 8cd'
00013D70	00000000 F8000000			2596	DC XL16'00000000F800000000000000F8000000'
00013D80	C4C9C5C2 D940D996			2597	DC CL48'DIEBR Rounding FPCR 8ef'
00013DB0	00000000 F8000000			2598	DC XL16'00000000F800000000000000F8000000'
00013DC0	C4C9C5C2 D940D996			2599	DC CL48'DIEBR Rounding FPCR 8gh'
00013DF0	00000000 F8000000			2600	DC XL16'00000000F800000000000000F8000000'
00013E00	C4C9C5C2 D940D996			2601	DC CL48'DIEBR Rounding FPCR 8ij'
00013E30	00000000 F8000000			2602	DC XL16'00000000F800000000000000F8000000'
00013E40	C4C9C5C2 D940D996			2603	DC CL48'DIEBR Rounding FPCR 8kl'
00013E70	00000000 F8000000			2604	DC XL16'00000000F800000000000000F8000000'
00013E80	C4C9C5C2 D940D996			2605	DC CL48'DIEBR Rounding FPCR 8mn'
00013EB0	00000000 F8000000			2606	DC XL16'00000000F800000000000000F8000000'
00013EC0	C4C9C5C2 D940D996			2607	DC CL48'DIEBR Rounding FPCR 8op'
00013EF0	00000000 F8000000			2608	DC XL16'00000000F800000000000000F8000000'
00013F00	C4C9C5C2 D940D996			2609	DC CL48'DIEBR Rounding FPCR 8qr'
00013F30	00000000 F8000000			2610	DC XL16'00000000F800000000000000F8000000'
00013F40	C4C9C5C2 D940D996			2611	DC CL48'DIEBR Rounding FPCR 8st'
00013F70	00000000 F8000000			2612	DC XL16'00000000F800000000000000F8000000'
00013F80	C4C9C5C2 D940D996			2613	DC CL48'DIEBR Rounding FPCR 8uv'
00013FB0	00000000 F8000000			2614	DC XL16'00000000F800000000000000F8000000'
00013FC0	C4C9C5C2 D940D996			2615	DC CL48'DIEBR Rounding FPCR 8wx'
00013FF0	00000000 F8000000			2616	DC XL16'00000000F800000000000000F8000000'
00014000	C4C9C5C2 D940D996			2617	DC CL48'DIEBR Rounding FPCR 9ab'
00014030	00000000 F8000000			2618	DC XL16'00000000F800000000000000F8000000'
00014040	C4C9C5C2 D940D996			2619	DC CL48'DIEBR Rounding FPCR 9cd'
00014070	00000000 F8000000			2620	DC XL16'00000000F800000000000000F8000000'
00014080	C4C9C5C2 D940D996			2621	DC CL48'DIEBR Rounding FPCR 9ef'
000140B0	00000000 F8000000			2622	DC XL16'00000000F800000000000000F8000000'
000140C0	C4C9C5C2 D940D996			2623	DC CL48'DIEBR Rounding FPCR 9gh'
000140F0	00000000 F8000000			2624	DC XL16'00000000F800000000000000F8000000'
00014100	C4C9C5C2 D940D996			2625	DC CL48'DIEBR Rounding FPCR 9ij'
00014130	00000000 F8000000			2626	DC XL16'00000000F800000000000000F8000000'
00014140	C4C9C5C2 D940D996			2627	DC CL48'DIEBR Rounding FPCR 9kl'
00014170	00000000 F8000000			2628	DC XL16'00000000F800000000000000F8000000'
00014180	C4C9C5C2 D940D996			2629	DC CL48'DIEBR Rounding FPCR 9mn'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
000141B0	00000000 F8000000			2630	DC XL16'00000000F800000000000000F8000000'
000141C0	C4C9C5C2 D940D996			2631	DC CL48'DIEBR Rounding FPCR 9op'
000141F0	00000000 F8000000			2632	DC XL16'00000000F800000000000000F8000000'
00014200	C4C9C5C2 D940D996			2633	DC CL48'DIEBR Rounding FPCR 9qr'
00014230	00000000 F8000000			2634	DC XL16'00000000F800000000000000F8000000'
00014240	C4C9C5C2 D940D996			2635	DC CL48'DIEBR Rounding FPCR 9st'
00014270	00000000 F8000000			2636	DC XL16'00000000F800000000000000F8000000'
00014280	C4C9C5C2 D940D996			2637	DC CL48'DIEBR Rounding FPCR 9uv'
000142B0	00000000 F8000000			2638	DC XL16'00000000F800000000000000F8000000'
000142C0	C4C9C5C2 D940D996			2639	DC CL48'DIEBR Rounding FPCR 9wx'
000142F0	00000000 F8000000			2640	DC XL16'00000000F800000000000000F8000000'
00014300	C4C9C5C2 D940D996			2641	DC CL48'DIEBR Rounding FPCR 10ab'
00014330	00000000 F8000000			2642	DC XL16'00000000F800000000000000F8000000'
00014340	C4C9C5C2 D940D996			2643	DC CL48'DIEBR Rounding FPCR 10cd'
00014370	00000000 F8000000			2644	DC XL16'00000000F800000000000000F8000000'
00014380	C4C9C5C2 D940D996			2645	DC CL48'DIEBR Rounding FPCR 10ef'
000143B0	00000000 F8000000			2646	DC XL16'00000000F800000000000000F8000000'
000143C0	C4C9C5C2 D940D996			2647	DC CL48'DIEBR Rounding FPCR 10gh'
000143F0	00000000 F8000000			2648	DC XL16'00000000F800000000000000F8000000'
00014400	C4C9C5C2 D940D996			2649	DC CL48'DIEBR Rounding FPCR 10ij'
00014430	00000000 F8000000			2650	DC XL16'00000000F800000000000000F8000000'
00014440	C4C9C5C2 D940D996			2651	DC CL48'DIEBR Rounding FPCR 10kl'
00014470	00000000 F8000000			2652	DC XL16'00000000F800000000000000F8000000'
00014480	C4C9C5C2 D940D996			2653	DC CL48'DIEBR Rounding FPCR 10mn'
000144B0	00000000 F8000000			2654	DC XL16'00000000F800000000000000F8000000'
000144C0	C4C9C5C2 D940D996			2655	DC CL48'DIEBR Rounding FPCR 10op'
000144F0	00000000 F8000000			2656	DC XL16'00000000F800000000000000F8000000'
00014500	C4C9C5C2 D940D996			2657	DC CL48'DIEBR Rounding FPCR 10qr'
00014530	00000000 F8000000			2658	DC XL16'00000000F800000000000000F8000000'
00014540	C4C9C5C2 D940D996			2659	DC CL48'DIEBR Rounding FPCR 10st'
00014570	00000000 F8000000			2660	DC XL16'00000000F800000000000000F8000000'
00014580	C4C9C5C2 D940D996			2661	DC CL48'DIEBR Rounding FPCR 10uv'
000145B0	00000000 F8000000			2662	DC XL16'00000000F800000000000000F8000000'
000145C0	C4C9C5C2 D940D996			2663	DC CL48'DIEBR Rounding FPCR 10wx'
000145F0	00000000 F8000000			2664	DC XL16'00000000F800000000000000F8000000'
00014600	C4C9C5C2 D940D996			2665	DC CL48'DIEBR Rounding FPCR 11ab'
00014630	00000000 F8000000			2666	DC XL16'00000000F800000000000000F8000000'
00014640	C4C9C5C2 D940D996			2667	DC CL48'DIEBR Rounding FPCR 11cd'
00014670	00000000 F8000000			2668	DC XL16'00000000F800000000000000F8000000'
00014680	C4C9C5C2 D940D996			2669	DC CL48'DIEBR Rounding FPCR 11ef'
000146B0	00000000 F8000000			2670	DC XL16'00000000F800000000000000F8000000'
000146C0	C4C9C5C2 D940D996			2671	DC CL48'DIEBR Rounding FPCR 11gh'
000146F0	00000000 F8000000			2672	DC XL16'00000000F800000000000000F8000000'
00014700	C4C9C5C2 D940D996			2673	DC CL48'DIEBR Rounding FPCR 11ij'
00014730	00000000 F8000000			2674	DC XL16'00000000F800000000000000F8000000'
00014740	C4C9C5C2 D940D996			2675	DC CL48'DIEBR Rounding FPCR 11kl'
00014770	00000000 F8000000			2676	DC XL16'00000000F800000000000000F8000000'
00014780	C4C9C5C2 D940D996			2677	DC CL48'DIEBR Rounding FPCR 11mn'
000147B0	00000000 F8000000			2678	DC XL16'00000000F800000000000000F8000000'
000147C0	C4C9C5C2 D940D996			2679	DC CL48'DIEBR Rounding FPCR 11op'
000147F0	00000000 F8000000			2680	DC XL16'00000000F800000000000000F8000000'
00014800	C4C9C5C2 D940D996			2681	DC CL48'DIEBR Rounding FPCR 11qr'
00014830	00000000 F8000000			2682	DC XL16'00000000F800000000000000F8000000'
00014840	C4C9C5C2 D940D996			2683	DC CL48'DIEBR Rounding FPCR 11st'
00014870	00000000 F8000000			2684	DC XL16'00000000F800000000000000F8000000'
00014880	C4C9C5C2 D940D996			2685	DC CL48'DIEBR Rounding FPCR 11uv'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000148B0	00000000 F8000000			2686 DC XL16'00000000F800000000000000F8000000'
000148C0	C4C9C5C2 D940D996			2687 DC CL48'DIEBR Rounding FPCR 11wx'
000148F0	00000000 F8000000			2688 DC XL16'00000000F800000000000000F8000000'
00014900	C4C9C5C2 D940D996			2689 DC CL48'DIEBR Rounding FPCR 12ab'
00014930	00000000 F8000000			2690 DC XL16'00000000F800000000000000F8000000'
00014940	C4C9C5C2 D940D996			2691 DC CL48'DIEBR Rounding FPCR 12cd'
00014970	00000000 F8000000			2692 DC XL16'00000000F800000000000000F8000000'
00014980	C4C9C5C2 D940D996			2693 DC CL48'DIEBR Rounding FPCR 12ef'
000149B0	00000000 F8000000			2694 DC XL16'00000000F800000000000000F8000000'
000149C0	C4C9C5C2 D940D996			2695 DC CL48'DIEBR Rounding FPCR 12gh'
000149F0	00000000 F8000000			2696 DC XL16'00000000F800000000000000F8000000'
00014A00	C4C9C5C2 D940D996			2697 DC CL48'DIEBR Rounding FPCR 12ij'
00014A30	00000000 F8000000			2698 DC XL16'00000000F800000000000000F8000000'
00014A40	C4C9C5C2 D940D996			2699 DC CL48'DIEBR Rounding FPCR 12kl'
00014A70	00000000 F8000000			2700 DC XL16'00000000F800000000000000F8000000'
00014A80	C4C9C5C2 D940D996			2701 DC CL48'DIEBR Rounding FPCR 12mn'
00014AB0	00000000 F8000000			2702 DC XL16'00000000F800000000000000F8000000'
00014AC0	C4C9C5C2 D940D996			2703 DC CL48'DIEBR Rounding FPCR 12op'
00014AF0	00000000 F8000000			2704 DC XL16'00000000F800000000000000F8000000'
00014B00	C4C9C5C2 D940D996			2705 DC CL48'DIEBR Rounding FPCR 12qr'
00014B30	00000000 F8000000			2706 DC XL16'00000000F800000000000000F8000000'
00014B40	C4C9C5C2 D940D996			2707 DC CL48'DIEBR Rounding FPCR 12st'
00014B70	00000000 F8000000			2708 DC XL16'00000000F800000000000000F8000000'
00014B80	C4C9C5C2 D940D996			2709 DC CL48'DIEBR Rounding FPCR 12uv'
00014BB0	00000000 F8000000			2710 DC XL16'00000000F800000000000000F8000000'
00014BC0	C4C9C5C2 D940D996			2711 DC CL48'DIEBR Rounding FPCR 12wx'
00014BF0	00000000 F8000000			2712 DC XL16'00000000F800000000000000F8000000'
		00000090	00000001	2713 SBFPRMOF_NUM EQU (*-SBFPRMOF_GOOD)/64
				2714 *
				2715 *
		00014C00	00000001	2716 LBFPRMO_GOOD EQU *
00014C00	C4C9C4C2 D9409996			2717 DC CL48'DIDBR rounding test 1a NT'
00014C30	3FE00000 00000000			2718 DC XL16'3FE0000000000000C014000000000000'
00014C40	C4C9C4C2 D9409996			2719 DC CL48'DIDBR rounding test 1a TR'
00014C70	3FE00000 00000000			2720 DC XL16'3FE0000000000000C014000000000000'
00014C80	C4C9C4C2 D9409996			2721 DC CL48'DIDBR rounding test 1b NT'
00014CB0	3FE00000 00000000			2722 DC XL16'3FE0000000000000C014000000000000'
00014CC0	C4C9C4C2 D9409996			2723 DC CL48'DIDBR rounding test 1b TR'
00014CF0	3FE00000 00000000			2724 DC XL16'3FE0000000000000C014000000000000'
00014D00	C4C9C4C2 D9409996			2725 DC CL48'DIDBR rounding test 1c NT'
00014D30	3FE00000 00000000			2726 DC XL16'3FE0000000000000C014000000000000'
00014D40	C4C9C4C2 D9409996			2727 DC CL48'DIDBR rounding test 1c TR'
00014D70	3FE00000 00000000			2728 DC XL16'3FE0000000000000C014000000000000'
00014D80	C4C9C4C2 D9409996			2729 DC CL48'DIDBR rounding test 1d NT'
00014DB0	BFF80000 00000000			2730 DC XL16'BFF8000000000000C010000000000000'
00014DC0	C4C9C4C2 D9409996			2731 DC CL48'DIDBR rounding test 1d TR'
00014DF0	BFF80000 00000000			2732 DC XL16'BFF8000000000000C010000000000000'
00014E00	C4C9C4C2 D9409996			2733 DC CL48'DIDBR rounding test 1e NT'
00014E30	BFF80000 00000000			2734 DC XL16'BFF8000000000000C010000000000000'
00014E40	C4C9C4C2 D9409996			2735 DC CL48'DIDBR rounding test 1e TR'
00014E70	BFF80000 00000000			2736 DC XL16'BFF8000000000000C010000000000000'
00014E80	C4C9C4C2 D9409996			2737 DC CL48'DIDBR rounding test 1f NT'
00014EB0	3FE00000 00000000			2738 DC XL16'3FE0000000000000C014000000000000'
00014EC0	C4C9C4C2 D9409996			2739 DC CL48'DIDBR rounding test 1f TR'
00014EF0	3FE00000 00000000			2740 DC XL16'3FE0000000000000C014000000000000'
00014F00	C4C9C4C2 D9409996			2741 DC CL48'DIDBR rounding test 1g NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00014F30	3FE00000 00000000			2742	DC XL16'3FE0000000000000C014000000000000'
00014F40	C4C9C4C2 D9409996			2743	DC CL48'DIDBR rounding test 1g TR'
00014F70	3FE00000 00000000			2744	DC XL16'3FE0000000000000C014000000000000'
00014F80	C4C9C4C2 D9409996			2745	DC CL48'DIDBR rounding test 1h NT'
00014FB0	3FE00000 00000000			2746	DC XL16'3FE0000000000000C014000000000000'
00014FC0	C4C9C4C2 D9409996			2747	DC CL48'DIDBR rounding test 1h TR'
00014FF0	3FE00000 00000000			2748	DC XL16'3FE0000000000000C014000000000000'
00015000	C4C9C4C2 D9409996			2749	DC CL48'DIDBR rounding test 1i NT'
00015030	3FE00000 00000000			2750	DC XL16'3FE0000000000000C014000000000000'
00015040	C4C9C4C2 D9409996			2751	DC CL48'DIDBR rounding test 1i TR'
00015070	3FE00000 00000000			2752	DC XL16'3FE0000000000000C014000000000000'
00015080	C4C9C4C2 D9409996			2753	DC CL48'DIDBR rounding test 1j NT'
000150B0	BFF80000 00000000			2754	DC XL16'BFF8000000000000C010000000000000'
000150C0	C4C9C4C2 D9409996			2755	DC CL48'DIDBR rounding test 1j TR'
000150F0	BFF80000 00000000			2756	DC XL16'BFF8000000000000C010000000000000'
00015100	C4C9C4C2 D9409996			2757	DC CL48'DIDBR rounding test 1k NT'
00015130	BFF80000 00000000			2758	DC XL16'BFF8000000000000C010000000000000'
00015140	C4C9C4C2 D9409996			2759	DC CL48'DIDBR rounding test 1k TR'
00015170	BFF80000 00000000			2760	DC XL16'BFF8000000000000C010000000000000'
00015180	C4C9C4C2 D9409996			2761	DC CL48'DIDBR rounding test 1l NT'
000151B0	3FE00000 00000000			2762	DC XL16'3FE0000000000000C014000000000000'
000151C0	C4C9C4C2 D9409996			2763	DC CL48'DIDBR rounding test 1l TR'
000151F0	3FE00000 00000000			2764	DC XL16'3FE0000000000000C014000000000000'
00015200	C4C9C4C2 D9409996			2765	DC CL48'DIDBR rounding test 1m NT'
00015230	3FE00000 00000000			2766	DC XL16'3FE0000000000000C014000000000000'
00015240	C4C9C4C2 D9409996			2767	DC CL48'DIDBR rounding test 1m TR'
00015270	3FE00000 00000000			2768	DC XL16'3FE0000000000000C014000000000000'
00015280	C4C9C4C2 D9409996			2769	DC CL48'DIDBR rounding test 1n NT'
000152B0	3FE00000 00000000			2770	DC XL16'3FE0000000000000C014000000000000'
000152C0	C4C9C4C2 D9409996			2771	DC CL48'DIDBR rounding test 1n TR'
000152F0	3FE00000 00000000			2772	DC XL16'3FE0000000000000C014000000000000'
00015300	C4C9C4C2 D9409996			2773	DC CL48'DIDBR rounding test 1o NT'
00015330	3FE00000 00000000			2774	DC XL16'3FE0000000000000C014000000000000'
00015340	C4C9C4C2 D9409996			2775	DC CL48'DIDBR rounding test 1o TR'
00015370	3FE00000 00000000			2776	DC XL16'3FE0000000000000C014000000000000'
00015380	C4C9C4C2 D9409996			2777	DC CL48'DIDBR rounding test 1p NT'
000153B0	BFF80000 00000000			2778	DC XL16'BFF8000000000000C010000000000000'
000153C0	C4C9C4C2 D9409996			2779	DC CL48'DIDBR rounding test 1p TR'
000153F0	BFF80000 00000000			2780	DC XL16'BFF8000000000000C010000000000000'
00015400	C4C9C4C2 D9409996			2781	DC CL48'DIDBR rounding test 1q NT'
00015430	BFF80000 00000000			2782	DC XL16'BFF8000000000000C010000000000000'
00015440	C4C9C4C2 D9409996			2783	DC CL48'DIDBR rounding test 1q TR'
00015470	BFF80000 00000000			2784	DC XL16'BFF8000000000000C010000000000000'
00015480	C4C9C4C2 D9409996			2785	DC CL48'DIDBR rounding test 1r NT'
000154B0	3FE00000 00000000			2786	DC XL16'3FE0000000000000C014000000000000'
000154C0	C4C9C4C2 D9409996			2787	DC CL48'DIDBR rounding test 1r TR'
000154F0	3FE00000 00000000			2788	DC XL16'3FE0000000000000C014000000000000'
00015500	C4C9C4C2 D9409996			2789	DC CL48'DIDBR rounding test 1s NT'
00015530	3FE00000 00000000			2790	DC XL16'3FE0000000000000C014000000000000'
00015540	C4C9C4C2 D9409996			2791	DC CL48'DIDBR rounding test 1s TR'
00015570	3FE00000 00000000			2792	DC XL16'3FE0000000000000C014000000000000'
00015580	C4C9C4C2 D9409996			2793	DC CL48'DIDBR rounding test 1t NT'
000155B0	3FE00000 00000000			2794	DC XL16'3FE0000000000000C014000000000000'
000155C0	C4C9C4C2 D9409996			2795	DC CL48'DIDBR rounding test 1t TR'
000155F0	3FE00000 00000000			2796	DC XL16'3FE0000000000000C014000000000000'
00015600	C4C9C4C2 D9409996			2797	DC CL48'DIDBR rounding test 1u NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00015630	3FE00000 00000000			2798 DC XL16'3FE0000000000000C014000000000000'
00015640	C4C9C4C2 D9409996			2799 DC CL48'DIDBR rounding test 1u TR'
00015670	3FE00000 00000000			2800 DC XL16'3FE0000000000000C014000000000000'
00015680	C4C9C4C2 D9409996			2801 DC CL48'DIDBR rounding test 1v NT'
000156B0	BFF80000 00000000			2802 DC XL16'BFF8000000000000C010000000000000'
000156C0	C4C9C4C2 D9409996			2803 DC CL48'DIDBR rounding test 1v TR'
000156F0	BFF80000 00000000			2804 DC XL16'BFF800000000000C010000000000000'
00015700	C4C9C4C2 D9409996			2805 DC CL48'DIDBR rounding test 1w NT'
00015730	BFF80000 00000000			2806 DC XL16'BFF800000000000C010000000000000'
00015740	C4C9C4C2 D9409996			2807 DC CL48'DIDBR rounding test 1w TR'
00015770	BFF80000 00000000			2808 DC XL16'BFF800000000000C010000000000000'
00015780	C4C9C4C2 D9409996			2809 DC CL48'DIDBR rounding test 1x NT'
000157B0	3FE00000 00000000			2810 DC XL16'3FE0000000000000C014000000000000'
000157C0	C4C9C4C2 D9409996			2811 DC CL48'DIDBR rounding test 1x TR'
000157F0	3FE00000 00000000			2812 DC XL16'3FE0000000000000C014000000000000'
00015800	C4C9C4C2 D9409996			2813 DC CL48'DIDBR rounding test 2a NT'
00015830	3FE00000 00000000			2814 DC XL16'3FE0000000000000C008000000000000'
00015840	C4C9C4C2 D9409996			2815 DC CL48'DIDBR rounding test 2a TR'
00015870	3FE00000 00000000			2816 DC XL16'3FE0000000000000C008000000000000'
00015880	C4C9C4C2 D9409996			2817 DC CL48'DIDBR rounding test 2b NT'
000158B0	3FE00000 00000000			2818 DC XL16'3FE0000000000000C008000000000000'
000158C0	C4C9C4C2 D9409996			2819 DC CL48'DIDBR rounding test 2b TR'
000158F0	3FE00000 00000000			2820 DC XL16'3FE0000000000000C008000000000000'
00015900	C4C9C4C2 D9409996			2821 DC CL48'DIDBR rounding test 2c NT'
00015930	3FE00000 00000000			2822 DC XL16'3FE0000000000000C008000000000000'
00015940	C4C9C4C2 D9409996			2823 DC CL48'DIDBR rounding test 2c TR'
00015970	3FE00000 00000000			2824 DC XL16'3FE0000000000000C008000000000000'
00015980	C4C9C4C2 D9409996			2825 DC CL48'DIDBR rounding test 2d NT'
000159B0	BFF80000 00000000			2826 DC XL16'BFF8000000000000C000000000000000'
000159C0	C4C9C4C2 D9409996			2827 DC CL48'DIDBR rounding test 2d TR'
000159F0	BFF80000 00000000			2828 DC XL16'BFF8000000000000C000000000000000'
00015A00	C4C9C4C2 D9409996			2829 DC CL48'DIDBR rounding test 2e NT'
00015A30	BFF80000 00000000			2830 DC XL16'BFF8000000000000C000000000000000'
00015A40	C4C9C4C2 D9409996			2831 DC CL48'DIDBR rounding test 2e TR'
00015A70	BFF80000 00000000			2832 DC XL16'BFF8000000000000C000000000000000'
00015A80	C4C9C4C2 D9409996			2833 DC CL48'DIDBR rounding test 2f NT'
00015AB0	3FE00000 00000000			2834 DC XL16'3FE0000000000000C008000000000000'
00015AC0	C4C9C4C2 D9409996			2835 DC CL48'DIDBR rounding test 2f TR'
00015AF0	3FE00000 00000000			2836 DC XL16'3FE0000000000000C008000000000000'
00015B00	C4C9C4C2 D9409996			2837 DC CL48'DIDBR rounding test 2g NT'
00015B30	3FE00000 00000000			2838 DC XL16'3FE0000000000000C008000000000000'
00015B40	C4C9C4C2 D9409996			2839 DC CL48'DIDBR rounding test 2g TR'
00015B70	3FE00000 00000000			2840 DC XL16'3FE0000000000000C008000000000000'
00015B80	C4C9C4C2 D9409996			2841 DC CL48'DIDBR rounding test 2h NT'
00015BB0	3FE00000 00000000			2842 DC XL16'3FE0000000000000C008000000000000'
00015BC0	C4C9C4C2 D9409996			2843 DC CL48'DIDBR rounding test 2h TR'
00015BF0	3FE00000 00000000			2844 DC XL16'3FE0000000000000C008000000000000'
00015C00	C4C9C4C2 D9409996			2845 DC CL48'DIDBR rounding test 2i NT'
00015C30	3FE00000 00000000			2846 DC XL16'3FE0000000000000C008000000000000'
00015C40	C4C9C4C2 D9409996			2847 DC CL48'DIDBR rounding test 2i TR'
00015C70	3FE00000 00000000			2848 DC XL16'3FE0000000000000C008000000000000'
00015C80	C4C9C4C2 D9409996			2849 DC CL48'DIDBR rounding test 2j NT'
00015CB0	BFF80000 00000000			2850 DC XL16'BFF8000000000000C000000000000000'
00015CC0	C4C9C4C2 D9409996			2851 DC CL48'DIDBR rounding test 2j TR'
00015CF0	BFF80000 00000000			2852 DC XL16'BFF8000000000000C000000000000000'
00015D00	C4C9C4C2 D9409996			2853 DC CL48'DIDBR rounding test 2k NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00015D30	BFF80000 00000000			2854	DC XL16'BFF8000000000000C0000000000000'
00015D40	C4C9C4C2 D9409996			2855	DC CL48'DIDBR rounding test 2k TR'
00015D70	BFF80000 00000000			2856	DC XL16'BFF8000000000000C0000000000000'
00015D80	C4C9C4C2 D9409996			2857	DC CL48'DIDBR rounding test 2l NT'
00015DB0	3FE00000 00000000			2858	DC XL16'3FE0000000000000C00800000000000'
00015DC0	C4C9C4C2 D9409996			2859	DC CL48'DIDBR rounding test 2l TR'
00015DF0	3FE00000 00000000			2860	DC XL16'3FE0000000000000C00800000000000'
00015E00	C4C9C4C2 D9409996			2861	DC CL48'DIDBR rounding test 2m NT'
00015E30	3FE00000 00000000			2862	DC XL16'3FE0000000000000C00800000000000'
00015E40	C4C9C4C2 D9409996			2863	DC CL48'DIDBR rounding test 2m TR'
00015E70	3FE00000 00000000			2864	DC XL16'3FE0000000000000C00800000000000'
00015E80	C4C9C4C2 D9409996			2865	DC CL48'DIDBR rounding test 2n NT'
00015EB0	3FE00000 00000000			2866	DC XL16'3FE0000000000000C00800000000000'
00015EC0	C4C9C4C2 D9409996			2867	DC CL48'DIDBR rounding test 2n TR'
00015EF0	3FE00000 00000000			2868	DC XL16'3FE0000000000000C00800000000000'
00015F00	C4C9C4C2 D9409996			2869	DC CL48'DIDBR rounding test 2o NT'
00015F30	3FE00000 00000000			2870	DC XL16'3FE0000000000000C00800000000000'
00015F40	C4C9C4C2 D9409996			2871	DC CL48'DIDBR rounding test 2o TR'
00015F70	3FE00000 00000000			2872	DC XL16'3FE0000000000000C00800000000000'
00015F80	C4C9C4C2 D9409996			2873	DC CL48'DIDBR rounding test 2p NT'
00015FB0	BFF80000 00000000			2874	DC XL16'BFF8000000000000C00000000000000'
00015FC0	C4C9C4C2 D9409996			2875	DC CL48'DIDBR rounding test 2p TR'
00015FF0	BFF80000 00000000			2876	DC XL16'BFF8000000000000C00000000000000'
00016000	C4C9C4C2 D9409996			2877	DC CL48'DIDBR rounding test 2q NT'
00016030	BFF80000 00000000			2878	DC XL16'BFF8000000000000C00000000000000'
00016040	C4C9C4C2 D9409996			2879	DC CL48'DIDBR rounding test 2q TR'
00016070	BFF80000 00000000			2880	DC XL16'BFF8000000000000C00000000000000'
00016080	C4C9C4C2 D9409996			2881	DC CL48'DIDBR rounding test 2r NT'
000160B0	3FE00000 00000000			2882	DC XL16'3FE0000000000000C00800000000000'
000160C0	C4C9C4C2 D9409996			2883	DC CL48'DIDBR rounding test 2r TR'
000160F0	3FE00000 00000000			2884	DC XL16'3FE0000000000000C00800000000000'
00016100	C4C9C4C2 D9409996			2885	DC CL48'DIDBR rounding test 2s NT'
00016130	3FE00000 00000000			2886	DC XL16'3FE0000000000000C00800000000000'
00016140	C4C9C4C2 D9409996			2887	DC CL48'DIDBR rounding test 2s TR'
00016170	3FE00000 00000000			2888	DC XL16'3FE0000000000000C00800000000000'
00016180	C4C9C4C2 D9409996			2889	DC CL48'DIDBR rounding test 2t NT'
000161B0	3FE00000 00000000			2890	DC XL16'3FE0000000000000C00800000000000'
000161C0	C4C9C4C2 D9409996			2891	DC CL48'DIDBR rounding test 2t TR'
000161F0	3FE00000 00000000			2892	DC XL16'3FE0000000000000C00800000000000'
00016200	C4C9C4C2 D9409996			2893	DC CL48'DIDBR rounding test 2u NT'
00016230	3FE00000 00000000			2894	DC XL16'3FE0000000000000C00800000000000'
00016240	C4C9C4C2 D9409996			2895	DC CL48'DIDBR rounding test 2u TR'
00016270	3FE00000 00000000			2896	DC XL16'3FE0000000000000C00800000000000'
00016280	C4C9C4C2 D9409996			2897	DC CL48'DIDBR rounding test 2v NT'
000162B0	BFF80000 00000000			2898	DC XL16'BFF8000000000000C00000000000000'
000162C0	C4C9C4C2 D9409996			2899	DC CL48'DIDBR rounding test 2v TR'
000162F0	BFF80000 00000000			2900	DC XL16'BFF8000000000000C00000000000000'
00016300	C4C9C4C2 D9409996			2901	DC CL48'DIDBR rounding test 2w NT'
00016330	BFF80000 00000000			2902	DC XL16'BFF8000000000000C00000000000000'
00016340	C4C9C4C2 D9409996			2903	DC CL48'DIDBR rounding test 2w TR'
00016370	BFF80000 00000000			2904	DC XL16'BFF8000000000000C00000000000000'
00016380	C4C9C4C2 D9409996			2905	DC CL48'DIDBR rounding test 2x NT'
000163B0	3FE00000 00000000			2906	DC XL16'3FE0000000000000C00800000000000'
000163C0	C4C9C4C2 D9409996			2907	DC CL48'DIDBR rounding test 2x TR'
000163F0	3FE00000 00000000			2908	DC XL16'3FE0000000000000C00800000000000'
00016400	C4C9C4C2 D9409996			2909	DC CL48'DIDBR rounding test 3a NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00016430	BFE00000 00000000			2910	DC XL16'BFE0000000000000BFF000000000000'
00016440	C4C9C4C2 D9409996			2911	DC CL48'DIDBR rounding test 3a TR'
00016470	BFE00000 00000000			2912	DC XL16'BFE0000000000000BFF000000000000'
00016480	C4C9C4C2 D9409996			2913	DC CL48'DIDBR rounding test 3b NT'
000164B0	BFE00000 00000000			2914	DC XL16'BFE0000000000000BFF000000000000'
000164C0	C4C9C4C2 D9409996			2915	DC CL48'DIDBR rounding test 3b TR'
000164F0	BFE00000 00000000			2916	DC XL16'BFE0000000000000BFF000000000000'
00016500	C4C9C4C2 D9409996			2917	DC CL48'DIDBR rounding test 3c NT'
00016530	BFE00000 00000000			2918	DC XL16'BFE0000000000000BFF000000000000'
00016540	C4C9C4C2 D9409996			2919	DC CL48'DIDBR rounding test 3c TR'
00016570	BFE00000 00000000			2920	DC XL16'BFE0000000000000BFF000000000000'
00016580	C4C9C4C2 D9409996			2921	DC CL48'DIDBR rounding test 3d NT'
000165B0	BFE00000 00000000			2922	DC XL16'BFE0000000000000BFF000000000000'
000165C0	C4C9C4C2 D9409996			2923	DC CL48'DIDBR rounding test 3d TR'
000165F0	BFE00000 00000000			2924	DC XL16'BFE0000000000000BFF000000000000'
00016600	C4C9C4C2 D9409996			2925	DC CL48'DIDBR rounding test 3e NT'
00016630	BFE00000 00000000			2926	DC XL16'BFE0000000000000BFF000000000000'
00016640	C4C9C4C2 D9409996			2927	DC CL48'DIDBR rounding test 3e TR'
00016670	BFE00000 00000000			2928	DC XL16'BFE0000000000000BFF000000000000'
00016680	C4C9C4C2 D9409996			2929	DC CL48'DIDBR rounding test 3f NT'
000166B0	3FF80000 00000000			2930	DC XL16'3FF8000000000000C0000000000000'
000166C0	C4C9C4C2 D9409996			2931	DC CL48'DIDBR rounding test 3f TR'
000166F0	3FF80000 00000000			2932	DC XL16'3FF8000000000000C0000000000000'
00016700	C4C9C4C2 D9409996			2933	DC CL48'DIDBR rounding test 3g NT'
00016730	BFE00000 00000000			2934	DC XL16'BFE0000000000000BFF000000000000'
00016740	C4C9C4C2 D9409996			2935	DC CL48'DIDBR rounding test 3g TR'
00016770	BFE00000 00000000			2936	DC XL16'BFE0000000000000BFF000000000000'
00016780	C4C9C4C2 D9409996			2937	DC CL48'DIDBR rounding test 3h NT'
000167B0	BFE00000 00000000			2938	DC XL16'BFE0000000000000BFF000000000000'
000167C0	C4C9C4C2 D9409996			2939	DC CL48'DIDBR rounding test 3h TR'
000167F0	BFE00000 00000000			2940	DC XL16'BFE0000000000000BFF000000000000'
00016800	C4C9C4C2 D9409996			2941	DC CL48'DIDBR rounding test 3i NT'
00016830	BFE00000 00000000			2942	DC XL16'BFE0000000000000BFF000000000000'
00016840	C4C9C4C2 D9409996			2943	DC CL48'DIDBR rounding test 3i TR'
00016870	BFE00000 00000000			2944	DC XL16'BFE0000000000000BFF000000000000'
00016880	C4C9C4C2 D9409996			2945	DC CL48'DIDBR rounding test 3j NT'
000168B0	BFE00000 00000000			2946	DC XL16'BFE0000000000000BFF000000000000'
000168C0	C4C9C4C2 D9409996			2947	DC CL48'DIDBR rounding test 3j TR'
000168F0	BFE00000 00000000			2948	DC XL16'BFE0000000000000BFF000000000000'
00016900	C4C9C4C2 D9409996			2949	DC CL48'DIDBR rounding test 3k NT'
00016930	BFE00000 00000000			2950	DC XL16'BFE0000000000000BFF000000000000'
00016940	C4C9C4C2 D9409996			2951	DC CL48'DIDBR rounding test 3k TR'
00016970	BFE00000 00000000			2952	DC XL16'BFE0000000000000BFF000000000000'
00016980	C4C9C4C2 D9409996			2953	DC CL48'DIDBR rounding test 3l NT'
000169B0	3FF80000 00000000			2954	DC XL16'3FF8000000000000C0000000000000'
000169C0	C4C9C4C2 D9409996			2955	DC CL48'DIDBR rounding test 3l TR'
000169F0	3FF80000 00000000			2956	DC XL16'3FF8000000000000C0000000000000'
00016A00	C4C9C4C2 D9409996			2957	DC CL48'DIDBR rounding test 3m NT'
00016A30	BFE00000 00000000			2958	DC XL16'BFE0000000000000BFF000000000000'
00016A40	C4C9C4C2 D9409996			2959	DC CL48'DIDBR rounding test 3m TR'
00016A70	BFE00000 00000000			2960	DC XL16'BFE0000000000000BFF000000000000'
00016A80	C4C9C4C2 D9409996			2961	DC CL48'DIDBR rounding test 3n NT'
00016AB0	BFE00000 00000000			2962	DC XL16'BFE0000000000000BFF000000000000'
00016AC0	C4C9C4C2 D9409996			2963	DC CL48'DIDBR rounding test 3n TR'
00016AF0	BFE00000 00000000			2964	DC XL16'BFE0000000000000BFF000000000000'
00016B00	C4C9C4C2 D9409996			2965	DC CL48'DIDBR rounding test 3o NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00016B30	BFE00000 00000000			2966 DC XL16'BFE0000000000000BFF0000000000000'
00016B40	C4C9C4C2 D9409996			2967 DC CL48'DIDBR rounding test 3o TR'
00016B70	BFE00000 00000000			2968 DC XL16'BFE0000000000000BFF0000000000000'
00016B80	C4C9C4C2 D9409996			2969 DC CL48'DIDBR rounding test 3p NT'
00016BB0	BFE00000 00000000			2970 DC XL16'BFE0000000000000BFF0000000000000'
00016BC0	C4C9C4C2 D9409996			2971 DC CL48'DIDBR rounding test 3p TR'
00016BF0	BFE00000 00000000			2972 DC XL16'BFE0000000000000BFF0000000000000'
00016C00	C4C9C4C2 D9409996			2973 DC CL48'DIDBR rounding test 3q NT'
00016C30	BFE00000 00000000			2974 DC XL16'BFE0000000000000BFF0000000000000'
00016C40	C4C9C4C2 D9409996			2975 DC CL48'DIDBR rounding test 3q TR'
00016C70	BFE00000 00000000			2976 DC XL16'BFE0000000000000BFF0000000000000'
00016C80	C4C9C4C2 D9409996			2977 DC CL48'DIDBR rounding test 3r NT'
00016CB0	3FF80000 00000000			2978 DC XL16'3FF8000000000000C00000000000000'
00016CC0	C4C9C4C2 D9409996			2979 DC CL48'DIDBR rounding test 3r TR'
00016CF0	3FF80000 00000000			2980 DC XL16'3FF8000000000000C00000000000000'
00016D00	C4C9C4C2 D9409996			2981 DC CL48'DIDBR rounding test 3s NT'
00016D30	BFE00000 00000000			2982 DC XL16'BFE0000000000000BFF0000000000000'
00016D40	C4C9C4C2 D9409996			2983 DC CL48'DIDBR rounding test 3s TR'
00016D70	BFE00000 00000000			2984 DC XL16'BFE0000000000000BFF0000000000000'
00016D80	C4C9C4C2 D9409996			2985 DC CL48'DIDBR rounding test 3t NT'
00016DB0	BFE00000 00000000			2986 DC XL16'BFE0000000000000BFF0000000000000'
00016DC0	C4C9C4C2 D9409996			2987 DC CL48'DIDBR rounding test 3t TR'
00016DF0	BFE00000 00000000			2988 DC XL16'BFE0000000000000BFF0000000000000'
00016E00	C4C9C4C2 D9409996			2989 DC CL48'DIDBR rounding test 3u NT'
00016E30	BFE00000 00000000			2990 DC XL16'BFE0000000000000BFF0000000000000'
00016E40	C4C9C4C2 D9409996			2991 DC CL48'DIDBR rounding test 3u TR'
00016E70	BFE00000 00000000			2992 DC XL16'BFE0000000000000BFF0000000000000'
00016E80	C4C9C4C2 D9409996			2993 DC CL48'DIDBR rounding test 3v NT'
00016EB0	BFE00000 00000000			2994 DC XL16'BFE0000000000000BFF0000000000000'
00016EC0	C4C9C4C2 D9409996			2995 DC CL48'DIDBR rounding test 3v TR'
00016EF0	BFE00000 00000000			2996 DC XL16'BFE0000000000000BFF0000000000000'
00016F00	C4C9C4C2 D9409996			2997 DC CL48'DIDBR rounding test 3w NT'
00016F30	BFE00000 00000000			2998 DC XL16'BFE0000000000000BFF0000000000000'
00016F40	C4C9C4C2 D9409996			2999 DC CL48'DIDBR rounding test 3w TR'
00016F70	BFE00000 00000000			3000 DC XL16'BFE0000000000000BFF0000000000000'
00016F80	C4C9C4C2 D9409996			3001 DC CL48'DIDBR rounding test 3x NT'
00016FB0	3FF80000 00000000			3002 DC XL16'3FF8000000000000C00000000000000'
00016FC0	C4C9C4C2 D9409996			3003 DC CL48'DIDBR rounding test 3x TR'
00016FF0	3FF80000 00000000			3004 DC XL16'3FF8000000000000C00000000000000'
00017000	C4C9C4C2 D9409996			3005 DC CL48'DIDBR rounding test 4a NT'
00017030	3FE00000 00000000			3006 DC XL16'3FE0000000000000BFF0000000000000'
00017040	C4C9C4C2 D9409996			3007 DC CL48'DIDBR rounding test 4a TR'
00017070	3FE00000 00000000			3008 DC XL16'3FE0000000000000BFF0000000000000'
00017080	C4C9C4C2 D9409996			3009 DC CL48'DIDBR rounding test 4b NT'
000170B0	3FE00000 00000000			3010 DC XL16'3FE0000000000000BFF0000000000000'
000170C0	C4C9C4C2 D9409996			3011 DC CL48'DIDBR rounding test 4b TR'
000170F0	3FE00000 00000000			3012 DC XL16'3FE0000000000000BFF0000000000000'
00017100	C4C9C4C2 D9409996			3013 DC CL48'DIDBR rounding test 4c NT'
00017130	3FE00000 00000000			3014 DC XL16'3FE0000000000000BFF0000000000000'
00017140	C4C9C4C2 D9409996			3015 DC CL48'DIDBR rounding test 4c TR'
00017170	3FE00000 00000000			3016 DC XL16'3FE0000000000000BFF0000000000000'
00017180	C4C9C4C2 D9409996			3017 DC CL48'DIDBR rounding test 4d NT'
000171B0	BFF80000 00000000			3018 DC XL16'BFF8000000000000800000000000000'
000171C0	C4C9C4C2 D9409996			3019 DC CL48'DIDBR rounding test 4d TR'
000171F0	BFF80000 00000000			3020 DC XL16'BFF8000000000000800000000000000'
00017200	C4C9C4C2 D9409996			3021 DC CL48'DIDBR rounding test 4e NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00017230	BFF80000 00000000			3022	DC XL16'BFF8000000000000080000000000000'
00017240	C4C9C4C2 D9409996			3023	DC CL48'DIDBR rounding test 4e TR'
00017270	BFF80000 00000000			3024	DC XL16'BFF8000000000000080000000000000'
00017280	C4C9C4C2 D9409996			3025	DC CL48'DIDBR rounding test 4f NT'
000172B0	3FE00000 00000000			3026	DC XL16'3FE0000000000000BFF0000000000000'
000172C0	C4C9C4C2 D9409996			3027	DC CL48'DIDBR rounding test 4f TR'
000172F0	3FE00000 00000000			3028	DC XL16'3FE0000000000000BFF0000000000000'
00017300	C4C9C4C2 D9409996			3029	DC CL48'DIDBR rounding test 4g NT'
00017330	3FE00000 00000000			3030	DC XL16'3FE0000000000000BFF0000000000000'
00017340	C4C9C4C2 D9409996			3031	DC CL48'DIDBR rounding test 4g TR'
00017370	3FE00000 00000000			3032	DC XL16'3FE0000000000000BFF0000000000000'
00017380	C4C9C4C2 D9409996			3033	DC CL48'DIDBR rounding test 4h NT'
000173B0	3FE00000 00000000			3034	DC XL16'3FE0000000000000BFF0000000000000'
000173C0	C4C9C4C2 D9409996			3035	DC CL48'DIDBR rounding test 4h TR'
000173F0	3FE00000 00000000			3036	DC XL16'3FE0000000000000BFF0000000000000'
00017400	C4C9C4C2 D9409996			3037	DC CL48'DIDBR rounding test 4i NT'
00017430	3FE00000 00000000			3038	DC XL16'3FE0000000000000BFF0000000000000'
00017440	C4C9C4C2 D9409996			3039	DC CL48'DIDBR rounding test 4i TR'
00017470	3FE00000 00000000			3040	DC XL16'3FE0000000000000BFF0000000000000'
00017480	C4C9C4C2 D9409996			3041	DC CL48'DIDBR rounding test 4j NT'
000174B0	BFF80000 00000000			3042	DC XL16'BFF8000000000000080000000000000'
000174C0	C4C9C4C2 D9409996			3043	DC CL48'DIDBR rounding test 4j TR'
000174F0	BFF80000 00000000			3044	DC XL16'BFF8000000000000080000000000000'
00017500	C4C9C4C2 D9409996			3045	DC CL48'DIDBR rounding test 4k NT'
00017530	BFF80000 00000000			3046	DC XL16'BFF8000000000000080000000000000'
00017540	C4C9C4C2 D9409996			3047	DC CL48'DIDBR rounding test 4k TR'
00017570	BFF80000 00000000			3048	DC XL16'BFF8000000000000080000000000000'
00017580	C4C9C4C2 D9409996			3049	DC CL48'DIDBR rounding test 4l NT'
000175B0	3FE00000 00000000			3050	DC XL16'3FE0000000000000BFF0000000000000'
000175C0	C4C9C4C2 D9409996			3051	DC CL48'DIDBR rounding test 4l TR'
000175F0	3FE00000 00000000			3052	DC XL16'3FE0000000000000BFF0000000000000'
00017600	C4C9C4C2 D9409996			3053	DC CL48'DIDBR rounding test 4m NT'
00017630	3FE00000 00000000			3054	DC XL16'3FE0000000000000BFF0000000000000'
00017640	C4C9C4C2 D9409996			3055	DC CL48'DIDBR rounding test 4m TR'
00017670	3FE00000 00000000			3056	DC XL16'3FE0000000000000BFF0000000000000'
00017680	C4C9C4C2 D9409996			3057	DC CL48'DIDBR rounding test 4n NT'
000176B0	3FE00000 00000000			3058	DC XL16'3FE0000000000000BFF0000000000000'
000176C0	C4C9C4C2 D9409996			3059	DC CL48'DIDBR rounding test 4n TR'
000176F0	3FE00000 00000000			3060	DC XL16'3FE0000000000000BFF0000000000000'
00017700	C4C9C4C2 D9409996			3061	DC CL48'DIDBR rounding test 4o NT'
00017730	3FE00000 00000000			3062	DC XL16'3FE0000000000000BFF0000000000000'
00017740	C4C9C4C2 D9409996			3063	DC CL48'DIDBR rounding test 4o TR'
00017770	3FE00000 00000000			3064	DC XL16'3FE0000000000000BFF0000000000000'
00017780	C4C9C4C2 D9409996			3065	DC CL48'DIDBR rounding test 4p NT'
000177B0	BFF80000 00000000			3066	DC XL16'BFF8000000000000080000000000000'
000177C0	C4C9C4C2 D9409996			3067	DC CL48'DIDBR rounding test 4p TR'
000177F0	BFF80000 00000000			3068	DC XL16'BFF8000000000000080000000000000'
00017800	C4C9C4C2 D9409996			3069	DC CL48'DIDBR rounding test 4q NT'
00017830	BFF80000 00000000			3070	DC XL16'BFF8000000000000080000000000000'
00017840	C4C9C4C2 D9409996			3071	DC CL48'DIDBR rounding test 4q TR'
00017870	BFF80000 00000000			3072	DC XL16'BFF8000000000000080000000000000'
00017880	C4C9C4C2 D9409996			3073	DC CL48'DIDBR rounding test 4r NT'
000178B0	3FE00000 00000000			3074	DC XL16'3FE0000000000000BFF0000000000000'
000178C0	C4C9C4C2 D9409996			3075	DC CL48'DIDBR rounding test 4r TR'
000178F0	3FE00000 00000000			3076	DC XL16'3FE0000000000000BFF0000000000000'
00017900	C4C9C4C2 D9409996			3077	DC CL48'DIDBR rounding test 4s NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00017930	3FE00000 00000000			3078 DC XL16'3FE0000000000000BFF0000000000000'
00017940	C4C9C4C2 D9409996			3079 DC CL48'DIDBR rounding test 4s TR'
00017970	3FE00000 00000000			3080 DC XL16'3FE0000000000000BFF0000000000000'
00017980	C4C9C4C2 D9409996			3081 DC CL48'DIDBR rounding test 4t NT'
000179B0	3FE00000 00000000			3082 DC XL16'3FE0000000000000BFF0000000000000'
000179C0	C4C9C4C2 D9409996			3083 DC CL48'DIDBR rounding test 4t TR'
000179F0	3FE00000 00000000			3084 DC XL16'3FE0000000000000BFF0000000000000'
00017A00	C4C9C4C2 D9409996			3085 DC CL48'DIDBR rounding test 4u NT'
00017A30	3FE00000 00000000			3086 DC XL16'3FE0000000000000BFF0000000000000'
00017A40	C4C9C4C2 D9409996			3087 DC CL48'DIDBR rounding test 4u TR'
00017A70	3FE00000 00000000			3088 DC XL16'3FE0000000000000BFF0000000000000'
00017A80	C4C9C4C2 D9409996			3089 DC CL48'DIDBR rounding test 4v NT'
00017AB0	BFF80000 00000000			3090 DC XL16'BFF80000000000000800000000000000'
00017AC0	C4C9C4C2 D9409996			3091 DC CL48'DIDBR rounding test 4v TR'
00017AF0	BFF80000 00000000			3092 DC XL16'BFF80000000000000800000000000000'
00017B00	C4C9C4C2 D9409996			3093 DC CL48'DIDBR rounding test 4w NT'
00017B30	BFF80000 00000000			3094 DC XL16'BFF80000000000000800000000000000'
00017B40	C4C9C4C2 D9409996			3095 DC CL48'DIDBR rounding test 4w TR'
00017B70	BFF80000 00000000			3096 DC XL16'BFF80000000000000800000000000000'
00017B80	C4C9C4C2 D9409996			3097 DC CL48'DIDBR rounding test 4x NT'
00017BB0	3FE00000 00000000			3098 DC XL16'3FE0000000000000BFF00000000000000'
00017BC0	C4C9C4C2 D9409996			3099 DC CL48'DIDBR rounding test 4x TR'
00017BF0	3FE00000 00000000			3100 DC XL16'3FE0000000000000BFF00000000000000'
00017C00	C4C9C4C2 D9409996			3101 DC CL48'DIDBR rounding test 5a NT'
00017C30	BFE00000 00000000			3102 DC XL16'BFE00000000000000800000000000000'
00017C40	C4C9C4C2 D9409996			3103 DC CL48'DIDBR rounding test 5a TR'
00017C70	BFE00000 00000000			3104 DC XL16'BFE00000000000000800000000000000'
00017C80	C4C9C4C2 D9409996			3105 DC CL48'DIDBR rounding test 5b NT'
00017CB0	3FF80000 00000000			3106 DC XL16'3FF8000000000000BFF00000000000000'
00017CC0	C4C9C4C2 D9409996			3107 DC CL48'DIDBR rounding test 5b TR'
00017CF0	3FF80000 00000000			3108 DC XL16'3FF8000000000000BFF00000000000000'
00017D00	C4C9C4C2 D9409996			3109 DC CL48'DIDBR rounding test 5c NT'
00017D30	BFE00000 00000000			3110 DC XL16'BFE00000000000000800000000000000'
00017D40	C4C9C4C2 D9409996			3111 DC CL48'DIDBR rounding test 5c TR'
00017D70	BFE00000 00000000			3112 DC XL16'BFE00000000000000800000000000000'
00017D80	C4C9C4C2 D9409996			3113 DC CL48'DIDBR rounding test 5d NT'
00017DB0	BFE00000 00000000			3114 DC XL16'BFE00000000000000800000000000000'
00017DC0	C4C9C4C2 D9409996			3115 DC CL48'DIDBR rounding test 5d TR'
00017DF0	BFE00000 00000000			3116 DC XL16'BFE00000000000000800000000000000'
00017E00	C4C9C4C2 D9409996			3117 DC CL48'DIDBR rounding test 5e NT'
00017E30	BFE00000 00000000			3118 DC XL16'BFE00000000000000800000000000000'
00017E40	C4C9C4C2 D9409996			3119 DC CL48'DIDBR rounding test 5e TR'
00017E70	BFE00000 00000000			3120 DC XL16'BFE00000000000000800000000000000'
00017E80	C4C9C4C2 D9409996			3121 DC CL48'DIDBR rounding test 5f NT'
00017EB0	3FF80000 00000000			3122 DC XL16'3FF8000000000000BFF00000000000000'
00017EC0	C4C9C4C2 D9409996			3123 DC CL48'DIDBR rounding test 5f TR'
00017EF0	3FF80000 00000000			3124 DC XL16'3FF8000000000000BFF00000000000000'
00017F00	C4C9C4C2 D9409996			3125 DC CL48'DIDBR rounding test 5g NT'
00017F30	BFE00000 00000000			3126 DC XL16'BFE00000000000000800000000000000'
00017F40	C4C9C4C2 D9409996			3127 DC CL48'DIDBR rounding test 5g TR'
00017F70	BFE00000 00000000			3128 DC XL16'BFE00000000000000800000000000000'
00017F80	C4C9C4C2 D9409996			3129 DC CL48'DIDBR rounding test 5h NT'
00017FB0	3FF80000 00000000			3130 DC XL16'3FF8000000000000BFF00000000000000'
00017FC0	C4C9C4C2 D9409996			3131 DC CL48'DIDBR rounding test 5h TR'
00017FF0	3FF80000 00000000			3132 DC XL16'3FF8000000000000BFF00000000000000'
00018000	C4C9C4C2 D9409996			3133 DC CL48'DIDBR rounding test 5i NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00018030	BFE00000 00000000			3134 DC XL16'BFE00000000000000800000000000000'
00018040	C4C9C4C2 D9409996			3135 DC CL48'DIDBR rounding test 5i TR'
00018070	BFE00000 00000000			3136 DC XL16'BFE00000000000000800000000000000'
00018080	C4C9C4C2 D9409996			3137 DC CL48'DIDBR rounding test 5j NT'
000180B0	BFE00000 00000000			3138 DC XL16'BFE00000000000000800000000000000'
000180C0	C4C9C4C2 D9409996			3139 DC CL48'DIDBR rounding test 5j TR'
000180F0	BFE00000 00000000			3140 DC XL16'BFE00000000000000800000000000000'
00018100	C4C9C4C2 D9409996			3141 DC CL48'DIDBR rounding test 5k NT'
00018130	BFE00000 00000000			3142 DC XL16'BFE00000000000000800000000000000'
00018140	C4C9C4C2 D9409996			3143 DC CL48'DIDBR rounding test 5k TR'
00018170	BFE00000 00000000			3144 DC XL16'BFE00000000000000800000000000000'
00018180	C4C9C4C2 D9409996			3145 DC CL48'DIDBR rounding test 5l NT'
000181B0	3FF80000 00000000			3146 DC XL16'3FF8000000000000BFF0000000000000'
000181C0	C4C9C4C2 D9409996			3147 DC CL48'DIDBR rounding test 5l TR'
000181F0	3FF80000 00000000			3148 DC XL16'3FF8000000000000BFF0000000000000'
00018200	C4C9C4C2 D9409996			3149 DC CL48'DIDBR rounding test 5m NT'
00018230	BFE00000 00000000			3150 DC XL16'BFE00000000000000800000000000000'
00018240	C4C9C4C2 D9409996			3151 DC CL48'DIDBR rounding test 5m TR'
00018270	BFE00000 00000000			3152 DC XL16'BFE00000000000000800000000000000'
00018280	C4C9C4C2 D9409996			3153 DC CL48'DIDBR rounding test 5n NT'
000182B0	3FF80000 00000000			3154 DC XL16'3FF8000000000000BFF0000000000000'
000182C0	C4C9C4C2 D9409996			3155 DC CL48'DIDBR rounding test 5n TR'
000182F0	3FF80000 00000000			3156 DC XL16'3FF8000000000000BFF0000000000000'
00018300	C4C9C4C2 D9409996			3157 DC CL48'DIDBR rounding test 5o NT'
00018330	BFE00000 00000000			3158 DC XL16'BFE00000000000000800000000000000'
00018340	C4C9C4C2 D9409996			3159 DC CL48'DIDBR rounding test 5o TR'
00018370	BFE00000 00000000			3160 DC XL16'BFE00000000000000800000000000000'
00018380	C4C9C4C2 D9409996			3161 DC CL48'DIDBR rounding test 5p NT'
000183B0	BFE00000 00000000			3162 DC XL16'BFE00000000000000800000000000000'
000183C0	C4C9C4C2 D9409996			3163 DC CL48'DIDBR rounding test 5p TR'
000183F0	BFE00000 00000000			3164 DC XL16'BFE00000000000000800000000000000'
00018400	C4C9C4C2 D9409996			3165 DC CL48'DIDBR rounding test 5q NT'
00018430	BFE00000 00000000			3166 DC XL16'BFE00000000000000800000000000000'
00018440	C4C9C4C2 D9409996			3167 DC CL48'DIDBR rounding test 5q TR'
00018470	BFE00000 00000000			3168 DC XL16'BFE00000000000000800000000000000'
00018480	C4C9C4C2 D9409996			3169 DC CL48'DIDBR rounding test 5r NT'
000184B0	3FF80000 00000000			3170 DC XL16'3FF8000000000000BFF0000000000000'
000184C0	C4C9C4C2 D9409996			3171 DC CL48'DIDBR rounding test 5r TR'
000184F0	3FF80000 00000000			3172 DC XL16'3FF8000000000000BFF0000000000000'
00018500	C4C9C4C2 D9409996			3173 DC CL48'DIDBR rounding test 5s NT'
00018530	BFE00000 00000000			3174 DC XL16'BFE00000000000000800000000000000'
00018540	C4C9C4C2 D9409996			3175 DC CL48'DIDBR rounding test 5s TR'
00018570	BFE00000 00000000			3176 DC XL16'BFE00000000000000800000000000000'
00018580	C4C9C4C2 D9409996			3177 DC CL48'DIDBR rounding test 5t NT'
000185B0	3FF80000 00000000			3178 DC XL16'3FF8000000000000BFF0000000000000'
000185C0	C4C9C4C2 D9409996			3179 DC CL48'DIDBR rounding test 5t TR'
000185F0	3FF80000 00000000			3180 DC XL16'3FF8000000000000BFF0000000000000'
00018600	C4C9C4C2 D9409996			3181 DC CL48'DIDBR rounding test 5u NT'
00018630	BFE00000 00000000			3182 DC XL16'BFE00000000000000800000000000000'
00018640	C4C9C4C2 D9409996			3183 DC CL48'DIDBR rounding test 5u TR'
00018670	BFE00000 00000000			3184 DC XL16'BFE00000000000000800000000000000'
00018680	C4C9C4C2 D9409996			3185 DC CL48'DIDBR rounding test 5v NT'
000186B0	BFE00000 00000000			3186 DC XL16'BFE00000000000000800000000000000'
000186C0	C4C9C4C2 D9409996			3187 DC CL48'DIDBR rounding test 5v TR'
000186F0	BFE00000 00000000			3188 DC XL16'BFE00000000000000800000000000000'
00018700	C4C9C4C2 D9409996			3189 DC CL48'DIDBR rounding test 5w NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00018730	BFE00000 00000000			3190 DC XL16'BFE00000000000000800000000000000'
00018740	C4C9C4C2 D9409996			3191 DC CL48'DIDBR rounding test 5w TR'
00018770	BFE00000 00000000			3192 DC XL16'BFE00000000000000800000000000000'
00018780	C4C9C4C2 D9409996			3193 DC CL48'DIDBR rounding test 5x NT'
000187B0	3FF80000 00000000			3194 DC XL16'3FF8000000000000BFF00000000000000'
000187C0	C4C9C4C2 D9409996			3195 DC CL48'DIDBR rounding test 5x TR'
000187F0	3FF80000 00000000			3196 DC XL16'3FF8000000000000BFF00000000000000'
00018800	C4C9C4C2 D9409996			3197 DC CL48'DIDBR rounding test 6a NT'
00018830	3FE00000 00000000			3198 DC XL16'3FE00000000000000000000000000000'
00018840	C4C9C4C2 D9409996			3199 DC CL48'DIDBR rounding test 6a TR'
00018870	3FE00000 00000000			3200 DC XL16'3FE00000000000000000000000000000'
00018880	C4C9C4C2 D9409996			3201 DC CL48'DIDBR rounding test 6b NT'
000188B0	BFF80000 00000000			3202 DC XL16'BFF800000000000003FF00000000000000'
000188C0	C4C9C4C2 D9409996			3203 DC CL48'DIDBR rounding test 6b TR'
000188F0	BFF80000 00000000			3204 DC XL16'BFF800000000000003FF00000000000000'
00018900	C4C9C4C2 D9409996			3205 DC CL48'DIDBR rounding test 6c NT'
00018930	3FE00000 00000000			3206 DC XL16'3FE00000000000000000000000000000'
00018940	C4C9C4C2 D9409996			3207 DC CL48'DIDBR rounding test 6c TR'
00018970	3FE00000 00000000			3208 DC XL16'3FE00000000000000000000000000000'
00018980	C4C9C4C2 D9409996			3209 DC CL48'DIDBR rounding test 6d NT'
000189B0	3FE00000 00000000			3210 DC XL16'3FE00000000000000000000000000000'
000189C0	C4C9C4C2 D9409996			3211 DC CL48'DIDBR rounding test 6d TR'
000189F0	3FE00000 00000000			3212 DC XL16'3FE00000000000000000000000000000'
00018A00	C4C9C4C2 D9409996			3213 DC CL48'DIDBR rounding test 6e NT'
00018A30	BFF80000 00000000			3214 DC XL16'BFF800000000000003FF00000000000000'
00018A40	C4C9C4C2 D9409996			3215 DC CL48'DIDBR rounding test 6e TR'
00018A70	BFF80000 00000000			3216 DC XL16'BFF800000000000003FF00000000000000'
00018A80	C4C9C4C2 D9409996			3217 DC CL48'DIDBR rounding test 6f NT'
00018AB0	3FE00000 00000000			3218 DC XL16'3FE00000000000000000000000000000'
00018AC0	C4C9C4C2 D9409996			3219 DC CL48'DIDBR rounding test 6f TR'
00018AF0	3FE00000 00000000			3220 DC XL16'3FE00000000000000000000000000000'
00018B00	C4C9C4C2 D9409996			3221 DC CL48'DIDBR rounding test 6g NT'
00018B30	3FE00000 00000000			3222 DC XL16'3FE00000000000000000000000000000'
00018B40	C4C9C4C2 D9409996			3223 DC CL48'DIDBR rounding test 6g TR'
00018B70	3FE00000 00000000			3224 DC XL16'3FE00000000000000000000000000000'
00018B80	C4C9C4C2 D9409996			3225 DC CL48'DIDBR rounding test 6h NT'
00018BB0	BFF80000 00000000			3226 DC XL16'BFF800000000000003FF00000000000000'
00018BC0	C4C9C4C2 D9409996			3227 DC CL48'DIDBR rounding test 6h TR'
00018BF0	BFF80000 00000000			3228 DC XL16'BFF800000000000003FF00000000000000'
00018C00	C4C9C4C2 D9409996			3229 DC CL48'DIDBR rounding test 6i NT'
00018C30	3FE00000 00000000			3230 DC XL16'3FE00000000000000000000000000000'
00018C40	C4C9C4C2 D9409996			3231 DC CL48'DIDBR rounding test 6i TR'
00018C70	3FE00000 00000000			3232 DC XL16'3FE00000000000000000000000000000'
00018C80	C4C9C4C2 D9409996			3233 DC CL48'DIDBR rounding test 6j NT'
00018CB0	3FE00000 00000000			3234 DC XL16'3FE00000000000000000000000000000'
00018CC0	C4C9C4C2 D9409996			3235 DC CL48'DIDBR rounding test 6j TR'
00018CF0	3FE00000 00000000			3236 DC XL16'3FE00000000000000000000000000000'
00018D00	C4C9C4C2 D9409996			3237 DC CL48'DIDBR rounding test 6k NT'
00018D30	BFF80000 00000000			3238 DC XL16'BFF800000000000003FF00000000000000'
00018D40	C4C9C4C2 D9409996			3239 DC CL48'DIDBR rounding test 6k TR'
00018D70	BFF80000 00000000			3240 DC XL16'BFF800000000000003FF00000000000000'
00018D80	C4C9C4C2 D9409996			3241 DC CL48'DIDBR rounding test 6l NT'
00018DB0	3FE00000 00000000			3242 DC XL16'3FE00000000000000000000000000000'
00018DC0	C4C9C4C2 D9409996			3243 DC CL48'DIDBR rounding test 6l TR'
00018DF0	3FE00000 00000000			3244 DC XL16'3FE00000000000000000000000000000'
00018E00	C4C9C4C2 D9409996			3245 DC CL48'DIDBR rounding test 6m NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00018E30	3FE00000 00000000			3246	DC XL16'3FE00000000000000000000000000000'
00018E40	C4C9C4C2 D9409996			3247	DC CL48'DIDBR rounding test 6m TR'
00018E70	3FE00000 00000000			3248	DC XL16'3FE00000000000000000000000000000'
00018E80	C4C9C4C2 D9409996			3249	DC CL48'DIDBR rounding test 6n NT'
00018EB0	BFF80000 00000000			3250	DC XL16'BFF800000000000003FF0000000000000'
00018EC0	C4C9C4C2 D9409996			3251	DC CL48'DIDBR rounding test 6n TR'
00018EF0	BFF80000 00000000			3252	DC XL16'BFF800000000000003FF0000000000000'
00018F00	C4C9C4C2 D9409996			3253	DC CL48'DIDBR rounding test 6o NT'
00018F30	3FE00000 00000000			3254	DC XL16'3FE00000000000000000000000000000'
00018F40	C4C9C4C2 D9409996			3255	DC CL48'DIDBR rounding test 6o TR'
00018F70	3FE00000 00000000			3256	DC XL16'3FE00000000000000000000000000000'
00018F80	C4C9C4C2 D9409996			3257	DC CL48'DIDBR rounding test 6p NT'
00018FB0	3FE00000 00000000			3258	DC XL16'3FE00000000000000000000000000000'
00018FC0	C4C9C4C2 D9409996			3259	DC CL48'DIDBR rounding test 6p TR'
00018FF0	3FE00000 00000000			3260	DC XL16'3FE00000000000000000000000000000'
00019000	C4C9C4C2 D9409996			3261	DC CL48'DIDBR rounding test 6q NT'
00019030	BFF80000 00000000			3262	DC XL16'BFF800000000000003FF0000000000000'
00019040	C4C9C4C2 D9409996			3263	DC CL48'DIDBR rounding test 6q TR'
00019070	BFF80000 00000000			3264	DC XL16'BFF800000000000003FF0000000000000'
00019080	C4C9C4C2 D9409996			3265	DC CL48'DIDBR rounding test 6r NT'
000190B0	3FE00000 00000000			3266	DC XL16'3FE00000000000000000000000000000'
000190C0	C4C9C4C2 D9409996			3267	DC CL48'DIDBR rounding test 6r TR'
000190F0	3FE00000 00000000			3268	DC XL16'3FE00000000000000000000000000000'
00019100	C4C9C4C2 D9409996			3269	DC CL48'DIDBR rounding test 6s NT'
00019130	3FE00000 00000000			3270	DC XL16'3FE00000000000000000000000000000'
00019140	C4C9C4C2 D9409996			3271	DC CL48'DIDBR rounding test 6s TR'
00019170	3FE00000 00000000			3272	DC XL16'3FE00000000000000000000000000000'
00019180	C4C9C4C2 D9409996			3273	DC CL48'DIDBR rounding test 6t NT'
000191B0	BFF80000 00000000			3274	DC XL16'BFF800000000000003FF0000000000000'
000191C0	C4C9C4C2 D9409996			3275	DC CL48'DIDBR rounding test 6t TR'
000191F0	BFF80000 00000000			3276	DC XL16'BFF800000000000003FF0000000000000'
00019200	C4C9C4C2 D9409996			3277	DC CL48'DIDBR rounding test 6u NT'
00019230	3FE00000 00000000			3278	DC XL16'3FE00000000000000000000000000000'
00019240	C4C9C4C2 D9409996			3279	DC CL48'DIDBR rounding test 6u TR'
00019270	3FE00000 00000000			3280	DC XL16'3FE00000000000000000000000000000'
00019280	C4C9C4C2 D9409996			3281	DC CL48'DIDBR rounding test 6v NT'
000192B0	3FE00000 00000000			3282	DC XL16'3FE00000000000000000000000000000'
000192C0	C4C9C4C2 D9409996			3283	DC CL48'DIDBR rounding test 6v TR'
000192F0	3FE00000 00000000			3284	DC XL16'3FE00000000000000000000000000000'
00019300	C4C9C4C2 D9409996			3285	DC CL48'DIDBR rounding test 6w NT'
00019330	BFF80000 00000000			3286	DC XL16'BFF800000000000003FF0000000000000'
00019340	C4C9C4C2 D9409996			3287	DC CL48'DIDBR rounding test 6w TR'
00019370	BFF80000 00000000			3288	DC XL16'BFF800000000000003FF0000000000000'
00019380	C4C9C4C2 D9409996			3289	DC CL48'DIDBR rounding test 6x NT'
000193B0	3FE00000 00000000			3290	DC XL16'3FE00000000000000000000000000000'
000193C0	C4C9C4C2 D9409996			3291	DC CL48'DIDBR rounding test 6x TR'
000193F0	3FE00000 00000000			3292	DC XL16'3FE00000000000000000000000000000'
00019400	C4C9C4C2 D9409996			3293	DC CL48'DIDBR rounding test 7a NT'
00019430	BFE00000 00000000			3294	DC XL16'BFE000000000000003FF0000000000000'
00019440	C4C9C4C2 D9409996			3295	DC CL48'DIDBR rounding test 7a TR'
00019470	BFE00000 00000000			3296	DC XL16'BFE000000000000003FF0000000000000'
00019480	C4C9C4C2 D9409996			3297	DC CL48'DIDBR rounding test 7b NT'
000194B0	BFE00000 00000000			3298	DC XL16'BFE000000000000003FF0000000000000'
000194C0	C4C9C4C2 D9409996			3299	DC CL48'DIDBR rounding test 7b TR'
000194F0	BFE00000 00000000			3300	DC XL16'BFE000000000000003FF0000000000000'
00019500	C4C9C4C2 D9409996			3301	DC CL48'DIDBR rounding test 7c NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00019530	BFE00000 00000000			3302 DC XL16'BFE000000000000003FF0000000000000'
00019540	C4C9C4C2 D9409996			3303 DC CL48'DIDBR rounding test 7c TR'
00019570	BFE00000 00000000			3304 DC XL16'BFE000000000000003FF0000000000000'
00019580	C4C9C4C2 D9409996			3305 DC CL48'DIDBR rounding test 7d NT'
000195B0	3FF80000 00000000			3306 DC XL16'3FF80000000000000000000000000000'
000195C0	C4C9C4C2 D9409996			3307 DC CL48'DIDBR rounding test 7d TR'
000195F0	3FF80000 00000000			3308 DC XL16'3FF80000000000000000000000000000'
00019600	C4C9C4C2 D9409996			3309 DC CL48'DIDBR rounding test 7e NT'
00019630	BFE00000 00000000			3310 DC XL16'BFE000000000000003FF0000000000000'
00019640	C4C9C4C2 D9409996			3311 DC CL48'DIDBR rounding test 7e TR'
00019670	BFE00000 00000000			3312 DC XL16'BFE000000000000003FF0000000000000'
00019680	C4C9C4C2 D9409996			3313 DC CL48'DIDBR rounding test 7f NT'
000196B0	3FF80000 00000000			3314 DC XL16'3FF80000000000000000000000000000'
000196C0	C4C9C4C2 D9409996			3315 DC CL48'DIDBR rounding test 7f TR'
000196F0	3FF80000 00000000			3316 DC XL16'3FF80000000000000000000000000000'
00019700	C4C9C4C2 D9409996			3317 DC CL48'DIDBR rounding test 7g NT'
00019730	BFE00000 00000000			3318 DC XL16'BFE000000000000003FF0000000000000'
00019740	C4C9C4C2 D9409996			3319 DC CL48'DIDBR rounding test 7g TR'
00019770	BFE00000 00000000			3320 DC XL16'BFE000000000000003FF0000000000000'
00019780	C4C9C4C2 D9409996			3321 DC CL48'DIDBR rounding test 7h NT'
000197B0	BFE00000 00000000			3322 DC XL16'BFE000000000000003FF0000000000000'
000197C0	C4C9C4C2 D9409996			3323 DC CL48'DIDBR rounding test 7h TR'
000197F0	BFE00000 00000000			3324 DC XL16'BFE000000000000003FF0000000000000'
00019800	C4C9C4C2 D9409996			3325 DC CL48'DIDBR rounding test 7i NT'
00019830	BFE00000 00000000			3326 DC XL16'BFE000000000000003FF0000000000000'
00019840	C4C9C4C2 D9409996			3327 DC CL48'DIDBR rounding test 7i TR'
00019870	BFE00000 00000000			3328 DC XL16'BFE000000000000003FF0000000000000'
00019880	C4C9C4C2 D9409996			3329 DC CL48'DIDBR rounding test 7j NT'
000198B0	3FF80000 00000000			3330 DC XL16'3FF80000000000000000000000000000'
000198C0	C4C9C4C2 D9409996			3331 DC CL48'DIDBR rounding test 7j TR'
000198F0	3FF80000 00000000			3332 DC XL16'3FF80000000000000000000000000000'
00019900	C4C9C4C2 D9409996			3333 DC CL48'DIDBR rounding test 7k NT'
00019930	BFE00000 00000000			3334 DC XL16'BFE000000000000003FF0000000000000'
00019940	C4C9C4C2 D9409996			3335 DC CL48'DIDBR rounding test 7k TR'
00019970	BFE00000 00000000			3336 DC XL16'BFE000000000000003FF0000000000000'
00019980	C4C9C4C2 D9409996			3337 DC CL48'DIDBR rounding test 7l NT'
000199B0	3FF80000 00000000			3338 DC XL16'3FF80000000000000000000000000000'
000199C0	C4C9C4C2 D9409996			3339 DC CL48'DIDBR rounding test 7l TR'
000199F0	3FF80000 00000000			3340 DC XL16'3FF80000000000000000000000000000'
00019A00	C4C9C4C2 D9409996			3341 DC CL48'DIDBR rounding test 7m NT'
00019A30	BFE00000 00000000			3342 DC XL16'BFE000000000000003FF0000000000000'
00019A40	C4C9C4C2 D9409996			3343 DC CL48'DIDBR rounding test 7m TR'
00019A70	BFE00000 00000000			3344 DC XL16'BFE000000000000003FF0000000000000'
00019A80	C4C9C4C2 D9409996			3345 DC CL48'DIDBR rounding test 7n NT'
00019AB0	BFE00000 00000000			3346 DC XL16'BFE000000000000003FF0000000000000'
00019AC0	C4C9C4C2 D9409996			3347 DC CL48'DIDBR rounding test 7n TR'
00019AF0	BFE00000 00000000			3348 DC XL16'BFE000000000000003FF0000000000000'
00019B00	C4C9C4C2 D9409996			3349 DC CL48'DIDBR rounding test 7o NT'
00019B30	BFE00000 00000000			3350 DC XL16'BFE000000000000003FF0000000000000'
00019B40	C4C9C4C2 D9409996			3351 DC CL48'DIDBR rounding test 7o TR'
00019B70	BFE00000 00000000			3352 DC XL16'BFE000000000000003FF0000000000000'
00019B80	C4C9C4C2 D9409996			3353 DC CL48'DIDBR rounding test 7p NT'
00019BB0	3FF80000 00000000			3354 DC XL16'3FF80000000000000000000000000000'
00019BC0	C4C9C4C2 D9409996			3355 DC CL48'DIDBR rounding test 7p TR'
00019BF0	3FF80000 00000000			3356 DC XL16'3FF80000000000000000000000000000'
00019C00	C4C9C4C2 D9409996			3357 DC CL48'DIDBR rounding test 7q NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00019C30	BFE00000 00000000			3358 DC XL16'BFE000000000000003FF0000000000000'
00019C40	C4C9C4C2 D9409996			3359 DC CL48'DIDBR rounding test 7q TR'
00019C70	BFE00000 00000000			3360 DC XL16'BFE000000000000003FF0000000000000'
00019C80	C4C9C4C2 D9409996			3361 DC CL48'DIDBR rounding test 7r NT'
00019CB0	3FF80000 00000000			3362 DC XL16'3FF80000000000000000000000000000'
00019CC0	C4C9C4C2 D9409996			3363 DC CL48'DIDBR rounding test 7r TR'
00019CF0	3FF80000 00000000			3364 DC XL16'3FF80000000000000000000000000000'
00019D00	C4C9C4C2 D9409996			3365 DC CL48'DIDBR rounding test 7s NT'
00019D30	BFE00000 00000000			3366 DC XL16'BFE000000000000003FF0000000000000'
00019D40	C4C9C4C2 D9409996			3367 DC CL48'DIDBR rounding test 7s TR'
00019D70	BFE00000 00000000			3368 DC XL16'BFE000000000000003FF0000000000000'
00019D80	C4C9C4C2 D9409996			3369 DC CL48'DIDBR rounding test 7t NT'
00019DB0	BFE00000 00000000			3370 DC XL16'BFE000000000000003FF0000000000000'
00019DC0	C4C9C4C2 D9409996			3371 DC CL48'DIDBR rounding test 7t TR'
00019DF0	BFE00000 00000000			3372 DC XL16'BFE000000000000003FF0000000000000'
00019E00	C4C9C4C2 D9409996			3373 DC CL48'DIDBR rounding test 7u NT'
00019E30	BFE00000 00000000			3374 DC XL16'BFE000000000000003FF0000000000000'
00019E40	C4C9C4C2 D9409996			3375 DC CL48'DIDBR rounding test 7u TR'
00019E70	BFE00000 00000000			3376 DC XL16'BFE000000000000003FF0000000000000'
00019E80	C4C9C4C2 D9409996			3377 DC CL48'DIDBR rounding test 7v NT'
00019EB0	3FF80000 00000000			3378 DC XL16'3FF80000000000000000000000000000'
00019EC0	C4C9C4C2 D9409996			3379 DC CL48'DIDBR rounding test 7v TR'
00019EF0	3FF80000 00000000			3380 DC XL16'3FF80000000000000000000000000000'
00019F00	C4C9C4C2 D9409996			3381 DC CL48'DIDBR rounding test 7w NT'
00019F30	BFE00000 00000000			3382 DC XL16'BFE000000000000003FF0000000000000'
00019F40	C4C9C4C2 D9409996			3383 DC CL48'DIDBR rounding test 7w TR'
00019F70	BFE00000 00000000			3384 DC XL16'BFE000000000000003FF0000000000000'
00019F80	C4C9C4C2 D9409996			3385 DC CL48'DIDBR rounding test 7x NT'
00019FB0	3FF80000 00000000			3386 DC XL16'3FF80000000000000000000000000000'
00019FC0	C4C9C4C2 D9409996			3387 DC CL48'DIDBR rounding test 7x TR'
00019FF0	3FF80000 00000000			3388 DC XL16'3FF80000000000000000000000000000'
0001A000	C4C9C4C2 D9409996			3389 DC CL48'DIDBR rounding test 8a NT'
0001A030	3FE00000 00000000			3390 DC XL16'3FE000000000000003FF0000000000000'
0001A040	C4C9C4C2 D9409996			3391 DC CL48'DIDBR rounding test 8a TR'
0001A070	3FE00000 00000000			3392 DC XL16'3FE000000000000003FF0000000000000'
0001A080	C4C9C4C2 D9409996			3393 DC CL48'DIDBR rounding test 8b NT'
0001A0B0	3FE00000 00000000			3394 DC XL16'3FE000000000000003FF0000000000000'
0001A0C0	C4C9C4C2 D9409996			3395 DC CL48'DIDBR rounding test 8b TR'
0001A0F0	3FE00000 00000000			3396 DC XL16'3FE000000000000003FF0000000000000'
0001A100	C4C9C4C2 D9409996			3397 DC CL48'DIDBR rounding test 8c NT'
0001A130	3FE00000 00000000			3398 DC XL16'3FE000000000000003FF0000000000000'
0001A140	C4C9C4C2 D9409996			3399 DC CL48'DIDBR rounding test 8c TR'
0001A170	3FE00000 00000000			3400 DC XL16'3FE000000000000003FF0000000000000'
0001A180	C4C9C4C2 D9409996			3401 DC CL48'DIDBR rounding test 8d NT'
0001A1B0	3FE00000 00000000			3402 DC XL16'3FE000000000000003FF0000000000000'
0001A1C0	C4C9C4C2 D9409996			3403 DC CL48'DIDBR rounding test 8d TR'
0001A1F0	3FE00000 00000000			3404 DC XL16'3FE000000000000003FF0000000000000'
0001A200	C4C9C4C2 D9409996			3405 DC CL48'DIDBR rounding test 8e NT'
0001A230	BFF80000 00000000			3406 DC XL16'BFF80000000000000400000000000000'
0001A240	C4C9C4C2 D9409996			3407 DC CL48'DIDBR rounding test 8e TR'
0001A270	BFF80000 00000000			3408 DC XL16'BFF80000000000000400000000000000'
0001A280	C4C9C4C2 D9409996			3409 DC CL48'DIDBR rounding test 8f NT'
0001A2B0	3FE00000 00000000			3410 DC XL16'3FE000000000000003FF0000000000000'
0001A2C0	C4C9C4C2 D9409996			3411 DC CL48'DIDBR rounding test 8f TR'
0001A2F0	3FE00000 00000000			3412 DC XL16'3FE000000000000003FF0000000000000'
0001A300	C4C9C4C2 D9409996			3413 DC CL48'DIDBR rounding test 8g NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001A330	3FE00000 00000000			3414 DC XL16'3FE000000000000003FF0000000000000'
0001A340	C4C9C4C2 D9409996			3415 DC CL48'DIDBR rounding test 8g TR'
0001A370	3FE00000 00000000			3416 DC XL16'3FE000000000000003FF0000000000000'
0001A380	C4C9C4C2 D9409996			3417 DC CL48'DIDBR rounding test 8h NT'
0001A3B0	3FE00000 00000000			3418 DC XL16'3FE000000000000003FF0000000000000'
0001A3C0	C4C9C4C2 D9409996			3419 DC CL48'DIDBR rounding test 8h TR'
0001A3F0	3FE00000 00000000			3420 DC XL16'3FE000000000000003FF0000000000000'
0001A400	C4C9C4C2 D9409996			3421 DC CL48'DIDBR rounding test 8i NT'
0001A430	3FE00000 00000000			3422 DC XL16'3FE000000000000003FF0000000000000'
0001A440	C4C9C4C2 D9409996			3423 DC CL48'DIDBR rounding test 8i TR'
0001A470	3FE00000 00000000			3424 DC XL16'3FE000000000000003FF0000000000000'
0001A480	C4C9C4C2 D9409996			3425 DC CL48'DIDBR rounding test 8j NT'
0001A4B0	3FE00000 00000000			3426 DC XL16'3FE000000000000003FF0000000000000'
0001A4C0	C4C9C4C2 D9409996			3427 DC CL48'DIDBR rounding test 8j TR'
0001A4F0	3FE00000 00000000			3428 DC XL16'3FE000000000000003FF0000000000000'
0001A500	C4C9C4C2 D9409996			3429 DC CL48'DIDBR rounding test 8k NT'
0001A530	BFF80000 00000000			3430 DC XL16'BFF80000000000000400000000000000'
0001A540	C4C9C4C2 D9409996			3431 DC CL48'DIDBR rounding test 8k TR'
0001A570	BFF80000 00000000			3432 DC XL16'BFF80000000000000400000000000000'
0001A580	C4C9C4C2 D9409996			3433 DC CL48'DIDBR rounding test 8l NT'
0001A5B0	3FE00000 00000000			3434 DC XL16'3FE000000000000003FF0000000000000'
0001A5C0	C4C9C4C2 D9409996			3435 DC CL48'DIDBR rounding test 8l TR'
0001A5F0	3FE00000 00000000			3436 DC XL16'3FE000000000000003FF0000000000000'
0001A600	C4C9C4C2 D9409996			3437 DC CL48'DIDBR rounding test 8m NT'
0001A630	3FE00000 00000000			3438 DC XL16'3FE000000000000003FF0000000000000'
0001A640	C4C9C4C2 D9409996			3439 DC CL48'DIDBR rounding test 8m TR'
0001A670	3FE00000 00000000			3440 DC XL16'3FE000000000000003FF0000000000000'
0001A680	C4C9C4C2 D9409996			3441 DC CL48'DIDBR rounding test 8n NT'
0001A6B0	3FE00000 00000000			3442 DC XL16'3FE000000000000003FF0000000000000'
0001A6C0	C4C9C4C2 D9409996			3443 DC CL48'DIDBR rounding test 8n TR'
0001A6F0	3FE00000 00000000			3444 DC XL16'3FE000000000000003FF0000000000000'
0001A700	C4C9C4C2 D9409996			3445 DC CL48'DIDBR rounding test 8o NT'
0001A730	3FE00000 00000000			3446 DC XL16'3FE000000000000003FF0000000000000'
0001A740	C4C9C4C2 D9409996			3447 DC CL48'DIDBR rounding test 8o TR'
0001A770	3FE00000 00000000			3448 DC XL16'3FE000000000000003FF0000000000000'
0001A780	C4C9C4C2 D9409996			3449 DC CL48'DIDBR rounding test 8p NT'
0001A7B0	3FE00000 00000000			3450 DC XL16'3FE000000000000003FF0000000000000'
0001A7C0	C4C9C4C2 D9409996			3451 DC CL48'DIDBR rounding test 8p TR'
0001A7F0	3FE00000 00000000			3452 DC XL16'3FE000000000000003FF0000000000000'
0001A800	C4C9C4C2 D9409996			3453 DC CL48'DIDBR rounding test 8q NT'
0001A830	BFF80000 00000000			3454 DC XL16'BFF80000000000000400000000000000'
0001A840	C4C9C4C2 D9409996			3455 DC CL48'DIDBR rounding test 8q TR'
0001A870	BFF80000 00000000			3456 DC XL16'BFF80000000000000400000000000000'
0001A880	C4C9C4C2 D9409996			3457 DC CL48'DIDBR rounding test 8r NT'
0001A8B0	3FE00000 00000000			3458 DC XL16'3FE000000000000003FF0000000000000'
0001A8C0	C4C9C4C2 D9409996			3459 DC CL48'DIDBR rounding test 8r TR'
0001A8F0	3FE00000 00000000			3460 DC XL16'3FE000000000000003FF0000000000000'
0001A900	C4C9C4C2 D9409996			3461 DC CL48'DIDBR rounding test 8s NT'
0001A930	3FE00000 00000000			3462 DC XL16'3FE000000000000003FF0000000000000'
0001A940	C4C9C4C2 D9409996			3463 DC CL48'DIDBR rounding test 8s TR'
0001A970	3FE00000 00000000			3464 DC XL16'3FE000000000000003FF0000000000000'
0001A980	C4C9C4C2 D9409996			3465 DC CL48'DIDBR rounding test 8t NT'
0001A9B0	3FE00000 00000000			3466 DC XL16'3FE000000000000003FF0000000000000'
0001A9C0	C4C9C4C2 D9409996			3467 DC CL48'DIDBR rounding test 8t TR'
0001A9F0	3FE00000 00000000			3468 DC XL16'3FE000000000000003FF0000000000000'
0001AA00	C4C9C4C2 D9409996			3469 DC CL48'DIDBR rounding test 8u NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001AA30	3FE00000 00000000			3470 DC XL16'3FE000000000000003FF0000000000000'
0001AA40	C4C9C4C2 D9409996			3471 DC CL48'DIDBR rounding test 8u TR'
0001AA70	3FE00000 00000000			3472 DC XL16'3FE000000000000003FF0000000000000'
0001AA80	C4C9C4C2 D9409996			3473 DC CL48'DIDBR rounding test 8v NT'
0001AAB0	3FE00000 00000000			3474 DC XL16'3FE000000000000003FF0000000000000'
0001AAC0	C4C9C4C2 D9409996			3475 DC CL48'DIDBR rounding test 8v TR'
0001AAF0	3FE00000 00000000			3476 DC XL16'3FE000000000000003FF0000000000000'
0001AB00	C4C9C4C2 D9409996			3477 DC CL48'DIDBR rounding test 8w NT'
0001AB30	BFF80000 00000000			3478 DC XL16'BFF80000000000000400000000000000'
0001AB40	C4C9C4C2 D9409996			3479 DC CL48'DIDBR rounding test 8w TR'
0001AB70	BFF80000 00000000			3480 DC XL16'BFF8000000000000400000000000000'
0001AB80	C4C9C4C2 D9409996			3481 DC CL48'DIDBR rounding test 8x NT'
0001ABB0	3FE00000 00000000			3482 DC XL16'3FE000000000000003FF0000000000000'
0001ABC0	C4C9C4C2 D9409996			3483 DC CL48'DIDBR rounding test 8x TR'
0001ABF0	3FE00000 00000000			3484 DC XL16'3FE000000000000003FF0000000000000'
0001AC00	C4C9C4C2 D9409996			3485 DC CL48'DIDBR rounding test 9a NT'
0001AC30	BFE00000 00000000			3486 DC XL16'BFE00000000000000400800000000000'
0001AC40	C4C9C4C2 D9409996			3487 DC CL48'DIDBR rounding test 9a TR'
0001AC70	BFE00000 00000000			3488 DC XL16'BFE0000000000000400800000000000'
0001AC80	C4C9C4C2 D9409996			3489 DC CL48'DIDBR rounding test 9b NT'
0001ACB0	BFE00000 00000000			3490 DC XL16'BFE0000000000000400800000000000'
0001ACC0	C4C9C4C2 D9409996			3491 DC CL48'DIDBR rounding test 9b TR'
0001ACF0	BFE00000 00000000			3492 DC XL16'BFE0000000000000400800000000000'
0001AD00	C4C9C4C2 D9409996			3493 DC CL48'DIDBR rounding test 9c NT'
0001AD30	BFE00000 00000000			3494 DC XL16'BFE0000000000000400800000000000'
0001AD40	C4C9C4C2 D9409996			3495 DC CL48'DIDBR rounding test 9c TR'
0001AD70	BFE00000 00000000			3496 DC XL16'BFE0000000000000400800000000000'
0001AD80	C4C9C4C2 D9409996			3497 DC CL48'DIDBR rounding test 9d NT'
0001ADB0	3FF80000 00000000			3498 DC XL16'3FF80000000000000400000000000000'
0001ADC0	C4C9C4C2 D9409996			3499 DC CL48'DIDBR rounding test 9d TR'
0001ADF0	3FF80000 00000000			3500 DC XL16'3FF80000000000000400000000000000'
0001AE00	C4C9C4C2 D9409996			3501 DC CL48'DIDBR rounding test 9e NT'
0001AE30	BFE00000 00000000			3502 DC XL16'BFE0000000000000400800000000000'
0001AE40	C4C9C4C2 D9409996			3503 DC CL48'DIDBR rounding test 9e TR'
0001AE70	BFE00000 00000000			3504 DC XL16'BFE0000000000000400800000000000'
0001AE80	C4C9C4C2 D9409996			3505 DC CL48'DIDBR rounding test 9f NT'
0001AEB0	3FF80000 00000000			3506 DC XL16'3FF80000000000000400000000000000'
0001AEC0	C4C9C4C2 D9409996			3507 DC CL48'DIDBR rounding test 9f TR'
0001AEF0	3FF80000 00000000			3508 DC XL16'3FF80000000000000400000000000000'
0001AF00	C4C9C4C2 D9409996			3509 DC CL48'DIDBR rounding test 9g NT'
0001AF30	BFE00000 00000000			3510 DC XL16'BFE0000000000000400800000000000'
0001AF40	C4C9C4C2 D9409996			3511 DC CL48'DIDBR rounding test 9g TR'
0001AF70	BFE00000 00000000			3512 DC XL16'BFE0000000000000400800000000000'
0001AF80	C4C9C4C2 D9409996			3513 DC CL48'DIDBR rounding test 9h NT'
0001AFB0	BFE00000 00000000			3514 DC XL16'BFE0000000000000400800000000000'
0001AFC0	C4C9C4C2 D9409996			3515 DC CL48'DIDBR rounding test 9h TR'
0001AFF0	BFE00000 00000000			3516 DC XL16'BFE0000000000000400800000000000'
0001B000	C4C9C4C2 D9409996			3517 DC CL48'DIDBR rounding test 9i NT'
0001B030	BFE00000 00000000			3518 DC XL16'BFE0000000000000400800000000000'
0001B040	C4C9C4C2 D9409996			3519 DC CL48'DIDBR rounding test 9i TR'
0001B070	BFE00000 00000000			3520 DC XL16'BFE0000000000000400800000000000'
0001B080	C4C9C4C2 D9409996			3521 DC CL48'DIDBR rounding test 9j NT'
0001B0B0	3FF80000 00000000			3522 DC XL16'3FF80000000000000400000000000000'
0001B0C0	C4C9C4C2 D9409996			3523 DC CL48'DIDBR rounding test 9j TR'
0001B0F0	3FF80000 00000000			3524 DC XL16'3FF80000000000000400000000000000'
0001B100	C4C9C4C2 D9409996			3525 DC CL48'DIDBR rounding test 9k NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001B130	BFE00000 00000000			3526 DC XL16'BFE00000000000000400800000000000'
0001B140	C4C9C4C2 D9409996			3527 DC CL48'DIDBR rounding test 9k TR'
0001B170	BFE00000 00000000			3528 DC XL16'BFE00000000000000400800000000000'
0001B180	C4C9C4C2 D9409996			3529 DC CL48'DIDBR rounding test 9l NT'
0001B1B0	3FF80000 00000000			3530 DC XL16'3FF80000000000000400000000000000'
0001B1C0	C4C9C4C2 D9409996			3531 DC CL48'DIDBR rounding test 9l TR'
0001B1F0	3FF80000 00000000			3532 DC XL16'3FF80000000000000400000000000000'
0001B200	C4C9C4C2 D9409996			3533 DC CL48'DIDBR rounding test 9m NT'
0001B230	BFE00000 00000000			3534 DC XL16'BFE00000000000000400800000000000'
0001B240	C4C9C4C2 D9409996			3535 DC CL48'DIDBR rounding test 9m TR'
0001B270	BFE00000 00000000			3536 DC XL16'BFE00000000000000400800000000000'
0001B280	C4C9C4C2 D9409996			3537 DC CL48'DIDBR rounding test 9n NT'
0001B2B0	BFE00000 00000000			3538 DC XL16'BFE00000000000000400800000000000'
0001B2C0	C4C9C4C2 D9409996			3539 DC CL48'DIDBR rounding test 9n TR'
0001B2F0	BFE00000 00000000			3540 DC XL16'BFE00000000000000400800000000000'
0001B300	C4C9C4C2 D9409996			3541 DC CL48'DIDBR rounding test 9o NT'
0001B330	BFE00000 00000000			3542 DC XL16'BFE00000000000000400800000000000'
0001B340	C4C9C4C2 D9409996			3543 DC CL48'DIDBR rounding test 9o TR'
0001B370	BFE00000 00000000			3544 DC XL16'BFE00000000000000400800000000000'
0001B380	C4C9C4C2 D9409996			3545 DC CL48'DIDBR rounding test 9p NT'
0001B3B0	3FF80000 00000000			3546 DC XL16'3FF80000000000000400000000000000'
0001B3C0	C4C9C4C2 D9409996			3547 DC CL48'DIDBR rounding test 9p TR'
0001B3F0	3FF80000 00000000			3548 DC XL16'3FF80000000000000400000000000000'
0001B400	C4C9C4C2 D9409996			3549 DC CL48'DIDBR rounding test 9q NT'
0001B430	BFE00000 00000000			3550 DC XL16'BFE00000000000000400800000000000'
0001B440	C4C9C4C2 D9409996			3551 DC CL48'DIDBR rounding test 9q TR'
0001B470	BFE00000 00000000			3552 DC XL16'BFE00000000000000400800000000000'
0001B480	C4C9C4C2 D9409996			3553 DC CL48'DIDBR rounding test 9r NT'
0001B4B0	3FF80000 00000000			3554 DC XL16'3FF80000000000000400000000000000'
0001B4C0	C4C9C4C2 D9409996			3555 DC CL48'DIDBR rounding test 9r TR'
0001B4F0	3FF80000 00000000			3556 DC XL16'3FF80000000000000400000000000000'
0001B500	C4C9C4C2 D9409996			3557 DC CL48'DIDBR rounding test 9s NT'
0001B530	BFE00000 00000000			3558 DC XL16'BFE00000000000000400800000000000'
0001B540	C4C9C4C2 D9409996			3559 DC CL48'DIDBR rounding test 9s TR'
0001B570	BFE00000 00000000			3560 DC XL16'BFE00000000000000400800000000000'
0001B580	C4C9C4C2 D9409996			3561 DC CL48'DIDBR rounding test 9t NT'
0001B5B0	BFE00000 00000000			3562 DC XL16'BFE00000000000000400800000000000'
0001B5C0	C4C9C4C2 D9409996			3563 DC CL48'DIDBR rounding test 9t TR'
0001B5F0	BFE00000 00000000			3564 DC XL16'BFE00000000000000400800000000000'
0001B600	C4C9C4C2 D9409996			3565 DC CL48'DIDBR rounding test 9u NT'
0001B630	BFE00000 00000000			3566 DC XL16'BFE00000000000000400800000000000'
0001B640	C4C9C4C2 D9409996			3567 DC CL48'DIDBR rounding test 9u TR'
0001B670	BFE00000 00000000			3568 DC XL16'BFE00000000000000400800000000000'
0001B680	C4C9C4C2 D9409996			3569 DC CL48'DIDBR rounding test 9v NT'
0001B6B0	3FF80000 00000000			3570 DC XL16'3FF80000000000000400000000000000'
0001B6C0	C4C9C4C2 D9409996			3571 DC CL48'DIDBR rounding test 9v TR'
0001B6F0	3FF80000 00000000			3572 DC XL16'3FF80000000000000400000000000000'
0001B700	C4C9C4C2 D9409996			3573 DC CL48'DIDBR rounding test 9w NT'
0001B730	BFE00000 00000000			3574 DC XL16'BFE00000000000000400800000000000'
0001B740	C4C9C4C2 D9409996			3575 DC CL48'DIDBR rounding test 9w TR'
0001B770	BFE00000 00000000			3576 DC XL16'BFE00000000000000400800000000000'
0001B780	C4C9C4C2 D9409996			3577 DC CL48'DIDBR rounding test 9x NT'
0001B7B0	3FF80000 00000000			3578 DC XL16'3FF80000000000000400000000000000'
0001B7C0	C4C9C4C2 D9409996			3579 DC CL48'DIDBR rounding test 9x TR'
0001B7F0	3FF80000 00000000			3580 DC XL16'3FF80000000000000400000000000000'
0001B800	C4C9C4C2 D9409996			3581 DC CL48'DIDBR rounding test 10a NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001B830	BFE00000 00000000			3582 DC XL16'BFE000000000000004014000000000000'
0001B840	C4C9C4C2 D9409996			3583 DC CL48'DIDBR rounding test 10a TR'
0001B870	BFE00000 00000000			3584 DC XL16'BFE000000000000004014000000000000'
0001B880	C4C9C4C2 D9409996			3585 DC CL48'DIDBR rounding test 10b NT'
0001B8B0	BFE00000 00000000			3586 DC XL16'BFE000000000000004014000000000000'
0001B8C0	C4C9C4C2 D9409996			3587 DC CL48'DIDBR rounding test 10b TR'
0001B8F0	BFE00000 00000000			3588 DC XL16'BFE000000000000004014000000000000'
0001B900	C4C9C4C2 D9409996			3589 DC CL48'DIDBR rounding test 10c NT'
0001B930	BFE00000 00000000			3590 DC XL16'BFE000000000000004014000000000000'
0001B940	C4C9C4C2 D9409996			3591 DC CL48'DIDBR rounding test 10c TR'
0001B970	BFE00000 00000000			3592 DC XL16'BFE000000000000004014000000000000'
0001B980	C4C9C4C2 D9409996			3593 DC CL48'DIDBR rounding test 10d NT'
0001B9B0	3FF80000 00000000			3594 DC XL16'3FF8000000000000040100000000000000'
0001B9C0	C4C9C4C2 D9409996			3595 DC CL48'DIDBR rounding test 10d TR'
0001B9F0	3FF80000 00000000			3596 DC XL16'3FF8000000000000040100000000000000'
0001BA00	C4C9C4C2 D9409996			3597 DC CL48'DIDBR rounding test 10e NT'
0001BA30	BFE00000 00000000			3598 DC XL16'BFE000000000000004014000000000000'
0001BA40	C4C9C4C2 D9409996			3599 DC CL48'DIDBR rounding test 10e TR'
0001BA70	BFE00000 00000000			3600 DC XL16'BFE000000000000004014000000000000'
0001BA80	C4C9C4C2 D9409996			3601 DC CL48'DIDBR rounding test 10f NT'
0001BAB0	3FF80000 00000000			3602 DC XL16'3FF8000000000000040100000000000000'
0001BAC0	C4C9C4C2 D9409996			3603 DC CL48'DIDBR rounding test 10f TR'
0001BAF0	3FF80000 00000000			3604 DC XL16'3FF8000000000000040100000000000000'
0001BB00	C4C9C4C2 D9409996			3605 DC CL48'DIDBR rounding test 10g NT'
0001BB30	BFE00000 00000000			3606 DC XL16'BFE000000000000004014000000000000'
0001BB40	C4C9C4C2 D9409996			3607 DC CL48'DIDBR rounding test 10g TR'
0001BB70	BFE00000 00000000			3608 DC XL16'BFE000000000000004014000000000000'
0001BB80	C4C9C4C2 D9409996			3609 DC CL48'DIDBR rounding test 10h NT'
0001BBB0	BFE00000 00000000			3610 DC XL16'BFE000000000000004014000000000000'
0001BBC0	C4C9C4C2 D9409996			3611 DC CL48'DIDBR rounding test 10h TR'
0001BBF0	BFE00000 00000000			3612 DC XL16'BFE000000000000004014000000000000'
0001BC00	C4C9C4C2 D9409996			3613 DC CL48'DIDBR rounding test 10i NT'
0001BC30	BFE00000 00000000			3614 DC XL16'BFE000000000000004014000000000000'
0001BC40	C4C9C4C2 D9409996			3615 DC CL48'DIDBR rounding test 10i TR'
0001BC70	BFE00000 00000000			3616 DC XL16'BFE000000000000004014000000000000'
0001BC80	C4C9C4C2 D9409996			3617 DC CL48'DIDBR rounding test 10j NT'
0001BCB0	3FF80000 00000000			3618 DC XL16'3FF8000000000000040100000000000000'
0001BCC0	C4C9C4C2 D9409996			3619 DC CL48'DIDBR rounding test 10j TR'
0001BCF0	3FF80000 00000000			3620 DC XL16'3FF8000000000000040100000000000000'
0001BD00	C4C9C4C2 D9409996			3621 DC CL48'DIDBR rounding test 10k NT'
0001BD30	BFE00000 00000000			3622 DC XL16'BFE000000000000004014000000000000'
0001BD40	C4C9C4C2 D9409996			3623 DC CL48'DIDBR rounding test 10k TR'
0001BD70	BFE00000 00000000			3624 DC XL16'BFE000000000000004014000000000000'
0001BD80	C4C9C4C2 D9409996			3625 DC CL48'DIDBR rounding test 10l NT'
0001BDB0	3FF80000 00000000			3626 DC XL16'3FF8000000000000040100000000000000'
0001BDC0	C4C9C4C2 D9409996			3627 DC CL48'DIDBR rounding test 10l TR'
0001BDF0	3FF80000 00000000			3628 DC XL16'3FF8000000000000040100000000000000'
0001BE00	C4C9C4C2 D9409996			3629 DC CL48'DIDBR rounding test 10m NT'
0001BE30	BFE00000 00000000			3630 DC XL16'BFE000000000000004014000000000000'
0001BE40	C4C9C4C2 D9409996			3631 DC CL48'DIDBR rounding test 10m TR'
0001BE70	BFE00000 00000000			3632 DC XL16'BFE000000000000004014000000000000'
0001BE80	C4C9C4C2 D9409996			3633 DC CL48'DIDBR rounding test 10n NT'
0001BEB0	BFE00000 00000000			3634 DC XL16'BFE000000000000004014000000000000'
0001BEC0	C4C9C4C2 D9409996			3635 DC CL48'DIDBR rounding test 10n TR'
0001BEF0	BFE00000 00000000			3636 DC XL16'BFE000000000000004014000000000000'
0001BF00	C4C9C4C2 D9409996			3637 DC CL48'DIDBR rounding test 10o NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001BF30	BFE00000 00000000			3638 DC XL16'BFE000000000000004014000000000000'
0001BF40	C4C9C4C2 D9409996			3639 DC CL48'DIDBR rounding test 10o TR'
0001BF70	BFE00000 00000000			3640 DC XL16'BFE000000000000004014000000000000'
0001BF80	C4C9C4C2 D9409996			3641 DC CL48'DIDBR rounding test 10p NT'
0001BFB0	3FF80000 00000000			3642 DC XL16'3FF8000000000000040100000000000000'
0001BFC0	C4C9C4C2 D9409996			3643 DC CL48'DIDBR rounding test 10p TR'
0001BFF0	3FF80000 00000000			3644 DC XL16'3FF8000000000000040100000000000000'
0001C000	C4C9C4C2 D9409996			3645 DC CL48'DIDBR rounding test 10q NT'
0001C030	BFE00000 00000000			3646 DC XL16'BFE000000000000004014000000000000'
0001C040	C4C9C4C2 D9409996			3647 DC CL48'DIDBR rounding test 10q TR'
0001C070	BFE00000 00000000			3648 DC XL16'BFE000000000000004014000000000000'
0001C080	C4C9C4C2 D9409996			3649 DC CL48'DIDBR rounding test 10r NT'
0001C0B0	3FF80000 00000000			3650 DC XL16'3FF8000000000000040100000000000000'
0001C0C0	C4C9C4C2 D9409996			3651 DC CL48'DIDBR rounding test 10r TR'
0001C0F0	3FF80000 00000000			3652 DC XL16'3FF8000000000000040100000000000000'
0001C100	C4C9C4C2 D9409996			3653 DC CL48'DIDBR rounding test 10s NT'
0001C130	BFE00000 00000000			3654 DC XL16'BFE000000000000004014000000000000'
0001C140	C4C9C4C2 D9409996			3655 DC CL48'DIDBR rounding test 10s TR'
0001C170	BFE00000 00000000			3656 DC XL16'BFE000000000000004014000000000000'
0001C180	C4C9C4C2 D9409996			3657 DC CL48'DIDBR rounding test 10t NT'
0001C1B0	BFE00000 00000000			3658 DC XL16'BFE000000000000004014000000000000'
0001C1C0	C4C9C4C2 D9409996			3659 DC CL48'DIDBR rounding test 10t TR'
0001C1F0	BFE00000 00000000			3660 DC XL16'BFE000000000000004014000000000000'
0001C200	C4C9C4C2 D9409996			3661 DC CL48'DIDBR rounding test 10u NT'
0001C230	BFE00000 00000000			3662 DC XL16'BFE000000000000004014000000000000'
0001C240	C4C9C4C2 D9409996			3663 DC CL48'DIDBR rounding test 10u TR'
0001C270	BFE00000 00000000			3664 DC XL16'BFE000000000000004014000000000000'
0001C280	C4C9C4C2 D9409996			3665 DC CL48'DIDBR rounding test 10v NT'
0001C2B0	3FF80000 00000000			3666 DC XL16'3FF8000000000000040100000000000000'
0001C2C0	C4C9C4C2 D9409996			3667 DC CL48'DIDBR rounding test 10v TR'
0001C2F0	3FF80000 00000000			3668 DC XL16'3FF8000000000000040100000000000000'
0001C300	C4C9C4C2 D9409996			3669 DC CL48'DIDBR rounding test 10w NT'
0001C330	BFE00000 00000000			3670 DC XL16'BFE000000000000004014000000000000'
0001C340	C4C9C4C2 D9409996			3671 DC CL48'DIDBR rounding test 10w TR'
0001C370	BFE00000 00000000			3672 DC XL16'BFE000000000000004014000000000000'
0001C380	C4C9C4C2 D9409996			3673 DC CL48'DIDBR rounding test 10x NT'
0001C3B0	3FF80000 00000000			3674 DC XL16'3FF8000000000000040100000000000000'
0001C3C0	C4C9C4C2 D9409996			3675 DC CL48'DIDBR rounding test 10x TR'
0001C3F0	3FF80000 00000000			3676 DC XL16'3FF8000000000000040100000000000000'
0001C400	C4C9C4C2 D9409996			3677 DC CL48'DIDBR rounding test 11a NT'
0001C430	00000000 00000000			3678 DC XL16'00000000000000003FF00000000000000'
0001C440	C4C9C4C2 D9409996			3679 DC CL48'DIDBR rounding test 11a TR'
0001C470	00000000 00000000			3680 DC XL16'00000000000000003FF00000000000000'
0001C480	C4C9C4C2 D9409996			3681 DC CL48'DIDBR rounding test 11b NT'
0001C4B0	00000000 00000000			3682 DC XL16'00000000000000003FF00000000000000'
0001C4C0	C4C9C4C2 D9409996			3683 DC CL48'DIDBR rounding test 11b TR'
0001C4F0	00000000 00000000			3684 DC XL16'00000000000000003FF00000000000000'
0001C500	C4C9C4C2 D9409996			3685 DC CL48'DIDBR rounding test 11c NT'
0001C530	00000000 00000000			3686 DC XL16'00000000000000003FF00000000000000'
0001C540	C4C9C4C2 D9409996			3687 DC CL48'DIDBR rounding test 11c TR'
0001C570	00000000 00000000			3688 DC XL16'00000000000000003FF00000000000000'
0001C580	C4C9C4C2 D9409996			3689 DC CL48'DIDBR rounding test 11d NT'
0001C5B0	00000000 00000000			3690 DC XL16'00000000000000003FF00000000000000'
0001C5C0	C4C9C4C2 D9409996			3691 DC CL48'DIDBR rounding test 11d TR'
0001C5F0	00000000 00000000			3692 DC XL16'00000000000000003FF00000000000000'
0001C600	C4C9C4C2 D9409996			3693 DC CL48'DIDBR rounding test 11e NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001C630	00000000 00000000			3694 DC XL16'000000000000000003FF0000000000000'
0001C640	C4C9C4C2 D9409996			3695 DC CL48'DIDBR rounding test 11e TR'
0001C670	00000000 00000000			3696 DC XL16'000000000000000003FF0000000000000'
0001C680	C4C9C4C2 D9409996			3697 DC CL48'DIDBR rounding test 11f NT'
0001C6B0	00000000 00000000			3698 DC XL16'000000000000000003FF0000000000000'
0001C6C0	C4C9C4C2 D9409996			3699 DC CL48'DIDBR rounding test 11f TR'
0001C6F0	00000000 00000000			3700 DC XL16'000000000000000003FF0000000000000'
0001C700	C4C9C4C2 D9409996			3701 DC CL48'DIDBR rounding test 11g NT'
0001C730	00000000 00000000			3702 DC XL16'000000000000000003FF0000000000000'
0001C740	C4C9C4C2 D9409996			3703 DC CL48'DIDBR rounding test 11g TR'
0001C770	00000000 00000000			3704 DC XL16'000000000000000003FF0000000000000'
0001C780	C4C9C4C2 D9409996			3705 DC CL48'DIDBR rounding test 11h NT'
0001C7B0	00000000 00000000			3706 DC XL16'000000000000000003FF0000000000000'
0001C7C0	C4C9C4C2 D9409996			3707 DC CL48'DIDBR rounding test 11h TR'
0001C7F0	00000000 00000000			3708 DC XL16'000000000000000003FF0000000000000'
0001C800	C4C9C4C2 D9409996			3709 DC CL48'DIDBR rounding test 11i NT'
0001C830	00000000 00000000			3710 DC XL16'000000000000000003FF0000000000000'
0001C840	C4C9C4C2 D9409996			3711 DC CL48'DIDBR rounding test 11i TR'
0001C870	00000000 00000000			3712 DC XL16'000000000000000003FF0000000000000'
0001C880	C4C9C4C2 D9409996			3713 DC CL48'DIDBR rounding test 11j NT'
0001C8B0	00000000 00000000			3714 DC XL16'000000000000000003FF0000000000000'
0001C8C0	C4C9C4C2 D9409996			3715 DC CL48'DIDBR rounding test 11j TR'
0001C8F0	00000000 00000000			3716 DC XL16'000000000000000003FF0000000000000'
0001C900	C4C9C4C2 D9409996			3717 DC CL48'DIDBR rounding test 11k NT'
0001C930	00000000 00000000			3718 DC XL16'000000000000000003FF0000000000000'
0001C940	C4C9C4C2 D9409996			3719 DC CL48'DIDBR rounding test 11k TR'
0001C970	00000000 00000000			3720 DC XL16'000000000000000003FF0000000000000'
0001C980	C4C9C4C2 D9409996			3721 DC CL48'DIDBR rounding test 11l NT'
0001C9B0	00000000 00000000			3722 DC XL16'000000000000000003FF0000000000000'
0001C9C0	C4C9C4C2 D9409996			3723 DC CL48'DIDBR rounding test 11l TR'
0001C9F0	00000000 00000000			3724 DC XL16'000000000000000003FF0000000000000'
0001CA00	C4C9C4C2 D9409996			3725 DC CL48'DIDBR rounding test 11m NT'
0001CA30	00000000 00000000			3726 DC XL16'000000000000000003FF0000000000000'
0001CA40	C4C9C4C2 D9409996			3727 DC CL48'DIDBR rounding test 11m TR'
0001CA70	00000000 00000000			3728 DC XL16'000000000000000003FF0000000000000'
0001CA80	C4C9C4C2 D9409996			3729 DC CL48'DIDBR rounding test 11n NT'
0001CAB0	00000000 00000000			3730 DC XL16'000000000000000003FF0000000000000'
0001CAC0	C4C9C4C2 D9409996			3731 DC CL48'DIDBR rounding test 11n TR'
0001CAF0	00000000 00000000			3732 DC XL16'000000000000000003FF0000000000000'
0001CB00	C4C9C4C2 D9409996			3733 DC CL48'DIDBR rounding test 11o NT'
0001CB30	00000000 00000000			3734 DC XL16'000000000000000003FF0000000000000'
0001CB40	C4C9C4C2 D9409996			3735 DC CL48'DIDBR rounding test 11o TR'
0001CB70	00000000 00000000			3736 DC XL16'000000000000000003FF0000000000000'
0001CB80	C4C9C4C2 D9409996			3737 DC CL48'DIDBR rounding test 11p NT'
0001CBB0	00000000 00000000			3738 DC XL16'000000000000000003FF0000000000000'
0001CBC0	C4C9C4C2 D9409996			3739 DC CL48'DIDBR rounding test 11p TR'
0001CBF0	00000000 00000000			3740 DC XL16'000000000000000003FF0000000000000'
0001CC00	C4C9C4C2 D9409996			3741 DC CL48'DIDBR rounding test 11q NT'
0001CC30	00000000 00000000			3742 DC XL16'000000000000000003FF0000000000000'
0001CC40	C4C9C4C2 D9409996			3743 DC CL48'DIDBR rounding test 11q TR'
0001CC70	00000000 00000000			3744 DC XL16'000000000000000003FF0000000000000'
0001CC80	C4C9C4C2 D9409996			3745 DC CL48'DIDBR rounding test 11r NT'
0001CCB0	00000000 00000000			3746 DC XL16'000000000000000003FF0000000000000'
0001CCC0	C4C9C4C2 D9409996			3747 DC CL48'DIDBR rounding test 11r TR'
0001CCF0	00000000 00000000			3748 DC XL16'000000000000000003FF0000000000000'
0001CD00	C4C9C4C2 D9409996			3749 DC CL48'DIDBR rounding test 11s NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001CD30	00000000 00000000			3750 DC XL16'00000000000000003FF000000000000'
0001CD40	C4C9C4C2 D9409996			3751 DC CL48'DIDBR rounding test 11s TR'
0001CD70	00000000 00000000			3752 DC XL16'00000000000000003FF000000000000'
0001CD80	C4C9C4C2 D9409996			3753 DC CL48'DIDBR rounding test 11t NT'
0001CDB0	00000000 00000000			3754 DC XL16'00000000000000003FF000000000000'
0001CDC0	C4C9C4C2 D9409996			3755 DC CL48'DIDBR rounding test 11t TR'
0001CDF0	00000000 00000000			3756 DC XL16'00000000000000003FF000000000000'
0001CE00	C4C9C4C2 D9409996			3757 DC CL48'DIDBR rounding test 11u NT'
0001CE30	00000000 00000000			3758 DC XL16'00000000000000003FF000000000000'
0001CE40	C4C9C4C2 D9409996			3759 DC CL48'DIDBR rounding test 11u TR'
0001CE70	00000000 00000000			3760 DC XL16'00000000000000003FF000000000000'
0001CE80	C4C9C4C2 D9409996			3761 DC CL48'DIDBR rounding test 11v NT'
0001CEB0	00000000 00000000			3762 DC XL16'00000000000000003FF000000000000'
0001CEC0	C4C9C4C2 D9409996			3763 DC CL48'DIDBR rounding test 11v TR'
0001CEF0	00000000 00000000			3764 DC XL16'00000000000000003FF000000000000'
0001CF00	C4C9C4C2 D9409996			3765 DC CL48'DIDBR rounding test 11w NT'
0001CF30	00000000 00000000			3766 DC XL16'00000000000000003FF000000000000'
0001CF40	C4C9C4C2 D9409996			3767 DC CL48'DIDBR rounding test 11w TR'
0001CF70	00000000 00000000			3768 DC XL16'00000000000000003FF000000000000'
0001CF80	C4C9C4C2 D9409996			3769 DC CL48'DIDBR rounding test 11x NT'
0001CFB0	00000000 00000000			3770 DC XL16'00000000000000003FF000000000000'
0001CFC0	C4C9C4C2 D9409996			3771 DC CL48'DIDBR rounding test 11x TR'
0001CFF0	00000000 00000000			3772 DC XL16'00000000000000003FF000000000000'
0001D000	C4C9C4C2 D9409996			3773 DC CL48'DIDBR rounding test 12a NT'
0001D030	C0000000 00000000			3774 DC XL16'C0000000000000003FF000000000000'
0001D040	C4C9C4C2 D9409996			3775 DC CL48'DIDBR rounding test 12a TR'
0001D070	C0000000 00000000			3776 DC XL16'C0000000000000003FF000000000000'
0001D080	C4C9C4C2 D9409996			3777 DC CL48'DIDBR rounding test 12b NT'
0001D0B0	C0000000 00000000			3778 DC XL16'C0000000000000003FF000000000000'
0001D0C0	C4C9C4C2 D9409996			3779 DC CL48'DIDBR rounding test 12b TR'
0001D0F0	C0000000 00000000			3780 DC XL16'C0000000000000003FF000000000000'
0001D100	C4C9C4C2 D9409996			3781 DC CL48'DIDBR rounding test 12c NT'
0001D130	C0000000 00000000			3782 DC XL16'C0000000000000003FF000000000000'
0001D140	C4C9C4C2 D9409996			3783 DC CL48'DIDBR rounding test 12c TR'
0001D170	C0000000 00000000			3784 DC XL16'C0000000000000003FF000000000000'
0001D180	C4C9C4C2 D9409996			3785 DC CL48'DIDBR rounding test 12d NT'
0001D1B0	40080000 00000000			3786 DC XL16'400800000000000000000000000000'
0001D1C0	C4C9C4C2 D9409996			3787 DC CL48'DIDBR rounding test 12d TR'
0001D1F0	40080000 00000000			3788 DC XL16'400800000000000000000000000000'
0001D200	C4C9C4C2 D9409996			3789 DC CL48'DIDBR rounding test 12e NT'
0001D230	C0000000 00000000			3790 DC XL16'C0000000000000003FF000000000000'
0001D240	C4C9C4C2 D9409996			3791 DC CL48'DIDBR rounding test 12e TR'
0001D270	C0000000 00000000			3792 DC XL16'C0000000000000003FF000000000000'
0001D280	C4C9C4C2 D9409996			3793 DC CL48'DIDBR rounding test 12f NT'
0001D2B0	40080000 00000000			3794 DC XL16'400800000000000000000000000000'
0001D2C0	C4C9C4C2 D9409996			3795 DC CL48'DIDBR rounding test 12f TR'
0001D2F0	40080000 00000000			3796 DC XL16'400800000000000000000000000000'
0001D300	C4C9C4C2 D9409996			3797 DC CL48'DIDBR rounding test 12g NT'
0001D330	C0000000 00000000			3798 DC XL16'C0000000000000003FF000000000000'
0001D340	C4C9C4C2 D9409996			3799 DC CL48'DIDBR rounding test 12g TR'
0001D370	C0000000 00000000			3800 DC XL16'C0000000000000003FF000000000000'
0001D380	C4C9C4C2 D9409996			3801 DC CL48'DIDBR rounding test 12h NT'
0001D3B0	C0000000 00000000			3802 DC XL16'C0000000000000003FF000000000000'
0001D3C0	C4C9C4C2 D9409996			3803 DC CL48'DIDBR rounding test 12h TR'
0001D3F0	C0000000 00000000			3804 DC XL16'C0000000000000003FF000000000000'
0001D400	C4C9C4C2 D9409996			3805 DC CL48'DIDBR rounding test 12i NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001D430	C0000000 00000000			3806 DC XL16'C00000000000000003FF0000000000000'
0001D440	C4C9C4C2 D9409996			3807 DC CL48'DIDBR rounding test 12i TR'
0001D470	C0000000 00000000			3808 DC XL16'C00000000000000003FF0000000000000'
0001D480	C4C9C4C2 D9409996			3809 DC CL48'DIDBR rounding test 12j NT'
0001D4B0	40080000 00000000			3810 DC XL16'40080000000000000000000000000000'
0001D4C0	C4C9C4C2 D9409996			3811 DC CL48'DIDBR rounding test 12j TR'
0001D4F0	40080000 00000000			3812 DC XL16'40080000000000000000000000000000'
0001D500	C4C9C4C2 D9409996			3813 DC CL48'DIDBR rounding test 12k NT'
0001D530	C0000000 00000000			3814 DC XL16'C00000000000000003FF0000000000000'
0001D540	C4C9C4C2 D9409996			3815 DC CL48'DIDBR rounding test 12k TR'
0001D570	C0000000 00000000			3816 DC XL16'C00000000000000003FF0000000000000'
0001D580	C4C9C4C2 D9409996			3817 DC CL48'DIDBR rounding test 12l NT'
0001D5B0	40080000 00000000			3818 DC XL16'40080000000000000000000000000000'
0001D5C0	C4C9C4C2 D9409996			3819 DC CL48'DIDBR rounding test 12l TR'
0001D5F0	40080000 00000000			3820 DC XL16'40080000000000000000000000000000'
0001D600	C4C9C4C2 D9409996			3821 DC CL48'DIDBR rounding test 12m NT'
0001D630	C0000000 00000000			3822 DC XL16'C00000000000000003FF0000000000000'
0001D640	C4C9C4C2 D9409996			3823 DC CL48'DIDBR rounding test 12m TR'
0001D670	C0000000 00000000			3824 DC XL16'C00000000000000003FF0000000000000'
0001D680	C4C9C4C2 D9409996			3825 DC CL48'DIDBR rounding test 12n NT'
0001D6B0	C0000000 00000000			3826 DC XL16'C00000000000000003FF0000000000000'
0001D6C0	C4C9C4C2 D9409996			3827 DC CL48'DIDBR rounding test 12n TR'
0001D6F0	C0000000 00000000			3828 DC XL16'C00000000000000003FF0000000000000'
0001D700	C4C9C4C2 D9409996			3829 DC CL48'DIDBR rounding test 12o NT'
0001D730	C0000000 00000000			3830 DC XL16'C00000000000000003FF0000000000000'
0001D740	C4C9C4C2 D9409996			3831 DC CL48'DIDBR rounding test 12o TR'
0001D770	C0000000 00000000			3832 DC XL16'C00000000000000003FF0000000000000'
0001D780	C4C9C4C2 D9409996			3833 DC CL48'DIDBR rounding test 12p NT'
0001D7B0	40080000 00000000			3834 DC XL16'40080000000000000000000000000000'
0001D7C0	C4C9C4C2 D9409996			3835 DC CL48'DIDBR rounding test 12p TR'
0001D7F0	40080000 00000000			3836 DC XL16'40080000000000000000000000000000'
0001D800	C4C9C4C2 D9409996			3837 DC CL48'DIDBR rounding test 12q NT'
0001D830	C0000000 00000000			3838 DC XL16'C00000000000000003FF0000000000000'
0001D840	C4C9C4C2 D9409996			3839 DC CL48'DIDBR rounding test 12q TR'
0001D870	C0000000 00000000			3840 DC XL16'C00000000000000003FF0000000000000'
0001D880	C4C9C4C2 D9409996			3841 DC CL48'DIDBR rounding test 12r NT'
0001D8B0	40080000 00000000			3842 DC XL16'40080000000000000000000000000000'
0001D8C0	C4C9C4C2 D9409996			3843 DC CL48'DIDBR rounding test 12r TR'
0001D8F0	40080000 00000000			3844 DC XL16'40080000000000000000000000000000'
0001D900	C4C9C4C2 D9409996			3845 DC CL48'DIDBR rounding test 12s NT'
0001D930	C0000000 00000000			3846 DC XL16'C00000000000000003FF0000000000000'
0001D940	C4C9C4C2 D9409996			3847 DC CL48'DIDBR rounding test 12s TR'
0001D970	C0000000 00000000			3848 DC XL16'C00000000000000003FF0000000000000'
0001D980	C4C9C4C2 D9409996			3849 DC CL48'DIDBR rounding test 12t NT'
0001D9B0	C0000000 00000000			3850 DC XL16'C00000000000000003FF0000000000000'
0001D9C0	C4C9C4C2 D9409996			3851 DC CL48'DIDBR rounding test 12t TR'
0001D9F0	C0000000 00000000			3852 DC XL16'C00000000000000003FF0000000000000'
0001DA00	C4C9C4C2 D9409996			3853 DC CL48'DIDBR rounding test 12u NT'
0001DA30	C0000000 00000000			3854 DC XL16'C00000000000000003FF0000000000000'
0001DA40	C4C9C4C2 D9409996			3855 DC CL48'DIDBR rounding test 12u TR'
0001DA70	C0000000 00000000			3856 DC XL16'C00000000000000003FF0000000000000'
0001DA80	C4C9C4C2 D9409996			3857 DC CL48'DIDBR rounding test 12v NT'
0001DAB0	40080000 00000000			3858 DC XL16'40080000000000000000000000000000'
0001DAC0	C4C9C4C2 D9409996			3859 DC CL48'DIDBR rounding test 12v TR'
0001DAF0	40080000 00000000			3860 DC XL16'40080000000000000000000000000000'
0001DB00	C4C9C4C2 D9409996			3861 DC CL48'DIDBR rounding test 12w NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001DB30	C0000000 00000000			3862 DC XL16'C00000000000000003FF0000000000000'
0001DB40	C4C9C4C2 D9409996			3863 DC CL48'DIDBR rounding test 12w TR'
0001DB70	C0000000 00000000			3864 DC XL16'C00000000000000003FF0000000000000'
0001DB80	C4C9C4C2 D9409996			3865 DC CL48'DIDBR rounding test 12x NT'
0001DBB0	40080000 00000000			3866 DC XL16'40080000000000000000000000000000'
0001DBC0	C4C9C4C2 D9409996			3867 DC CL48'DIDBR rounding test 12x TR'
0001DBF0	40080000 00000000			3868 DC XL16'40080000000000000000000000000000'
		00000240	00000001	3869 LBFPRMO_NUM EQU (*-LBFPRMO_GOOD)/64
				3870 *
				3871 *
		0001DC00	00000001	3872 LBFPRMOF_GOOD EQU *
0001DC00	C4C9C4C2 D940D996			3873 DC CL48'DIDBR Rounding FPCR 1ab'
0001DC30	00000000 F8000000			3874 DC XL16'00000000F80000000000000000F8000000'
0001DC40	C4C9C4C2 D940D996			3875 DC CL48'DIDBR Rounding FPCR 1cd'
0001DC70	00000000 F8000000			3876 DC XL16'00000000F80000000000000000F8000000'
0001DC80	C4C9C4C2 D940D996			3877 DC CL48'DIDBR Rounding FPCR 1ef'
0001DCB0	00000000 F8000000			3878 DC XL16'00000000F80000000000000000F8000000'
0001DCC0	C4C9C4C2 D940D996			3879 DC CL48'DIDBR Rounding FPCR 1gh'
0001DCF0	00000000 F8000000			3880 DC XL16'00000000F80000000000000000F8000000'
0001DD00	C4C9C4C2 D940D996			3881 DC CL48'DIDBR Rounding FPCR 1ij'
0001DD30	00000000 F8000000			3882 DC XL16'00000000F80000000000000000F8000000'
0001DD40	C4C9C4C2 D940D996			3883 DC CL48'DIDBR Rounding FPCR 1kl'
0001DD70	00000000 F8000000			3884 DC XL16'00000000F80000000000000000F8000000'
0001DD80	C4C9C4C2 D940D996			3885 DC CL48'DIDBR Rounding FPCR 1mn'
0001ddb0	00000000 F8000000			3886 DC XL16'00000000F80000000000000000F8000000'
0001DDC0	C4C9C4C2 D940D996			3887 DC CL48'DIDBR Rounding FPCR 1op'
0001DDF0	00000000 F8000000			3888 DC XL16'00000000F80000000000000000F8000000'
0001DE00	C4C9C4C2 D940D996			3889 DC CL48'DIDBR Rounding FPCR 1qr'
0001DE30	00000000 F8000000			3890 DC XL16'00000000F80000000000000000F8000000'
0001DE40	C4C9C4C2 D940D996			3891 DC CL48'DIDBR Rounding FPCR 1st'
0001DE70	00000000 F8000000			3892 DC XL16'00000000F80000000000000000F8000000'
0001DE80	C4C9C4C2 D940D996			3893 DC CL48'DIDBR Rounding FPCR 1uv'
0001DEB0	00000000 F8000000			3894 DC XL16'00000000F80000000000000000F8000000'
0001DEC0	C4C9C4C2 D940D996			3895 DC CL48'DIDBR Rounding FPCR 1wx'
0001DEF0	00000000 F8000000			3896 DC XL16'00000000F80000000000000000F8000000'
0001DF00	C4C9C4C2 D940D996			3897 DC CL48'DIDBR Rounding FPCR 2ab'
0001DF30	00000000 F8000000			3898 DC XL16'00000000F80000000000000000F8000000'
0001DF40	C4C9C4C2 D940D996			3899 DC CL48'DIDBR Rounding FPCR 2cd'
0001DF70	00000000 F8000000			3900 DC XL16'00000000F80000000000000000F8000000'
0001DF80	C4C9C4C2 D940D996			3901 DC CL48'DIDBR Rounding FPCR 2ef'
0001DFB0	00000000 F8000000			3902 DC XL16'00000000F80000000000000000F8000000'
0001DFC0	C4C9C4C2 D940D996			3903 DC CL48'DIDBR Rounding FPCR 2gh'
0001DFF0	00000000 F8000000			3904 DC XL16'00000000F80000000000000000F8000000'
0001E000	C4C9C4C2 D940D996			3905 DC CL48'DIDBR Rounding FPCR 2ij'
0001E030	00000000 F8000000			3906 DC XL16'00000000F80000000000000000F8000000'
0001E040	C4C9C4C2 D940D996			3907 DC CL48'DIDBR Rounding FPCR 2kl'
0001E070	00000000 F8000000			3908 DC XL16'00000000F80000000000000000F8000000'
0001E080	C4C9C4C2 D940D996			3909 DC CL48'DIDBR Rounding FPCR 2mn'
0001E0B0	00000000 F8000000			3910 DC XL16'00000000F80000000000000000F8000000'
0001E0C0	C4C9C4C2 D940D996			3911 DC CL48'DIDBR Rounding FPCR 2op'
0001E0F0	00000000 F8000000			3912 DC XL16'00000000F80000000000000000F8000000'
0001E100	C4C9C4C2 D940D996			3913 DC CL48'DIDBR Rounding FPCR 2qr'
0001E130	00000000 F8000000			3914 DC XL16'00000000F80000000000000000F8000000'
0001E140	C4C9C4C2 D940D996			3915 DC CL48'DIDBR Rounding FPCR 2st'
0001E170	00000000 F8000000			3916 DC XL16'00000000F80000000000000000F8000000'
0001E180	C4C9C4C2 D940D996			3917 DC CL48'DIDBR Rounding FPCR 2uv'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001E1B0	00000000 F8000000			3918 DC XL16'00000000F800000000000000F8000000'
0001E1C0	C4C9C4C2 D940D996			3919 DC CL48'DIDBR Rounding FPCR 2wx'
0001E1F0	00000000 F8000000			3920 DC XL16'00000000F800000000000000F8000000'
0001E200	C4C9C4C2 D940D996			3921 DC CL48'DIDBR Rounding FPCR 3ab'
0001E230	00000000 F8000000			3922 DC XL16'00000000F800000000000000F8000000'
0001E240	C4C9C4C2 D940D996			3923 DC CL48'DIDBR Rounding FPCR 3cd'
0001E270	00000000 F8000000			3924 DC XL16'00000000F800000000000000F8000000'
0001E280	C4C9C4C2 D940D996			3925 DC CL48'DIDBR Rounding FPCR 3ef'
0001E2B0	00000000 F8000000			3926 DC XL16'00000000F800000000000000F8000000'
0001E2C0	C4C9C4C2 D940D996			3927 DC CL48'DIDBR Rounding FPCR 3gh'
0001E2F0	00000000 F8000000			3928 DC XL16'00000000F800000000000000F8000000'
0001E300	C4C9C4C2 D940D996			3929 DC CL48'DIDBR Rounding FPCR 3ij'
0001E330	00000000 F8000000			3930 DC XL16'00000000F800000000000000F8000000'
0001E340	C4C9C4C2 D940D996			3931 DC CL48'DIDBR Rounding FPCR 3kl'
0001E370	00000000 F8000000			3932 DC XL16'00000000F800000000000000F8000000'
0001E380	C4C9C4C2 D940D996			3933 DC CL48'DIDBR Rounding FPCR 3mn'
0001E3B0	00000000 F8000000			3934 DC XL16'00000000F800000000000000F8000000'
0001E3C0	C4C9C4C2 D940D996			3935 DC CL48'DIDBR Rounding FPCR 3op'
0001E3F0	00000000 F8000000			3936 DC XL16'00000000F800000000000000F8000000'
0001E400	C4C9C4C2 D940D996			3937 DC CL48'DIDBR Rounding FPCR 3qr'
0001E430	00000000 F8000000			3938 DC XL16'00000000F800000000000000F8000000'
0001E440	C4C9C4C2 D940D996			3939 DC CL48'DIDBR Rounding FPCR 3st'
0001E470	00000000 F8000000			3940 DC XL16'00000000F800000000000000F8000000'
0001E480	C4C9C4C2 D940D996			3941 DC CL48'DIDBR Rounding FPCR 3uv'
0001E4B0	00000000 F8000000			3942 DC XL16'00000000F800000000000000F8000000'
0001E4C0	C4C9C4C2 D940D996			3943 DC CL48'DIDBR Rounding FPCR 3wx'
0001E4F0	00000000 F8000000			3944 DC XL16'00000000F800000000000000F8000000'
0001E500	C4C9C4C2 D940D996			3945 DC CL48'DIDBR Rounding FPCR 4ab'
0001E530	00000000 F8000000			3946 DC XL16'00000000F800000000000000F8000000'
0001E540	C4C9C4C2 D940D996			3947 DC CL48'DIDBR Rounding FPCR 4cd'
0001E570	00000000 F8000000			3948 DC XL16'00000000F800000000000000F8000000'
0001E580	C4C9C4C2 D940D996			3949 DC CL48'DIDBR Rounding FPCR 4ef'
0001E5B0	00000000 F8000000			3950 DC XL16'00000000F800000000000000F8000000'
0001E5C0	C4C9C4C2 D940D996			3951 DC CL48'DIDBR Rounding FPCR 4gh'
0001E5F0	00000000 F8000000			3952 DC XL16'00000000F800000000000000F8000000'
0001E600	C4C9C4C2 D940D996			3953 DC CL48'DIDBR Rounding FPCR 4ij'
0001E630	00000000 F8000000			3954 DC XL16'00000000F800000000000000F8000000'
0001E640	C4C9C4C2 D940D996			3955 DC CL48'DIDBR Rounding FPCR 4kl'
0001E670	00000000 F8000000			3956 DC XL16'00000000F800000000000000F8000000'
0001E680	C4C9C4C2 D940D996			3957 DC CL48'DIDBR Rounding FPCR 4mn'
0001E6B0	00000000 F8000000			3958 DC XL16'00000000F800000000000000F8000000'
0001E6C0	C4C9C4C2 D940D996			3959 DC CL48'DIDBR Rounding FPCR 4op'
0001E6F0	00000000 F8000000			3960 DC XL16'00000000F800000000000000F8000000'
0001E700	C4C9C4C2 D940D996			3961 DC CL48'DIDBR Rounding FPCR 4qr'
0001E730	00000000 F8000000			3962 DC XL16'00000000F800000000000000F8000000'
0001E740	C4C9C4C2 D940D996			3963 DC CL48'DIDBR Rounding FPCR 4st'
0001E770	00000000 F8000000			3964 DC XL16'00000000F800000000000000F8000000'
0001E780	C4C9C4C2 D940D996			3965 DC CL48'DIDBR Rounding FPCR 4uv'
0001E7B0	00000000 F8000000			3966 DC XL16'00000000F800000000000000F8000000'
0001E7C0	C4C9C4C2 D940D996			3967 DC CL48'DIDBR Rounding FPCR 4wx'
0001E7F0	00000000 F8000000			3968 DC XL16'00000000F800000000000000F8000000'
0001E800	C4C9C4C2 D940D996			3969 DC CL48'DIDBR Rounding FPCR 5ab'
0001E830	00000000 F8000000			3970 DC XL16'00000000F800000000000000F8000000'
0001E840	C4C9C4C2 D940D996			3971 DC CL48'DIDBR Rounding FPCR 5cd'
0001E870	00000000 F8000000			3972 DC XL16'00000000F800000000000000F8000000'
0001E880	C4C9C4C2 D940D996			3973 DC CL48'DIDBR Rounding FPCR 5ef'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001E8B0	00000000 F8000000			3974 DC XL16'00000000F800000000000000F8000000'
0001E8C0	C4C9C4C2 D940D996			3975 DC CL48'DIDBR Rounding FPCR 5gh'
0001E8F0	00000000 F8000000			3976 DC XL16'00000000F800000000000000F8000000'
0001E900	C4C9C4C2 D940D996			3977 DC CL48'DIDBR Rounding FPCR 5ij'
0001E930	00000000 F8000000			3978 DC XL16'00000000F800000000000000F8000000'
0001E940	C4C9C4C2 D940D996			3979 DC CL48'DIDBR Rounding FPCR 5kl'
0001E970	00000000 F8000000			3980 DC XL16'00000000F800000000000000F8000000'
0001E980	C4C9C4C2 D940D996			3981 DC CL48'DIDBR Rounding FPCR 5mn'
0001E9B0	00000000 F8000000			3982 DC XL16'00000000F800000000000000F8000000'
0001E9C0	C4C9C4C2 D940D996			3983 DC CL48'DIDBR Rounding FPCR 5op'
0001E9F0	00000000 F8000000			3984 DC XL16'00000000F800000000000000F8000000'
0001EA00	C4C9C4C2 D940D996			3985 DC CL48'DIDBR Rounding FPCR 5qr'
0001EA30	00000000 F8000000			3986 DC XL16'00000000F800000000000000F8000000'
0001EA40	C4C9C4C2 D940D996			3987 DC CL48'DIDBR Rounding FPCR 5st'
0001EA70	00000000 F8000000			3988 DC XL16'00000000F800000000000000F8000000'
0001EA80	C4C9C4C2 D940D996			3989 DC CL48'DIDBR Rounding FPCR 5uv'
0001EAB0	00000000 F8000000			3990 DC XL16'00000000F800000000000000F8000000'
0001EAC0	C4C9C4C2 D940D996			3991 DC CL48'DIDBR Rounding FPCR 5wx'
0001EAF0	00000000 F8000000			3992 DC XL16'00000000F800000000000000F8000000'
0001EB00	C4C9C4C2 D940D996			3993 DC CL48'DIDBR Rounding FPCR 6ab'
0001EB30	00000000 F8000000			3994 DC XL16'00000000F800000000000000F8000000'
0001EB40	C4C9C4C2 D940D996			3995 DC CL48'DIDBR Rounding FPCR 6cd'
0001EB70	00000000 F8000000			3996 DC XL16'00000000F800000000000000F8000000'
0001EB80	C4C9C4C2 D940D996			3997 DC CL48'DIDBR Rounding FPCR 6ef'
0001EBB0	00000000 F8000000			3998 DC XL16'00000000F800000000000000F8000000'
0001EBC0	C4C9C4C2 D940D996			3999 DC CL48'DIDBR Rounding FPCR 6gh'
0001EBF0	00000000 F8000000			4000 DC XL16'00000000F800000000000000F8000000'
0001EC00	C4C9C4C2 D940D996			4001 DC CL48'DIDBR Rounding FPCR 6ij'
0001EC30	00000000 F8000000			4002 DC XL16'00000000F800000000000000F8000000'
0001EC40	C4C9C4C2 D940D996			4003 DC CL48'DIDBR Rounding FPCR 6kl'
0001EC70	00000000 F8000000			4004 DC XL16'00000000F800000000000000F8000000'
0001EC80	C4C9C4C2 D940D996			4005 DC CL48'DIDBR Rounding FPCR 6mn'
0001ECB0	00000000 F8000000			4006 DC XL16'00000000F800000000000000F8000000'
0001ECC0	C4C9C4C2 D940D996			4007 DC CL48'DIDBR Rounding FPCR 6op'
0001ECF0	00000000 F8000000			4008 DC XL16'00000000F800000000000000F8000000'
0001ED00	C4C9C4C2 D940D996			4009 DC CL48'DIDBR Rounding FPCR 6qr'
0001ED30	00000000 F8000000			4010 DC XL16'00000000F800000000000000F8000000'
0001ED40	C4C9C4C2 D940D996			4011 DC CL48'DIDBR Rounding FPCR 6st'
0001ED70	00000000 F8000000			4012 DC XL16'00000000F800000000000000F8000000'
0001ED80	C4C9C4C2 D940D996			4013 DC CL48'DIDBR Rounding FPCR 6uv'
0001EDB0	00000000 F8000000			4014 DC XL16'00000000F800000000000000F8000000'
0001EDC0	C4C9C4C2 D940D996			4015 DC CL48'DIDBR Rounding FPCR 6wx'
0001EDF0	00000000 F8000000			4016 DC XL16'00000000F800000000000000F8000000'
0001EE00	C4C9C4C2 D940D996			4017 DC CL48'DIDBR Rounding FPCR 7ab'
0001EE30	00000000 F8000000			4018 DC XL16'00000000F800000000000000F8000000'
0001EE40	C4C9C4C2 D940D996			4019 DC CL48'DIDBR Rounding FPCR 7cd'
0001EE70	00000000 F8000000			4020 DC XL16'00000000F800000000000000F8000000'
0001EE80	C4C9C4C2 D940D996			4021 DC CL48'DIDBR Rounding FPCR 7ef'
0001EEB0	00000000 F8000000			4022 DC XL16'00000000F800000000000000F8000000'
0001EEC0	C4C9C4C2 D940D996			4023 DC CL48'DIDBR Rounding FPCR 7gh'
0001EEF0	00000000 F8000000			4024 DC XL16'00000000F800000000000000F8000000'
0001EF00	C4C9C4C2 D940D996			4025 DC CL48'DIDBR Rounding FPCR 7ij'
0001EF30	00000000 F8000000			4026 DC XL16'00000000F800000000000000F8000000'
0001EF40	C4C9C4C2 D940D996			4027 DC CL48'DIDBR Rounding FPCR 7kl'
0001EF70	00000000 F8000000			4028 DC XL16'00000000F800000000000000F8000000'
0001EF80	C4C9C4C2 D940D996			4029 DC CL48'DIDBR Rounding FPCR 7mn'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001EFB0	00000000 F8000000			4030 DC XL16'00000000F800000000000000F8000000'
0001EFC0	C4C9C4C2 D940D996			4031 DC CL48'DIDBR Rounding FPCR 7op'
0001EFF0	00000000 F8000000			4032 DC XL16'00000000F800000000000000F8000000'
0001F000	C4C9C4C2 D940D996			4033 DC CL48'DIDBR Rounding FPCR 7qr'
0001F030	00000000 F8000000			4034 DC XL16'00000000F800000000000000F8000000'
0001F040	C4C9C4C2 D940D996			4035 DC CL48'DIDBR Rounding FPCR 7st'
0001F070	00000000 F8000000			4036 DC XL16'00000000F800000000000000F8000000'
0001F080	C4C9C4C2 D940D996			4037 DC CL48'DIDBR Rounding FPCR 7uv'
0001F0B0	00000000 F8000000			4038 DC XL16'00000000F800000000000000F8000000'
0001F0C0	C4C9C4C2 D940D996			4039 DC CL48'DIDBR Rounding FPCR 7wx'
0001F0F0	00000000 F8000000			4040 DC XL16'00000000F800000000000000F8000000'
0001F100	C4C9C4C2 D940D996			4041 DC CL48'DIDBR Rounding FPCR 8ab'
0001F130	00000000 F8000000			4042 DC XL16'00000000F800000000000000F8000000'
0001F140	C4C9C4C2 D940D996			4043 DC CL48'DIDBR Rounding FPCR 8cd'
0001F170	00000000 F8000000			4044 DC XL16'00000000F800000000000000F8000000'
0001F180	C4C9C4C2 D940D996			4045 DC CL48'DIDBR Rounding FPCR 8ef'
0001F1B0	00000000 F8000000			4046 DC XL16'00000000F800000000000000F8000000'
0001F1C0	C4C9C4C2 D940D996			4047 DC CL48'DIDBR Rounding FPCR 8gh'
0001F1F0	00000000 F8000000			4048 DC XL16'00000000F800000000000000F8000000'
0001F200	C4C9C4C2 D940D996			4049 DC CL48'DIDBR Rounding FPCR 8ij'
0001F230	00000000 F8000000			4050 DC XL16'00000000F800000000000000F8000000'
0001F240	C4C9C4C2 D940D996			4051 DC CL48'DIDBR Rounding FPCR 8kl'
0001F270	00000000 F8000000			4052 DC XL16'00000000F800000000000000F8000000'
0001F280	C4C9C4C2 D940D996			4053 DC CL48'DIDBR Rounding FPCR 8mn'
0001F2B0	00000000 F8000000			4054 DC XL16'00000000F800000000000000F8000000'
0001F2C0	C4C9C4C2 D940D996			4055 DC CL48'DIDBR Rounding FPCR 8op'
0001F2F0	00000000 F8000000			4056 DC XL16'00000000F800000000000000F8000000'
0001F300	C4C9C4C2 D940D996			4057 DC CL48'DIDBR Rounding FPCR 8qr'
0001F330	00000000 F8000000			4058 DC XL16'00000000F800000000000000F8000000'
0001F340	C4C9C4C2 D940D996			4059 DC CL48'DIDBR Rounding FPCR 8st'
0001F370	00000000 F8000000			4060 DC XL16'00000000F800000000000000F8000000'
0001F380	C4C9C4C2 D940D996			4061 DC CL48'DIDBR Rounding FPCR 8uv'
0001F3B0	00000000 F8000000			4062 DC XL16'00000000F800000000000000F8000000'
0001F3C0	C4C9C4C2 D940D996			4063 DC CL48'DIDBR Rounding FPCR 8wx'
0001F3F0	00000000 F8000000			4064 DC XL16'00000000F800000000000000F8000000'
0001F400	C4C9C4C2 D940D996			4065 DC CL48'DIDBR Rounding FPCR 9ab'
0001F430	00000000 F8000000			4066 DC XL16'00000000F800000000000000F8000000'
0001F440	C4C9C4C2 D940D996			4067 DC CL48'DIDBR Rounding FPCR 9cd'
0001F470	00000000 F8000000			4068 DC XL16'00000000F800000000000000F8000000'
0001F480	C4C9C4C2 D940D996			4069 DC CL48'DIDBR Rounding FPCR 9ef'
0001F4B0	00000000 F8000000			4070 DC XL16'00000000F800000000000000F8000000'
0001F4C0	C4C9C4C2 D940D996			4071 DC CL48'DIDBR Rounding FPCR 9gh'
0001F4F0	00000000 F8000000			4072 DC XL16'00000000F800000000000000F8000000'
0001F500	C4C9C4C2 D940D996			4073 DC CL48'DIDBR Rounding FPCR 9ij'
0001F530	00000000 F8000000			4074 DC XL16'00000000F800000000000000F8000000'
0001F540	C4C9C4C2 D940D996			4075 DC CL48'DIDBR Rounding FPCR 9kl'
0001F570	00000000 F8000000			4076 DC XL16'00000000F800000000000000F8000000'
0001F580	C4C9C4C2 D940D996			4077 DC CL48'DIDBR Rounding FPCR 9mn'
0001F5B0	00000000 F8000000			4078 DC XL16'00000000F800000000000000F8000000'
0001F5C0	C4C9C4C2 D940D996			4079 DC CL48'DIDBR Rounding FPCR 9op'
0001F5F0	00000000 F8000000			4080 DC XL16'00000000F800000000000000F8000000'
0001F600	C4C9C4C2 D940D996			4081 DC CL48'DIDBR Rounding FPCR 9qr'
0001F630	00000000 F8000000			4082 DC XL16'00000000F800000000000000F8000000'
0001F640	C4C9C4C2 D940D996			4083 DC CL48'DIDBR Rounding FPCR 9st'
0001F670	00000000 F8000000			4084 DC XL16'00000000F800000000000000F8000000'
0001F680	C4C9C4C2 D940D996			4085 DC CL48'DIDBR Rounding FPCR 9uv'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001F6B0	00000000 F8000000			4086 DC XL16'00000000F800000000000000F8000000'
0001F6C0	C4C9C4C2 D940D996			4087 DC CL48'DIDBR Rounding FPCR 9wx'
0001F6F0	00000000 F8000000			4088 DC XL16'00000000F800000000000000F8000000'
0001F700	C4C9C4C2 D940D996			4089 DC CL48'DIDBR Rounding FPCR 10ab'
0001F730	00000000 F8000000			4090 DC XL16'00000000F800000000000000F8000000'
0001F740	C4C9C4C2 D940D996			4091 DC CL48'DIDBR Rounding FPCR 10cd'
0001F770	00000000 F8000000			4092 DC XL16'00000000F800000000000000F8000000'
0001F780	C4C9C4C2 D940D996			4093 DC CL48'DIDBR Rounding FPCR 10ef'
0001F7B0	00000000 F8000000			4094 DC XL16'00000000F800000000000000F8000000'
0001F7C0	C4C9C4C2 D940D996			4095 DC CL48'DIDBR Rounding FPCR 10gh'
0001F7F0	00000000 F8000000			4096 DC XL16'00000000F800000000000000F8000000'
0001F800	C4C9C4C2 D940D996			4097 DC CL48'DIDBR Rounding FPCR 10ij'
0001F830	00000000 F8000000			4098 DC XL16'00000000F800000000000000F8000000'
0001F840	C4C9C4C2 D940D996			4099 DC CL48'DIDBR Rounding FPCR 10kl'
0001F870	00000000 F8000000			4100 DC XL16'00000000F800000000000000F8000000'
0001F880	C4C9C4C2 D940D996			4101 DC CL48'DIDBR Rounding FPCR 10mn'
0001F8B0	00000000 F8000000			4102 DC XL16'00000000F800000000000000F8000000'
0001F8C0	C4C9C4C2 D940D996			4103 DC CL48'DIDBR Rounding FPCR 10op'
0001F8F0	00000000 F8000000			4104 DC XL16'00000000F800000000000000F8000000'
0001F900	C4C9C4C2 D940D996			4105 DC CL48'DIDBR Rounding FPCR 10qr'
0001F930	00000000 F8000000			4106 DC XL16'00000000F800000000000000F8000000'
0001F940	C4C9C4C2 D940D996			4107 DC CL48'DIDBR Rounding FPCR 10st'
0001F970	00000000 F8000000			4108 DC XL16'00000000F800000000000000F8000000'
0001F980	C4C9C4C2 D940D996			4109 DC CL48'DIDBR Rounding FPCR 10uv'
0001F9B0	00000000 F8000000			4110 DC XL16'00000000F800000000000000F8000000'
0001F9C0	C4C9C4C2 D940D996			4111 DC CL48'DIDBR Rounding FPCR 10wx'
0001F9F0	00000000 F8000000			4112 DC XL16'00000000F800000000000000F8000000'
0001FA00	C4C9C4C2 D940D996			4113 DC CL48'DIDBR Rounding FPCR 11ab'
0001FA30	00000000 F8000000			4114 DC XL16'00000000F800000000000000F8000000'
0001FA40	C4C9C4C2 D940D996			4115 DC CL48'DIDBR Rounding FPCR 11cd'
0001FA70	00000000 F8000000			4116 DC XL16'00000000F800000000000000F8000000'
0001FA80	C4C9C4C2 D940D996			4117 DC CL48'DIDBR Rounding FPCR 11ef'
0001FAB0	00000000 F8000000			4118 DC XL16'00000000F800000000000000F8000000'
0001FAC0	C4C9C4C2 D940D996			4119 DC CL48'DIDBR Rounding FPCR 11gh'
0001FAF0	00000000 F8000000			4120 DC XL16'00000000F800000000000000F8000000'
0001FB00	C4C9C4C2 D940D996			4121 DC CL48'DIDBR Rounding FPCR 11ij'
0001FB30	00000000 F8000000			4122 DC XL16'00000000F800000000000000F8000000'
0001FB40	C4C9C4C2 D940D996			4123 DC CL48'DIDBR Rounding FPCR 11kl'
0001FB70	00000000 F8000000			4124 DC XL16'00000000F800000000000000F8000000'
0001FB80	C4C9C4C2 D940D996			4125 DC CL48'DIDBR Rounding FPCR 11mn'
0001FBB0	00000000 F8000000			4126 DC XL16'00000000F800000000000000F8000000'
0001FBC0	C4C9C4C2 D940D996			4127 DC CL48'DIDBR Rounding FPCR 11op'
0001FBF0	00000000 F8000000			4128 DC XL16'00000000F800000000000000F8000000'
0001FC00	C4C9C4C2 D940D996			4129 DC CL48'DIDBR Rounding FPCR 11qr'
0001FC30	00000000 F8000000			4130 DC XL16'00000000F800000000000000F8000000'
0001FC40	C4C9C4C2 D940D996			4131 DC CL48'DIDBR Rounding FPCR 11st'
0001FC70	00000000 F8000000			4132 DC XL16'00000000F800000000000000F8000000'
0001FC80	C4C9C4C2 D940D996			4133 DC CL48'DIDBR Rounding FPCR 11uv'
0001FCB0	00000000 F8000000			4134 DC XL16'00000000F800000000000000F8000000'
0001FCC0	C4C9C4C2 D940D996			4135 DC CL48'DIDBR Rounding FPCR 11wx'
0001FCF0	00000000 F8000000			4136 DC XL16'00000000F800000000000000F8000000'
0001FD00	C4C9C4C2 D940D996			4137 DC CL48'DIDBR Rounding FPCR 12ab'
0001FD30	00000000 F8000000			4138 DC XL16'00000000F800000000000000F8000000'
0001FD40	C4C9C4C2 D940D996			4139 DC CL48'DIDBR Rounding FPCR 12cd'
0001FD70	00000000 F8000000			4140 DC XL16'00000000F800000000000000F8000000'
0001FD80	C4C9C4C2 D940D996			4141 DC CL48'DIDBR Rounding FPCR 12ef'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001FDB0	00000000 F8000000			4142 DC XL16'00000000F800000000000000F8000000'
0001FDC0	C4C9C4C2 D940D996			4143 DC CL48'DIDBR Rounding FPCR 12gh'
0001FDF0	00000000 F8000000			4144 DC XL16'00000000F800000000000000F8000000'
0001FE00	C4C9C4C2 D940D996			4145 DC CL48'DIDBR Rounding FPCR 12ij'
0001FE30	00000000 F8000000			4146 DC XL16'00000000F800000000000000F8000000'
0001FE40	C4C9C4C2 D940D996			4147 DC CL48'DIDBR Rounding FPCR 12kl'
0001FE70	00000000 F8000000			4148 DC XL16'00000000F800000000000000F8000000'
0001FE80	C4C9C4C2 D940D996			4149 DC CL48'DIDBR Rounding FPCR 12mn'
0001FEB0	00000000 F8000000			4150 DC XL16'00000000F800000000000000F8000000'
0001FEC0	C4C9C4C2 D940D996			4151 DC CL48'DIDBR Rounding FPCR 12op'
0001FEF0	00000000 F8000000			4152 DC XL16'00000000F800000000000000F8000000'
0001FF00	C4C9C4C2 D940D996			4153 DC CL48'DIDBR Rounding FPCR 12qr'
0001FF30	00000000 F8000000			4154 DC XL16'00000000F800000000000000F8000000'
0001FF40	C4C9C4C2 D940D996			4155 DC CL48'DIDBR Rounding FPCR 12st'
0001FF70	00000000 F8000000			4156 DC XL16'00000000F800000000000000F8000000'
0001FF80	C4C9C4C2 D940D996			4157 DC CL48'DIDBR Rounding FPCR 12uv'
0001FFB0	00000000 F8000000			4158 DC XL16'00000000F800000000000000F8000000'
0001FFC0	C4C9C4C2 D940D996			4159 DC CL48'DIDBR Rounding FPCR 12wx'
0001FFF0	00000000 F8000000			4160 DC XL16'00000000F800000000000000F8000000'
		00000090	00000001	4161 LBFPRMOF_NUM EQU (*-LBFPRMOF_GOOD)/64
				4162 *
				4163 *
		00020000	00000001	4164 SBFPOUT_GOOD EQU *
00020000	C4C9C5C2 D9408689			4165 DC CL48'DIEBR finite test -8/-4 1a'
00020030	80000000 40000000			4166 DC XL16'8000000040000000C100000000000000'
00020040	C4C9C5C2 D9408689			4167 DC CL48'DIEBR finite test -8/-4 1b'
00020070	80000000 40000000			4168 DC XL16'8000000040000000C100000000000000'
00020080	C4C9C5C2 D9408689			4169 DC CL48'DIEBR finite test -7/-4 2a'
000200B0	3F800000 40000000			4170 DC XL16'3F80000040000000C0E0000000000000'
000200C0	C4C9C5C2 D9408689			4171 DC CL48'DIEBR finite test -7/-4 2b'
000200F0	3F800000 40000000			4172 DC XL16'3F80000040000000C0E0000000000000'
00020100	C4C9C5C2 D9408689			4173 DC CL48'DIEBR finite test -6/-4 3a'
00020130	40000000 40000000			4174 DC XL16'4000000040000000C0C0000000000000'
00020140	C4C9C5C2 D9408689			4175 DC CL48'DIEBR finite test -6/-4 3b'
00020170	40000000 40000000			4176 DC XL16'4000000040000000C0C0000000000000'
00020180	C4C9C5C2 D9408689			4177 DC CL48'DIEBR finite test -5/-4 4a'
000201B0	BF800000 3F800000			4178 DC XL16'BF8000003F800000C0A0000000000000'
000201C0	C4C9C5C2 D9408689			4179 DC CL48'DIEBR finite test -5/-4 4b'
000201F0	BF800000 3F800000			4180 DC XL16'BF8000003F800000C0A0000000000000'
00020200	C4C9C5C2 D9408689			4181 DC CL48'DIEBR finite test -4/-4 5a'
00020230	80000000 3F800000			4182 DC XL16'800000003F800000C080000000000000'
00020240	C4C9C5C2 D9408689			4183 DC CL48'DIEBR finite test -4/-4 5b'
00020270	80000000 3F800000			4184 DC XL16'800000003F800000C080000000000000'
00020280	C4C9C5C2 D9408689			4185 DC CL48'DIEBR finite test -3/-4 6a'
000202B0	3F800000 3F800000			4186 DC XL16'3F8000003F800000C040000000000000'
000202C0	C4C9C5C2 D9408689			4187 DC CL48'DIEBR finite test -3/-4 6b'
000202F0	3F800000 3F800000			4188 DC XL16'3F8000003F800000C040000000000000'
00020300	C4C9C5C2 D9408689			4189 DC CL48'DIEBR finite test -2/-4 7a'
00020330	C0000000 00000000			4190 DC XL16'C000000000000000C000000000000000'
00020340	C4C9C5C2 D9408689			4191 DC CL48'DIEBR finite test -2/-4 7b'
00020370	C0000000 00000000			4192 DC XL16'C000000000000000C000000000000000'
00020380	C4C9C5C2 D9408689			4193 DC CL48'DIEBR finite test -1/-4 8a'
000203B0	BF800000 00000000			4194 DC XL16'BF80000000000000BF80000000000000'
000203C0	C4C9C5C2 D9408689			4195 DC CL48'DIEBR finite test -1/-4 8b'
000203F0	BF800000 00000000			4196 DC XL16'BF80000000000000BF80000000000000'
00020400	C4C9C5C2 D9408689			4197 DC CL48'DIEBR finite test +1/-4 9a'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00020430	3F800000 80000000			4198 DC XL16'3F800000800000003F80000000000000'
00020440	C4C9C5C2 D9408689			4199 DC CL48'DIEBR finite test +1/-4 9b'
00020470	3F800000 80000000			4200 DC XL16'3F800000800000003F80000000000000'
00020480	C4C9C5C2 D9408689			4201 DC CL48'DIEBR finite test +2/-4 10a'
000204B0	40000000 80000000			4202 DC XL16'40000000800000004000000000000000'
000204C0	C4C9C5C2 D9408689			4203 DC CL48'DIEBR finite test +2/-4 10b'
000204F0	40000000 80000000			4204 DC XL16'40000000800000004000000000000000'
00020500	C4C9C5C2 D9408689			4205 DC CL48'DIEBR finite test +3/-4 11a'
00020530	BF800000 BF800000			4206 DC XL16'BF800000BF8000004040000000000000'
00020540	C4C9C5C2 D9408689			4207 DC CL48'DIEBR finite test +3/-4 11b'
00020570	BF800000 BF800000			4208 DC XL16'BF800000BF8000004040000000000000'
00020580	C4C9C5C2 D9408689			4209 DC CL48'DIEBR finite test +4/-4 12a'
000205B0	00000000 BF800000			4210 DC XL16'00000000BF8000004080000000000000'
000205C0	C4C9C5C2 D9408689			4211 DC CL48'DIEBR finite test +4/-4 12b'
000205F0	00000000 BF800000			4212 DC XL16'00000000BF8000004080000000000000'
00020600	C4C9C5C2 D9408689			4213 DC CL48'DIEBR finite test +5/-4 13a'
00020630	3F800000 BF800000			4214 DC XL16'3F800000BF80000040A0000000000000'
00020640	C4C9C5C2 D9408689			4215 DC CL48'DIEBR finite test +5/-4 13b'
00020670	3F800000 BF800000			4216 DC XL16'3F800000BF80000040A0000000000000'
00020680	C4C9C5C2 D9408689			4217 DC CL48'DIEBR finite test +6/-4 14a'
000206B0	C0000000 C0000000			4218 DC XL16'C0000000C000000040C0000000000000'
000206C0	C4C9C5C2 D9408689			4219 DC CL48'DIEBR finite test +6/-4 14b'
000206F0	C0000000 C0000000			4220 DC XL16'C0000000C000000040C0000000000000'
00020700	C4C9C5C2 D9408689			4221 DC CL48'DIEBR finite test 15a'
00020730	BF800000 C0000000			4222 DC XL16'BF800000C000000040E0000000000000'
00020740	C4C9C5C2 D9408689			4223 DC CL48'DIEBR finite test 15b'
00020770	BF800000 C0000000			4224 DC XL16'BF800000C000000040E0000000000000'
00020780	C4C9C5C2 D9408689			4225 DC CL48'DIEBR finite test 16a'
000207B0	00000000 C0000000			4226 DC XL16'00000000C00000004100000000000000'
000207C0	C4C9C5C2 D9408689			4227 DC CL48'DIEBR finite test 16b'
000207F0	00000000 C0000000			4228 DC XL16'00000000C00000004100000000000000'
00020800	C4C9C5C2 D9408689			4229 DC CL48'DIEBR finite test 17a'
00020830	80000000 C0000000			4230 DC XL16'80000000C0000000C100000000000000'
00020840	C4C9C5C2 D9408689			4231 DC CL48'DIEBR finite test 17b'
00020870	80000000 C0000000			4232 DC XL16'80000000C0000000C100000000000000'
00020880	C4C9C5C2 D9408689			4233 DC CL48'DIEBR finite test 18a'
000208B0	3F800000 C0000000			4234 DC XL16'3F800000C0000000C0E0000000000000'
000208C0	C4C9C5C2 D9408689			4235 DC CL48'DIEBR finite test 18b'
000208F0	3F800000 C0000000			4236 DC XL16'3F800000C0000000C0E0000000000000'
00020900	C4C9C5C2 D9408689			4237 DC CL48'DIEBR finite test 19a'
00020930	40000000 C0000000			4238 DC XL16'40000000C0000000C0C0000000000000'
00020940	C4C9C5C2 D9408689			4239 DC CL48'DIEBR finite test 19b'
00020970	40000000 C0000000			4240 DC XL16'40000000C0000000C0C0000000000000'
00020980	C4C9C5C2 D9408689			4241 DC CL48'DIEBR finite test 20a'
000209B0	BF800000 BF800000			4242 DC XL16'BF800000BF800000C0A0000000000000'
000209C0	C4C9C5C2 D9408689			4243 DC CL48'DIEBR finite test 20b'
000209F0	BF800000 BF800000			4244 DC XL16'BF800000BF800000C0A0000000000000'
00020A00	C4C9C5C2 D9408689			4245 DC CL48'DIEBR finite test 21a'
00020A30	80000000 BF800000			4246 DC XL16'80000000BF800000C080000000000000'
00020A40	C4C9C5C2 D9408689			4247 DC CL48'DIEBR finite test 21b'
00020A70	80000000 BF800000			4248 DC XL16'80000000BF800000C080000000000000'
00020A80	C4C9C5C2 D9408689			4249 DC CL48'DIEBR finite test 22a'
00020AB0	3F800000 BF800000			4250 DC XL16'3F800000BF800000C040000000000000'
00020AC0	C4C9C5C2 D9408689			4251 DC CL48'DIEBR finite test 22b'
00020AF0	3F800000 BF800000			4252 DC XL16'3F800000BF800000C040000000000000'
00020B00	C4C9C5C2 D9408689			4253 DC CL48'DIEBR finite test 23a'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00020B30	C0000000 80000000			4254 DC XL16'C000000080000000C000000000000000'
00020B40	C4C9C5C2 D9408689			4255 DC CL48'DIEBR finite test 23b'
00020B70	C0000000 80000000			4256 DC XL16'C000000080000000C000000000000000'
00020B80	C4C9C5C2 D9408689			4257 DC CL48'DIEBR finite test 24a'
00020BB0	3F800000 00000000			4258 DC XL16'3F800000000000003F80000000000000'
00020BC0	C4C9C5C2 D9408689			4259 DC CL48'DIEBR finite test 24b'
00020BF0	3F800000 00000000			4260 DC XL16'3F800000000000003F80000000000000'
00020C00	C4C9C5C2 D9408689			4261 DC CL48'DIEBR finite test 25a'
00020C30	3F800000 00000000			4262 DC XL16'3F800000000000003F80000000000000'
00020C40	C4C9C5C2 D9408689			4263 DC CL48'DIEBR finite test 25b'
00020C70	3F800000 00000000			4264 DC XL16'3F800000000000003F80000000000000'
00020C80	C4C9C5C2 D9408689			4265 DC CL48'DIEBR finite test 26a'
00020CB0	40000000 00000000			4266 DC XL16'40000000000000004000000000000000'
00020CC0	C4C9C5C2 D9408689			4267 DC CL48'DIEBR finite test 26b'
00020CF0	40000000 00000000			4268 DC XL16'40000000000000004000000000000000'
00020D00	C4C9C5C2 D9408689			4269 DC CL48'DIEBR finite test 27a'
00020D30	BF800000 3F800000			4270 DC XL16'BF8000003F8000004040000000000000'
00020D40	C4C9C5C2 D9408689			4271 DC CL48'DIEBR finite test 27b'
00020D70	BF800000 3F800000			4272 DC XL16'BF8000003F8000004040000000000000'
00020D80	C4C9C5C2 D9408689			4273 DC CL48'DIEBR finite test 28a'
00020DB0	00000000 3F800000			4274 DC XL16'000000003F8000004080000000000000'
00020DC0	C4C9C5C2 D9408689			4275 DC CL48'DIEBR finite test 28b'
00020DF0	00000000 3F800000			4276 DC XL16'000000003F8000004080000000000000'
00020E00	C4C9C5C2 D9408689			4277 DC CL48'DIEBR finite test 29a'
00020E30	3F800000 3F800000			4278 DC XL16'3F8000003F80000040A0000000000000'
00020E40	C4C9C5C2 D9408689			4279 DC CL48'DIEBR finite test 29b'
00020E70	3F800000 3F800000			4280 DC XL16'3F8000003F80000040A0000000000000'
00020E80	C4C9C5C2 D9408689			4281 DC CL48'DIEBR finite test 30a'
00020EB0	C0000000 40000000			4282 DC XL16'C00000004000000040C0000000000000'
00020EC0	C4C9C5C2 D9408689			4283 DC CL48'DIEBR finite test 30b'
00020EF0	C0000000 40000000			4284 DC XL16'C00000004000000040C0000000000000'
00020F00	C4C9C5C2 D9408689			4285 DC CL48'DIEBR finite test 31a'
00020F30	BF800000 40000000			4286 DC XL16'BF8000004000000040E0000000000000'
00020F40	C4C9C5C2 D9408689			4287 DC CL48'DIEBR finite test 31b'
00020F70	BF800000 40000000			4288 DC XL16'BF8000004000000040E0000000000000'
00020F80	C4C9C5C2 D9408689			4289 DC CL48'DIEBR finite test 32a'
00020FB0	00000000 40000000			4290 DC XL16'00000000400000004100000000000000'
00020FC0	C4C9C5C2 D9408689			4291 DC CL48'DIEBR finite test 32b'
00020FF0	00000000 40000000			4292 DC XL16'00000000400000004100000000000000'
00021000	C4C9C5C2 D940A385			4293 DC CL48'DIEBR test 33a two finites'
00021030	40800000 C0800000			4294 DC XL16'40800000C08000004220000000000000'
00021040	C4C9C5C2 D940A385			4295 DC CL48'DIEBR test 33b two finites'
00021070	40800000 C0800000			4296 DC XL16'40800000C08000004220000000000000'
00021080	C4C9C5C2 D940A385			4297 DC CL48'DIEBR test 34a two finites'
000210B0	00000000 69FFFFFF			4298 DC XL16'0000000069FFFFFF1F7FFFFFF00000000'
000210C0	C4C9C5C2 D940A385			4299 DC CL48'DIEBR test 34b two finites'
000210F0	00000000 69FFFFFF			4300 DC XL16'0000000069FFFFFF1F7FFFFFF00000000'
00021100	C4C9C5C2 D940A385			4301 DC CL48'DIEBR test 35a two finites'
00021130	00000001 3F800000			4302 DC XL16'000000013F80000000FFFFFF00000000'
00021140	C4C9C5C2 D940A385			4303 DC CL48'DIEBR test 35b two finites'
00021170	55000000 3F800000			4304 DC XL16'550000003F8000005500000000000000'
00021180	C4C9C5C2 D940A385			4305 DC CL48'DIEBR test 36a two finites'
000211B0	40800000 4BAAAAAA			4306 DC XL16'408000004BAAAAAA4C80000000000000'
000211C0	C4C9C5C2 D940A385			4307 DC CL48'DIEBR test 36b two finites'
000211F0	40800000 4BAAAAAA			4308 DC XL16'408000004BAAAAAA4C80000000000000'
00021200	C4C9C5C2 D940A385			4309 DC CL48'DIEBR test 37a two finites'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00021230	40100000 00000000			4310 DC XL16'40100000000000004010000000000000'
00021240	C4C9C5C2 D940A385			4311 DC CL48'DIEBR test 37b two finites'
00021270	40100000 00000000			4312 DC XL16'40100000000000004010000000000000'
00021280	C4C9C5C2 D940A385			4313 DC CL48'DIEBR test 38a two finites'
000212B0	73A00000 683A2E8A			4314 DC XL16'73A00000683A2E8A73A0000000000000'
000212C0	C4C9C5C2 D940A385			4315 DC CL48'DIEBR test 38a two finites'
000212F0	73A00000 683A2E8A			4316 DC XL16'73A00000683A2E8A73A0000000000000'
		0000004C	00000001	4317 SBFPOUT_NUM EQU (*-SBFPOUT_GOOD)/64
				4318 *
				4319 *
		00021300	00000001	4320 SBFPLGS_GOOD EQU *
00021300	C4C9C5C2 D940C6D7			4321 DC CL48'DIEBR FPCR finite test -8/-4 1'
00021330	00000000 00000000			4322 DC XL16'0000000000000000F800000000000000'
00021340	C4C9C5C2 D940C6D7			4323 DC CL48'DIEBR FPCR finite test -7/-4 2'
00021370	00000000 00000000			4324 DC XL16'0000000000000000F800000000000000'
00021380	C4C9C5C2 D940C6D7			4325 DC CL48'DIEBR FPCR finite test -6/-4 3'
000213B0	00000000 00000000			4326 DC XL16'0000000000000000F800000000000000'
000213C0	C4C9C5C2 D940C6D7			4327 DC CL48'DIEBR FPCR finite test -5/-4 4'
000213F0	00000000 00000000			4328 DC XL16'0000000000000000F800000000000000'
00021400	C4C9C5C2 D940C6D7			4329 DC CL48'DIEBR FPCR finite test -4/-4 5'
00021430	00000000 00000000			4330 DC XL16'0000000000000000F800000000000000'
00021440	C4C9C5C2 D940C6D7			4331 DC CL48'DIEBR FPCR finite test -3/-4 6'
00021470	00000000 00000000			4332 DC XL16'0000000000000000F800000000000000'
00021480	C4C9C5C2 D940C6D7			4333 DC CL48'DIEBR FPCR finite test -2/-4 7'
000214B0	00000000 00000000			4334 DC XL16'0000000000000000F800000000000000'
000214C0	C4C9C5C2 D940C6D7			4335 DC CL48'DIEBR FPCR finite test -1/-4 8'
000214F0	00000000 00000000			4336 DC XL16'0000000000000000F800000000000000'
00021500	C4C9C5C2 D940C6D7			4337 DC CL48'DIEBR FPCR finite test +1/-4 9'
00021530	00000000 00000000			4338 DC XL16'0000000000000000F800000000000000'
00021540	C4C9C5C2 D940C6D7			4339 DC CL48'DIEBR FPCR finite test +2/-4 10'
00021570	00000000 00000000			4340 DC XL16'0000000000000000F800000000000000'
00021580	C4C9C5C2 D940C6D7			4341 DC CL48'DIEBR FPCR finite test +3/-4 11'
000215B0	00000000 00000000			4342 DC XL16'0000000000000000F800000000000000'
000215C0	C4C9C5C2 D940C6D7			4343 DC CL48'DIEBR FPCR finite test +4/-4 12'
000215F0	00000000 00000000			4344 DC XL16'0000000000000000F800000000000000'
00021600	C4C9C5C2 D940C6D7			4345 DC CL48'DIEBR FPCR finite test +5/-4 13'
00021630	00000000 00000000			4346 DC XL16'0000000000000000F800000000000000'
00021640	C4C9C5C2 D940C6D7			4347 DC CL48'DIEBR FPCR finite test +6/-4 14'
00021670	00000000 00000000			4348 DC XL16'0000000000000000F800000000000000'
00021680	C4C9C5C2 D940C6D7			4349 DC CL48'DIEBR FPCR finite test 15'
000216B0	00000000 00000000			4350 DC XL16'0000000000000000F800000000000000'
000216C0	C4C9C5C2 D940C6D7			4351 DC CL48'DIEBR FPCR finite test 16'
000216F0	00000000 00000000			4352 DC XL16'0000000000000000F800000000000000'
00021700	C4C9C5C2 D940C6D7			4353 DC CL48'DIEBR FPCR finite test 17'
00021730	00000000 00000000			4354 DC XL16'0000000000000000F800000000000000'
00021740	C4C9C5C2 D940C6D7			4355 DC CL48'DIEBR FPCR finite test 18'
00021770	00000000 00000000			4356 DC XL16'0000000000000000F800000000000000'
00021780	C4C9C5C2 D940C6D7			4357 DC CL48'DIEBR FPCR finite test 19'
000217B0	00000000 00000000			4358 DC XL16'0000000000000000F800000000000000'
000217C0	C4C9C5C2 D940C6D7			4359 DC CL48'DIEBR FPCR finite test 20'
000217F0	00000000 00000000			4360 DC XL16'0000000000000000F800000000000000'
00021800	C4C9C5C2 D940C6D7			4361 DC CL48'DIEBR FPCR finite test 21'
00021830	00000000 00000000			4362 DC XL16'0000000000000000F800000000000000'
00021840	C4C9C5C2 D940C6D7			4363 DC CL48'DIEBR FPCR finite test 22'
00021870	00000000 00000000			4364 DC XL16'0000000000000000F800000000000000'
00021880	C4C9C5C2 D940C6D7			4365 DC CL48'DIEBR FPCR finite test 23'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000218B0	00000000 00000000			4366 DC XL16'0000000000000000F800000000000000'
000218C0	C4C9C5C2 D940C6D7			4367 DC CL48'DIEBR FPCR finite test 24'
000218F0	00000000 00000000			4368 DC XL16'0000000000000000F800000000000000'
00021900	C4C9C5C2 D940C6D7			4369 DC CL48'DIEBR FPCR finite test 25'
00021930	00000000 00000000			4370 DC XL16'0000000000000000F800000000000000'
00021940	C4C9C5C2 D940C6D7			4371 DC CL48'DIEBR FPCR finite test 26'
00021970	00000000 00000000			4372 DC XL16'0000000000000000F800000000000000'
00021980	C4C9C5C2 D940C6D7			4373 DC CL48'DIEBR FPCR finite test 27'
000219B0	00000000 00000000			4374 DC XL16'0000000000000000F800000000000000'
000219C0	C4C9C5C2 D940C6D7			4375 DC CL48'DIEBR FPCR finite test 28'
000219F0	00000000 00000000			4376 DC XL16'0000000000000000F800000000000000'
00021A00	C4C9C5C2 D940C6D7			4377 DC CL48'DIEBR FPCR finite test 29'
00021A30	00000000 00000000			4378 DC XL16'0000000000000000F800000000000000'
00021A40	C4C9C5C2 D940C6D7			4379 DC CL48'DIEBR FPCR finite test 30'
00021A70	00000000 00000000			4380 DC XL16'0000000000000000F800000000000000'
00021A80	C4C9C5C2 D940C6D7			4381 DC CL48'DIEBR FPCR finite test 31'
00021AB0	00000000 00000000			4382 DC XL16'0000000000000000F800000000000000'
00021AC0	C4C9C5C2 D940C6D7			4383 DC CL48'DIEBR FPCR finite test 32'
00021AF0	00000000 00000000			4384 DC XL16'0000000000000000F800000000000000'
00021B00	C4C9C5C2 D940C6D7			4385 DC CL48'DIEBR FPCR finite test 33'
00021B30	00000000 00000000			4386 DC XL16'0000000000000000F800000000000000'
00021B40	C4C9C5C2 D940C6D7			4387 DC CL48'DIEBR FPCR finite test 34'
00021B70	00000001 00000001			4388 DC XL16'0000000100000001F800000100000001'
00021B80	C4C9C5C2 D940C6D7			4389 DC CL48'DIEBR FPCR finite test 35'
00021BB0	00000000 00000000			4390 DC XL16'0000000000000000F8001000000080000'
00021BC0	C4C9C5C2 D940C6D7			4391 DC CL48'DIEBR FPCR finite test 36'
00021BF0	00000002 00000002			4392 DC XL16'0000000200000002F800000200000002'
00021C00	C4C9C5C2 D940C6D7			4393 DC CL48'DIEBR FPCR finite test 37'
00021C30	00000000 00000000			4394 DC XL16'0000000000000000F800000000000000'
00021C40	C4C9C5C2 D940C6D7			4395 DC CL48'DIEBR FPCR finite test 38'
00021C70	00000003 00080003			4396 DC XL16'0000000300080003F800000300080003'
		00000026	00000001	4397 SBFPFLGS_NUM EQU (*-SBFPFLGS_GOOD)/64
				4398 *
				4399 *
		00021C80	00000001	4400 LBFPFLGS_GOOD EQU *
00021C80	C4C9C4C2 D940C6D7			4401 DC CL48'DIDBR FPCR finite test -8/-4 1'
00021CB0	00000000 00000000			4402 DC XL16'0000000000000000F800000000000000'
00021CC0	C4C9C4C2 D940C6D7			4403 DC CL48'DIDBR FPCR finite test -7/-4 2'
00021CF0	00000000 00000000			4404 DC XL16'0000000000000000F800000000000000'
00021D00	C4C9C4C2 D940C6D7			4405 DC CL48'DIDBR FPCR finite test -6/-4 3'
00021D30	00000000 00000000			4406 DC XL16'0000000000000000F800000000000000'
00021D40	C4C9C4C2 D940C6D7			4407 DC CL48'DIDBR FPCR finite test -5/-4 4'
00021D70	00000000 00000000			4408 DC XL16'0000000000000000F800000000000000'
00021D80	C4C9C4C2 D940C6D7			4409 DC CL48'DIDBR FPCR finite test -4/-4 5'
00021DB0	00000000 00000000			4410 DC XL16'0000000000000000F800000000000000'
00021DC0	C4C9C4C2 D940C6D7			4411 DC CL48'DIDBR FPCR finite test -3/-4 6'
00021DF0	00000000 00000000			4412 DC XL16'0000000000000000F800000000000000'
00021E00	C4C9C4C2 D940C6D7			4413 DC CL48'DIDBR FPCR finite test -2/-4 7'
00021E30	00000000 00000000			4414 DC XL16'0000000000000000F800000000000000'
00021E40	C4C9C4C2 D940C6D7			4415 DC CL48'DIDBR FPCR finite test -1/-4 8'
00021E70	00000000 00000000			4416 DC XL16'0000000000000000F800000000000000'
00021E80	C4C9C4C2 D940C6D7			4417 DC CL48'DIDBR FPCR finite test +1/-4 9'
00021EB0	00000000 00000000			4418 DC XL16'0000000000000000F800000000000000'
00021EC0	C4C9C4C2 D940C6D7			4419 DC CL48'DIDBR FPCR finite test +2/-4 10'
00021EF0	00000000 00000000			4420 DC XL16'0000000000000000F800000000000000'
00021F00	C4C9C4C2 D940C6D7			4421 DC CL48'DIDBR FPCR finite test +3/-4 10'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00021F30	00000000 00000000			4422 DC XL16'0000000000000000F800000000000000'
00021F40	C4C9C4C2 D940C6D7			4423 DC CL48'DIDBR FPCR finite test +4/-4 12'
00021F70	00000000 00000000			4424 DC XL16'0000000000000000F800000000000000'
00021F80	C4C9C4C2 D940C6D7			4425 DC CL48'DIDBR FPCR finite test +5/-4 13'
00021FB0	00000000 00000000			4426 DC XL16'0000000000000000F800000000000000'
00021FC0	C4C9C4C2 D940C6D7			4427 DC CL48'DIDBR FPCR finite test +6/-4 14'
00021FF0	00000000 00000000			4428 DC XL16'0000000000000000F800000000000000'
00022000	C4C9C4C2 D940C6D7			4429 DC CL48'DIDBR FPCR finite test 15'
00022030	00000000 00000000			4430 DC XL16'0000000000000000F800000000000000'
00022040	C4C9C4C2 D940C6D7			4431 DC CL48'DIDBR FPCR finite test 16'
00022070	00000000 00000000			4432 DC XL16'0000000000000000F800000000000000'
00022080	C4C9C4C2 D940C6D7			4433 DC CL48'DIDBR FPCR finite test 17'
000220B0	00000000 00000000			4434 DC XL16'0000000000000000F800000000000000'
000220C0	C4C9C4C2 D940C6D7			4435 DC CL48'DIDBR FPCR finite test 18'
000220F0	00000000 00000000			4436 DC XL16'0000000000000000F800000000000000'
00022100	C4C9C4C2 D940C6D7			4437 DC CL48'DIDBR FPCR finite test 19'
00022130	00000000 00000000			4438 DC XL16'0000000000000000F800000000000000'
00022140	C4C9C4C2 D940C6D7			4439 DC CL48'DIDBR FPCR finite test 20'
00022170	00000000 00000000			4440 DC XL16'0000000000000000F800000000000000'
00022180	C4C9C4C2 D940C6D7			4441 DC CL48'DIDBR FPCR finite test 21'
000221B0	00000000 00000000			4442 DC XL16'0000000000000000F800000000000000'
000221C0	C4C9C4C2 D940C6D7			4443 DC CL48'DIDBR FPCR finite test 22'
000221F0	00000000 00000000			4444 DC XL16'0000000000000000F800000000000000'
00022200	C4C9C4C2 D940C6D7			4445 DC CL48'DIDBR FPCR finite test 23'
00022230	00000000 00000000			4446 DC XL16'0000000000000000F800000000000000'
00022240	C4C9C4C2 D940C6D7			4447 DC CL48'DIDBR FPCR finite test 24'
00022270	00000000 00000000			4448 DC XL16'0000000000000000F800000000000000'
00022280	C4C9C4C2 D940C6D7			4449 DC CL48'DIDBR FPCR finite test 25'
000222B0	00000000 00000000			4450 DC XL16'0000000000000000F800000000000000'
000222C0	C4C9C4C2 D940C6D7			4451 DC CL48'DIDBR FPCR finite test 26'
000222F0	00000000 00000000			4452 DC XL16'0000000000000000F800000000000000'
00022300	C4C9C4C2 D940C6D7			4453 DC CL48'DIDBR FPCR finite test 27'
00022330	00000000 00000000			4454 DC XL16'0000000000000000F800000000000000'
00022340	C4C9C4C2 D940C6D7			4455 DC CL48'DIDBR FPCR finite test 28'
00022370	00000000 00000000			4456 DC XL16'0000000000000000F800000000000000'
00022380	C4C9C4C2 D940C6D7			4457 DC CL48'DIDBR FPCR finite test 29'
000223B0	00000000 00000000			4458 DC XL16'0000000000000000F800000000000000'
000223C0	C4C9C4C2 D940C6D7			4459 DC CL48'DIDBR FPCR finite test 30'
000223F0	00000000 00000000			4460 DC XL16'0000000000000000F800000000000000'
00022400	C4C9C4C2 D940C6D7			4461 DC CL48'DIDBR FPCR finite test 31'
00022430	00000000 00000000			4462 DC XL16'0000000000000000F800000000000000'
00022440	C4C9C4C2 D940C6D7			4463 DC CL48'DIDBR FPCR finite test 32'
00022470	00000000 00000000			4464 DC XL16'0000000000000000F800000000000000'
00022480	C4C9C4C2 D940C6D7			4465 DC CL48'DIDBR FPCR finite test 33'
000224B0	00000000 00000000			4466 DC XL16'0000000000000000F800000000000000'
000224C0	C4C9C4C2 D940C6D7			4467 DC CL48'DIDBR FPCR finite test 34'
000224F0	00000001 00000001			4468 DC XL16'0000000100000001F800000100000001'
00022500	C4C9C4C2 D940C6D7			4469 DC CL48'DIDBR FPCR finite test 35'
00022530	00000003 00080003			4470 DC XL16'0000000300080003F800000300080003'
00022540	C4C9C4C2 D940C6D7			4471 DC CL48'DIDBR FPCR finite test 36'
00022570	00000000 00000000			4472 DC XL16'0000000000000000F800100000080000'
00022580	C4C9C4C2 D940C6D7			4473 DC CL48'DIDBR FPCR finite test 37'
000225B0	00000002 00000002			4474 DC XL16'0000000200000002F800000200000002'
000225C0	C4C9C4C2 D940C6D7			4475 DC CL48'DIDBR FPCR finite test 38'
000225F0	00000000 00000000			4476 DC XL16'0000000000000000F800000000000000'
		00000026	00000001	4477 LBFPFLGS_NUM EQU (*-LBFPFLGS_GOOD)/64

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				4478 *
				4479 *
		00022600	00000001	4480 LBFPOUT_GOOD EQU *
00022600	C4C9C4C2	D9408689		4481 DC CL48'DIDBR finite test -8/-4 1a'
00022630	80000000	00000000		4482 DC XL16'8000000000000000400000000000000'
00022640	C4C9C4C2	D9408689		4483 DC CL48'DIDBR finite test -8/-4 1a'
00022670	C0200000	00000000		4484 DC XL16'C02000000000000000000000000000'
00022680	C4C9C4C2	D9408689		4485 DC CL48'DIDBR finite test -8/-4 1b'
000226B0	80000000	00000000		4486 DC XL16'8000000000000000400000000000000'
000226C0	C4C9C4C2	D9408689		4487 DC CL48'DIDBR finite test -8/-4 1b'
000226F0	C0200000	00000000		4488 DC XL16'C02000000000000000000000000000'
00022700	C4C9C4C2	D9408689		4489 DC CL48'DIDBR finite test -7/-4 2a'
00022730	3FF00000	00000000		4490 DC XL16'3FF0000000000000400000000000000'
00022740	C4C9C4C2	D9408689		4491 DC CL48'DIDBR finite test -7/-4 2a'
00022770	C01C0000	00000000		4492 DC XL16'C01C00000000000000000000000000'
00022780	C4C9C4C2	D9408689		4493 DC CL48'DIDBR finite test -7/-4 2b'
000227B0	3FF00000	00000000		4494 DC XL16'3FF0000000000000400000000000000'
000227C0	C4C9C4C2	D9408689		4495 DC CL48'DIDBR finite test -7/-4 2b'
000227F0	C01C0000	00000000		4496 DC XL16'C01C00000000000000000000000000'
00022800	C4C9C4C2	D9408689		4497 DC CL48'DIDBR finite test -6/-4 3a'
00022830	40000000	00000000		4498 DC XL16'4000000000000000400000000000000'
00022840	C4C9C4C2	D9408689		4499 DC CL48'DIDBR finite test -6/-4 3a'
00022870	C0180000	00000000		4500 DC XL16'C01800000000000000000000000000'
00022880	C4C9C4C2	D9408689		4501 DC CL48'DIDBR finite test -6/-4 3b'
000228B0	40000000	00000000		4502 DC XL16'4000000000000000400000000000000'
000228C0	C4C9C4C2	D9408689		4503 DC CL48'DIDBR finite test -6/-4 3b'
000228F0	C0180000	00000000		4504 DC XL16'C01800000000000000000000000000'
00022900	C4C9C4C2	D9408689		4505 DC CL48'DIDBR finite test -5/-4 4a'
00022930	BFF00000	00000000		4506 DC XL16'BFF00000000000003FF00000000000000'
00022940	C4C9C4C2	D9408689		4507 DC CL48'DIDBR finite test -5/-4 4a'
00022970	C0140000	00000000		4508 DC XL16'C01400000000000000000000000000'
00022980	C4C9C4C2	D9408689		4509 DC CL48'DIDBR finite test -5/-4 4b'
000229B0	BFF00000	00000000		4510 DC XL16'BFF00000000000003FF00000000000000'
000229C0	C4C9C4C2	D9408689		4511 DC CL48'DIDBR finite test -5/-4 4b'
000229F0	C0140000	00000000		4512 DC XL16'C01400000000000000000000000000'
00022A00	C4C9C4C2	D9408689		4513 DC CL48'DIDBR finite test -4/-4 5a'
00022A30	80000000	00000000		4514 DC XL16'80000000000000003FF0000000000000'
00022A40	C4C9C4C2	D9408689		4515 DC CL48'DIDBR finite test -4/-4 5a'
00022A70	C0100000	00000000		4516 DC XL16'C01000000000000000000000000000'
00022A80	C4C9C4C2	D9408689		4517 DC CL48'DIDBR finite test -4/-4 5b'
00022AB0	80000000	00000000		4518 DC XL16'80000000000000003FF0000000000000'
00022AC0	C4C9C4C2	D9408689		4519 DC CL48'DIDBR finite test -4/-4 5b'
00022AF0	C0100000	00000000		4520 DC XL16'C01000000000000000000000000000'
00022B00	C4C9C4C2	D9408689		4521 DC CL48'DIDBR finite test -3/-4 6a'
00022B30	3FF00000	00000000		4522 DC XL16'3FF00000000000003FF0000000000000'
00022B40	C4C9C4C2	D9408689		4523 DC CL48'DIDBR finite test -3/-4 6a'
00022B70	C0080000	00000000		4524 DC XL16'C00800000000000000000000000000'
00022B80	C4C9C4C2	D9408689		4525 DC CL48'DIDBR finite test -3/-4 6b'
00022BB0	3FF00000	00000000		4526 DC XL16'3FF00000000000003FF0000000000000'
00022BC0	C4C9C4C2	D9408689		4527 DC CL48'DIDBR finite test -3/-4 6b'
00022BF0	C0080000	00000000		4528 DC XL16'C00800000000000000000000000000'
00022C00	C4C9C4C2	D9408689		4529 DC CL48'DIDBR finite test -2/-4 7a'
00022C30	C0000000	00000000		4530 DC XL16'C00000000000000000000000000000'
00022C40	C4C9C4C2	D9408689		4531 DC CL48'DIDBR finite test -2/-4 7a'
00022C70	C0000000	00000000		4532 DC XL16'C00000000000000000000000000000'
00022C80	C4C9C4C2	D9408689		4533 DC CL48'DIDBR finite test -2/-4 7b'

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT	
00022CB0	C0000000	00000000			4534	DC XL16'C0000000000000000000000000000000'
00022CC0	C4C9C4C2	D9408689			4535	DC CL48'DIDBR finite test -2/-4 7b'
00022CF0	C0000000	00000000			4536	DC XL16'C0000000000000000000000000000000'
00022D00	C4C9C4C2	D9408689			4537	DC CL48'DIDBR finite test -1/-4 8a'
00022D30	BFF00000	00000000			4538	DC XL16'BFF0000000000000000000000000000'
00022D40	C4C9C4C2	D9408689			4539	DC CL48'DIDBR finite test -1/-4 8a'
00022D70	BFF00000	00000000			4540	DC XL16'BFF0000000000000000000000000000'
00022D80	C4C9C4C2	D9408689			4541	DC CL48'DIDBR finite test -1/-4 8b'
00022DB0	BFF00000	00000000			4542	DC XL16'BFF0000000000000000000000000000'
00022DC0	C4C9C4C2	D9408689			4543	DC CL48'DIDBR finite test -1/-4 8b'
00022DF0	BFF00000	00000000			4544	DC XL16'BFF0000000000000000000000000000'
00022E00	C4C9C4C2	D9408689			4545	DC CL48'DIDBR finite test +1/-4 9a'
00022E30	3FF00000	00000000			4546	DC XL16'3FF0000000000000000080000000000000'
00022E40	C4C9C4C2	D9408689			4547	DC CL48'DIDBR finite test +1/-4 9a'
00022E70	3FF00000	00000000			4548	DC XL16'3FF00000000000000000000000000000'
00022E80	C4C9C4C2	D9408689			4549	DC CL48'DIDBR finite test +1/-4 9b'
00022EB0	3FF00000	00000000			4550	DC XL16'3FF0000000000000000080000000000000'
00022EC0	C4C9C4C2	D9408689			4551	DC CL48'DIDBR finite test +1/-4 9b'
00022EF0	3FF00000	00000000			4552	DC XL16'3FF00000000000000000000000000000'
00022F00	C4C9C4C2	D9408689			4553	DC CL48'DIDBR finite test +2/-4 10a'
00022F30	40000000	00000000			4554	DC XL16'4000000000000000000080000000000000'
00022F40	C4C9C4C2	D9408689			4555	DC CL48'DIDBR finite test +2/-4 10a'
00022F70	40000000	00000000			4556	DC XL16'40000000000000000000000000000000'
00022F80	C4C9C4C2	D9408689			4557	DC CL48'DIDBR finite test +2/-4 10b'
00022FB0	40000000	00000000			4558	DC XL16'4000000000000000000080000000000000'
00022FC0	C4C9C4C2	D9408689			4559	DC CL48'DIDBR finite test +2/-4 10b'
00022FF0	40000000	00000000			4560	DC XL16'40000000000000000000000000000000'
00023000	C4C9C4C2	D9408689			4561	DC CL48'DIDBR finite test +3/-4 11a'
00023030	BFF00000	00000000			4562	DC XL16'BFF00000000000000BFF00000000000000'
00023040	C4C9C4C2	D9408689			4563	DC CL48'DIDBR finite test +3/-4 11a'
00023070	40080000	00000000			4564	DC XL16'4008000000000000000000000000000000'
00023080	C4C9C4C2	D9408689			4565	DC CL48'DIDBR finite test +3/-4 11b'
000230B0	BFF00000	00000000			4566	DC XL16'BFF00000000000000BFF00000000000000'
000230C0	C4C9C4C2	D9408689			4567	DC CL48'DIDBR finite test +3/-4 11b'
000230F0	40080000	00000000			4568	DC XL16'4008000000000000000000000000000000'
00023100	C4C9C4C2	D9408689			4569	DC CL48'DIDBR finite test +4/-4 12a'
00023130	00000000	00000000			4570	DC XL16'000000000000000000BFF00000000000000'
00023140	C4C9C4C2	D9408689			4571	DC CL48'DIDBR finite test +4/-4 12a'
00023170	40100000	00000000			4572	DC XL16'4010000000000000000000000000000000'
00023180	C4C9C4C2	D9408689			4573	DC CL48'DIDBR finite test +4/-4 12b'
000231B0	00000000	00000000			4574	DC XL16'000000000000000000BFF00000000000000'
000231C0	C4C9C4C2	D9408689			4575	DC CL48'DIDBR finite test +4/-4 12b'
000231F0	40100000	00000000			4576	DC XL16'4010000000000000000000000000000000'
00023200	C4C9C4C2	D9408689			4577	DC CL48'DIDBR finite test +5/-4 13a'
00023230	3FF00000	00000000			4578	DC XL16'3FF000000000000000B

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000233B0	C0000000 00000000			4590 DC XL16'C000000000000000C000000000000000'
000233C0	C4C9C4C2 D9408689			4591 DC CL48'DIDBR finite test +6/-4 14b'
000233F0	40180000 00000000			4592 DC XL16'40180000000000000000000000000000'
00023400	C4C9C4C2 D9408689			4593 DC CL48'DIDBR finite test 15a'
00023430	BFF00000 00000000			4594 DC XL16'BFF00000000000000C000000000000000'
00023440	C4C9C4C2 D9408689			4595 DC CL48'DIDBR finite test 15a'
00023470	401C0000 00000000			4596 DC XL16'401C0000000000000000000000000000'
00023480	C4C9C4C2 D9408689			4597 DC CL48'DIDBR finite test 15b'
000234B0	BFF00000 00000000			4598 DC XL16'BFF00000000000000C000000000000000'
000234C0	C4C9C4C2 D9408689			4599 DC CL48'DIDBR finite test 15b'
000234F0	401C0000 00000000			4600 DC XL16'401C0000000000000000000000000000'
00023500	C4C9C4C2 D9408689			4601 DC CL48'DIDBR finite test 16a'
00023530	00000000 00000000			4602 DC XL16'0000000000000000C000000000000000'
00023540	C4C9C4C2 D9408689			4603 DC CL48'DIDBR finite test 16a'
00023570	40200000 00000000			4604 DC XL16'40200000000000000000000000000000'
00023580	C4C9C4C2 D9408689			4605 DC CL48'DIDBR finite test 16b'
000235B0	00000000 00000000			4606 DC XL16'0000000000000000C000000000000000'
000235C0	C4C9C4C2 D9408689			4607 DC CL48'DIDBR finite test 16b'
000235F0	40200000 00000000			4608 DC XL16'40200000000000000000000000000000'
00023600	C4C9C4C2 D9408689			4609 DC CL48'DIDBR finite test 17a'
00023630	80000000 00000000			4610 DC XL16'8000000000000000C000000000000000'
00023640	C4C9C4C2 D9408689			4611 DC CL48'DIDBR finite test 17a'
00023670	C0200000 00000000			4612 DC XL16'C0200000000000000000000000000000'
00023680	C4C9C4C2 D9408689			4613 DC CL48'DIDBR finite test 17b'
000236B0	80000000 00000000			4614 DC XL16'8000000000000000C000000000000000'
000236C0	C4C9C4C2 D9408689			4615 DC CL48'DIDBR finite test 17b'
000236F0	C0200000 00000000			4616 DC XL16'C0200000000000000000000000000000'
00023700	C4C9C4C2 D9408689			4617 DC CL48'DIDBR finite test 18a'
00023730	3FF00000 00000000			4618 DC XL16'3FF0000000000000C000000000000000'
00023740	C4C9C4C2 D9408689			4619 DC CL48'DIDBR finite test 18a'
00023770	C01C0000 00000000			4620 DC XL16'C01C0000000000000000000000000000'
00023780	C4C9C4C2 D9408689			4621 DC CL48'DIDBR finite test 18b'
000237B0	3FF00000 00000000			4622 DC XL16'3FF0000000000000C000000000000000'
000237C0	C4C9C4C2 D9408689			4623 DC CL48'DIDBR finite test 18b'
000237F0	C01C0000 00000000			4624 DC XL16'C01C0000000000000000000000000000'
00023800	C4C9C4C2 D9408689			4625 DC CL48'DIDBR finite test 19a'
00023830	40000000 00000000			4626 DC XL16'4000000000000000C000000000000000'
00023840	C4C9C4C2 D9408689			4627 DC CL48'DIDBR finite test 19a'
00023870	C0180000 00000000			4628 DC XL16'C0180000000000000000000000000000'
00023880	C4C9C4C2 D9408689			4629 DC CL48'DIDBR finite test 19b'
000238B0	40000000 00000000			4630 DC XL16'4000000000000000C000000000000000'
000238C0	C4C9C4C2 D9408689			4631 DC CL48'DIDBR finite test 19b'
000238F0	C0180000 00000000			4632 DC XL16'C0180000000000000000000000000000'
00023900	C4C9C4C2 D9408689			4633 DC CL48'DIDBR finite test 20a'
00023930	BFF00000 00000000			4634 DC XL16'BFF00000000000000BFF00000000000000'
00023940	C4C9C4C2 D9408689			4635 DC CL48'DIDBR finite test 20a'
00023970	C0140000 00000000			4636 DC XL16'C0140000000000000000000000000000'
00023980	C4C9C4C2 D9408689			4637 DC CL48'DIDBR finite test 20b'
000239B0	BFF00000 00000000			4638 DC XL16'BFF00000000000000BFF00000000000000'
000239C0	C4C9C4C2 D9408689			4639 DC CL48'DIDBR finite test 20b'
000239F0	C0140000 00000000			4640 DC XL16'C0140000000000000000000000000000'
00023A00	C4C9C4C2 D9408689			4641 DC CL48'DIDBR finite test 21a'
00023A30	80000000 00000000			4642 DC XL16'8000000000000000BFF00000000000000'
00023A40	C4C9C4C2 D9408689			4643 DC CL48'DIDBR finite test 21a'
00023A70	C0100000 00000000			4644 DC XL16'C0100000000000000000000000000000'
00023A80	C4C9C4C2 D9408689			4645 DC CL48'DIDBR finite test 21b'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00023AB0	80000000 00000000			4646 DC XL16'8000000000000000BFF000000000000'
00023AC0	C4C9C4C2 D9408689			4647 DC CL48'DIDBR finite test 21b'
00023AF0	C0100000 00000000			4648 DC XL16'C01000000000000000000000000000'
00023B00	C4C9C4C2 D9408689			4649 DC CL48'DIDBR finite test 22a'
00023B30	3FF00000 00000000			4650 DC XL16'3FF0000000000000BFF000000000000'
00023B40	C4C9C4C2 D9408689			4651 DC CL48'DIDBR finite test 22a'
00023B70	C0080000 00000000			4652 DC XL16'C00800000000000000000000000000'
00023B80	C4C9C4C2 D9408689			4653 DC CL48'DIDBR finite test 22b'
00023BB0	3FF00000 00000000			4654 DC XL16'3FF0000000000000BFF000000000000'
00023BC0	C4C9C4C2 D9408689			4655 DC CL48'DIDBR finite test 22b'
00023BF0	C0080000 00000000			4656 DC XL16'C00800000000000000000000000000'
00023C00	C4C9C4C2 D9408689			4657 DC CL48'DIDBR finite test 23a'
00023C30	C0000000 00000000			4658 DC XL16'C00000000000000080000000000000'
00023C40	C4C9C4C2 D9408689			4659 DC CL48'DIDBR finite test 23a'
00023C70	C0000000 00000000			4660 DC XL16'C00000000000000000000000000000'
00023C80	C4C9C4C2 D9408689			4661 DC CL48'DIDBR finite test 23b'
00023CB0	C0000000 00000000			4662 DC XL16'C00000000000000080000000000000'
00023CC0	C4C9C4C2 D9408689			4663 DC CL48'DIDBR finite test 23b'
00023CF0	C0000000 00000000			4664 DC XL16'C00000000000000000000000000000'
00023D00	C4C9C4C2 D9408689			4665 DC CL48'DIDBR finite test 24a'
00023D30	3FF00000 00000000			4666 DC XL16'3FF000000000000000000000000000'
00023D40	C4C9C4C2 D9408689			4667 DC CL48'DIDBR finite test 24a'
00023D70	3FF00000 00000000			4668 DC XL16'3FF000000000000000000000000000'
00023D80	C4C9C4C2 D9408689			4669 DC CL48'DIDBR finite test 24b'
00023DB0	3FF00000 00000000			4670 DC XL16'3FF000000000000000000000000000'
00023DC0	C4C9C4C2 D9408689			4671 DC CL48'DIDBR finite test 24b'
00023DF0	3FF00000 00000000			4672 DC XL16'3FF000000000000000000000000000'
00023E00	C4C9C4C2 D9408689			4673 DC CL48'DIDBR finite test 25a'
00023E30	3FF00000 00000000			4674 DC XL16'3FF000000000000000000000000000'
00023E40	C4C9C4C2 D9408689			4675 DC CL48'DIDBR finite test 25a'
00023E70	3FF00000 00000000			4676 DC XL16'3FF000000000000000000000000000'
00023E80	C4C9C4C2 D9408689			4677 DC CL48'DIDBR finite test 25b'
00023EB0	3FF00000 00000000			4678 DC XL16'3FF000000000000000000000000000'
00023EC0	C4C9C4C2 D9408689			4679 DC CL48'DIDBR finite test 25b'
00023EF0	3FF00000 00000000			4680 DC XL16'3FF000000000000000000000000000'
00023F00	C4C9C4C2 D9408689			4681 DC CL48'DIDBR finite test 26a'
00023F30	40000000 00000000			4682 DC XL16'400000000000000000000000000000'
00023F40	C4C9C4C2 D9408689			4683 DC CL48'DIDBR finite test 26a'
00023F70	40000000 00000000			4684 DC XL16'400000000000000000000000000000'
00023F80	C4C9C4C2 D9408689			4685 DC CL48'DIDBR finite test 26b'
00023FB0	40000000 00000000			4686 DC XL16'400000000000000000000000000000'
00023FC0	C4C9C4C2 D9408689			4687 DC CL48'DIDBR finite test 26b'
00023FF0	40000000 00000000			4688 DC XL16'400000000000000000000000000000'
00024000	C4C9C4C2 D9408689			4689 DC CL48'DIDBR finite test 27a'
00024030	BFF00000 00000000			4690 DC XL16'BFF000000000000003FF000000000000'
00024040	C4C9C4C2 D9408689			4691 DC CL48'DIDBR finite test 27a'
00024070	40080000 00000000			4692 DC XL16'400800000000000000000000000000'
00024080	C4C9C4C2 D9408689			4693 DC CL48'DIDBR finite test 27b'
000240B0	BFF00000 00000000			4694 DC XL16'BFF000000000000003FF000000000000'
000240C0	C4C9C4C2 D9408689			4695 DC CL48'DIDBR finite test 27b'
000240F0	40080000 00000000			4696 DC XL16'400800000000000000000000000000'
00024100	C4C9C4C2 D9408689			4697 DC CL48'DIDBR finite test 28a'
00024130	00000000 00000000			4698 DC XL16'00000000000000003FF000000000000'
00024140	C4C9C4C2 D9408689			4699 DC CL48'DIDBR finite test 28a'
00024170	40100000 00000000			4700 DC XL16'401000000000000000000000000000'
00024180	C4C9C4C2 D9408689			4701 DC CL48'DIDBR finite test 28b'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000241B0	00000000 00000000			4702 DC XL16'00000000000000003FF000000000000'
000241C0	C4C9C4C2 D9408689			4703 DC CL48'DIDBR finite test 28b'
000241F0	40100000 00000000			4704 DC XL16'4010000000000000000000000000000'
00024200	C4C9C4C2 D9408689			4705 DC CL48'DIDBR finite test 29a'
00024230	3FF00000 00000000			4706 DC XL16'3FF00000000000003FF000000000000'
00024240	C4C9C4C2 D9408689			4707 DC CL48'DIDBR finite test 29a'
00024270	40140000 00000000			4708 DC XL16'40140000000000000000000000000'
00024280	C4C9C4C2 D9408689			4709 DC CL48'DIDBR finite test 29b'
000242B0	3FF00000 00000000			4710 DC XL16'3FF00000000000003FF000000000000'
000242C0	C4C9C4C2 D9408689			4711 DC CL48'DIDBR finite test 29b'
000242F0	40140000 00000000			4712 DC XL16'40140000000000000000000000000'
00024300	C4C9C4C2 D9408689			4713 DC CL48'DIDBR finite test 30a'
00024330	C0000000 00000000			4714 DC XL16'C000000000000000400000000000000'
00024340	C4C9C4C2 D9408689			4715 DC CL48'DIDBR finite test 30a'
00024370	40180000 00000000			4716 DC XL16'40180000000000000000000000000'
00024380	C4C9C4C2 D9408689			4717 DC CL48'DIDBR finite test 30b'
000243B0	C0000000 00000000			4718 DC XL16'C000000000000000400000000000000'
000243C0	C4C9C4C2 D9408689			4719 DC CL48'DIDBR finite test 30b'
000243F0	40180000 00000000			4720 DC XL16'40180000000000000000000000000'
00024400	C4C9C4C2 D9408689			4721 DC CL48'DIDBR finite test 31a'
00024430	BFF00000 00000000			4722 DC XL16'BFF0000000000000400000000000000'
00024440	C4C9C4C2 D9408689			4723 DC CL48'DIDBR finite test 31a'
00024470	401C0000 00000000			4724 DC XL16'401C0000000000000000000000000'
00024480	C4C9C4C2 D9408689			4725 DC CL48'DIDBR finite test 31b'
000244B0	BFF00000 00000000			4726 DC XL16'BFF0000000000000400000000000000'
000244C0	C4C9C4C2 D9408689			4727 DC CL48'DIDBR finite test 31b'
000244F0	401C0000 00000000			4728 DC XL16'401C0000000000000000000000000'
00024500	C4C9C4C2 D9408689			4729 DC CL48'DIDBR finite test 32a'
00024530	00000000 00000000			4730 DC XL16'0000000000000000400000000000000'
00024540	C4C9C4C2 D9408689			4731 DC CL48'DIDBR finite test 32a'
00024570	40200000 00000000			4732 DC XL16'40200000000000000000000000000'
00024580	C4C9C4C2 D9408689			4733 DC CL48'DIDBR finite test 32b'
000245B0	00000000 00000000			4734 DC XL16'0000000000000000400000000000000'
000245C0	C4C9C4C2 D9408689			4735 DC CL48'DIDBR finite test 32b'
000245F0	40200000 00000000			4736 DC XL16'40200000000000000000000000000'
00024600	C4C9C4C2 D9408689			4737 DC CL48'DIDBR finite test 33a'
00024630	40100000 00000000			4738 DC XL16'4010000000000000C01000000000000'
00024640	C4C9C4C2 D9408689			4739 DC CL48'DIDBR finite test 33a'
00024670	40440000 00000000			4740 DC XL16'40440000000000000000000000000'
00024680	C4C9C4C2 D9408689			4741 DC CL48'DIDBR finite test 33b'
000246B0	40100000 00000000			4742 DC XL16'4010000000000000C01000000000000'
000246C0	C4C9C4C2 D9408689			4743 DC CL48'DIDBR finite test 33b'
000246F0	40440000 00000000			4744 DC XL16'40440000000000000000000000000'
00024700	C4C9C4C2 D9408689			4745 DC CL48'DIDBR finite test 34a'
00024730	00000000 00000000			4746 DC XL16'0000000000000000630FFFFFFFFFFFFF'
00024740	C4C9C4C2 D9408689			4747 DC CL48'DIDBR finite test 34a'
00024770	1FEFFFFFF FFFFFFFF			4748 DC XL16'1FEFFFFFFF00000000000000000000'
00024780	C4C9C4C2 D9408689			4749 DC CL48'DIDBR finite test 34b'
000247B0	00000000 00000000			4750 DC XL16'0000000000000000630FFFFFFFFFFFFF'
000247C0	C4C9C4C2 D9408689			4751 DC CL48'DIDBR finite test 34b'
000247F0	1FEFFFFFF FFFFFFFF			4752 DC XL16'1FEFFFFFFF00000000000000000000'
00024800	C4C9C4C2 D9408689			4753 DC CL48'DIDBR finite test 35a'
00024830	7CA00000 00000000			4754 DC XL16'7CA000000000000062F555555555554'
00024840	C4C9C4C2 D9408689			4755 DC CL48'DIDBR finite test 35a'
00024870	7CA00000 00000000			4756 DC XL16'7CA00000000000000000000000000'
00024880	C4C9C4C2 D9408689			4757 DC CL48'DIDBR finite test 35b'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				4827 *****
				4828 * VERIFICATION ROUTINE
				4829 *****
00024CA0				4831 VERISUB DS 0H
				4832 *
				4833 ** Loop through the VERIFY TABLE...
				4834 *
00024CA0	4110 C32C		00024F2C	4836 LA R1,VERIFTAB R1 --> Verify table
00024CA4	4120 000C		0000000C	4837 LA R2,VERIFLEN R2 <= Number of entries
00024CA8	0D30			4838 BASR R3,0 Set top of loop
00024CAA	9846 1000		00000000	4840 LM R4,R6,0(R1) Load verify table values
00024CAE	4D70 C0C2		00024CC2	4841 BAS R7,VERIFY Verify results
00024CB2	4110 100C		0000000C	4842 LA R1,12(,R1) Next verify table entry
00024CB6	0623			4843 BCTR R2,R3 Loop through verify table
00024CB8	9500 C278		00024E78	4845 CLI FAILFLAG,X'00' Did all tests verify okay?
00024CBC	078D			4846 BER R13 Yes, return to caller
00024CBE	47F0 F238		00000238	4847 B FAIL No, load FAILURE disabled wait PSW
				4849 *
				4850 ** Loop through the ACTUAL / EXPECTED results...
				4851 *
00024CC2	0D80			4853 VERIFY BASR R8,0 Set top of loop
00024CC4	D50F 4000 5030	00000000	00000030	4855 CLC 0(16,R4),48(R5) Actual results == Expected results?
00024CCA	4770 C0DA		00024CDA	4856 BNE VERIFAIL No, show failure
00024CCE	4140 4010		00000010	4857 VERINEXT LA R4,16(,R4) Next actual result
00024CD2	4150 5040		00000040	4858 LA R5,64(,R5) Next expected result
00024CD6	0668			4859 BCTR R6,R8 Loop through results
00024CD8	07F7			4861 BR R7 Return to caller

LOC	OBJECT CODE			ADDR1	ADDR2	STMT
						4863 *****
						4864 * Report the failure...
						4865 *****
00024CDA	9005	C250			00024E50	4867 VERIFAIL STM R0,R5,SAVER0R5 Save registers
00024CDE	92FF	C278			00024E78	4868 MVI FAILFLAG,X'FF' Remember verification failure
						4869 *
						4870 ** First, show them the description...
						4871 *
00024CE2	D22F	C1E0	5000	00024DE0	00000000	4872 MVC FAILDESC,0(R5) Save results/test description
00024CE8	4100	0044			00000044	4873 LA R0,L'FAILMSG1 R0 <= length of message
00024CEC	4110	C1CC			00024DCC	4874 LA R1,FAILMSG1 R1 --> the message text itself
00024CF0	4520	C27A			00024E7A	4875 BAL R2,MSG Go display this message
						4876 *
						4877 ** Save address of actual and expected results
						4878 *
00024CF4	5040	C24C			00024E4C	4879 ST R4,AACTUAL Save A(actual results)
00024CF8	4150	5030			00000030	4880 LA R5,48(,R5) R5 ==> expected results
00024CFC	5050	C248			00024E48	4881 ST R5,AEXPECT Save A(expected results)
						4882 *
						4883 ** Format and show them the EXPECTED ("Want") results...
						4884 *
00024D00	D205	C210	C3C0	00024E10	00024FC0	4885 MVC WANTGOT,=CL6'Want: '
00024D06	F384	C216	C248	00024E16	00024E48	4886 UNPK FAILADR(L'FAILADR+1),AEXPECT(L'AEXPECT+1)
00024D0C	9240	C21E			00024E1E	4887 MVI BLANKEQ,C' '
00024D10	DC07	C216	C178	00024E16	00024D78	4888 TR FAILADR,HEXTRTAB
00024D16	F384	C221	5000	00024E21	00000000	4890 UNPK FAILVALS+(0*9)(9),(0*4)(5,R5)
00024D1C	9240	C229			00024E29	4891 MVI FAILVALS+(0*9)+8,C' '
00024D20	DC07	C221	C178	00024E21	00024D78	4892 TR FAILVALS+(0*9)(8),HEXTRTAB
00024D26	F384	C22A	5004	00024E2A	00000004	4894 UNPK FAILVALS+(1*9)(9),(1*4)(5,R5)
00024D2C	9240	C232			00024E32	4895 MVI FAILVALS+(1*9)+8,C' '
00024D30	DC07	C22A	C178	00024E2A	00024D78	4896 TR FAILVALS+(1*9)(8),HEXTRTAB
00024D36	F384	C233	5008	00024E33	00000008	4898 UNPK FAILVALS+(2*9)(9),(2*4)(5,R5)
00024D3C	9240	C23B			00024E3B	4899 MVI FAILVALS+(2*9)+8,C' '
00024D40	DC07	C233	C178	00024E33	00024D78	4900 TR FAILVALS+(2*9)(8),HEXTRTAB
00024D46	F384	C23C	500C	00024E3C	0000000C	4902 UNPK FAILVALS+(3*9)(9),(3*4)(5,R5)
00024D4C	9240	C244			00024E44	4903 MVI FAILVALS+(3*9)+8,C' '
00024D50	DC07	C23C	C178	00024E3C	00024D78	4904 TR FAILVALS+(3*9)(8),HEXTRTAB
00024D56	4100	0035			00000035	4906 LA R0,L'FAILMSG2 R0 <= length of message
00024D5A	4110	C210			00024E10	4907 LA R1,FAILMSG2 R1 --> the message text itself
00024D5E	4520	C27A			00024E7A	4908 BAL R2,MSG Go display this message

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				4958	*****			
				4959	*	Issue HERCULES MESSAGE pointed to by R1, length in R0		
				4960	*****			
00024E7A	4900 C3BC		00024FBC	4962	MSG	CH	R0,=H'0'	Do we even HAVE a message?
00024E7E	07D2			4963		BNHR	R2	No, ignore
00024E80	9002 C2B0		00024EB0	4965		STM	R0,R2,MSGSAVE	Save registers
00024E84	4900 C3BE		00024FBE	4967		CH	R0,=AL2(L'MSGMSG)	Message length within limits?
00024E88	47D0 C290		00024E90	4968		BNH	MSGOK	Yes, continue
00024E8C	4100 005F		0000005F	4969		LA	R0,L'MSGMSG	No, set to maximum
00024E90	1820			4971	MSGOK	LR	R2,R0	Copy length to work register
00024E92	0620			4972		BCTR	R2,0	Minus-1 for execute
00024E94	4420 C2BC		00024EBC	4973		EX	R2,MSGMVC	Copy message to O/P buffer
00024E98	4120 200A		0000000A	4975		LA	R2,1+L'MSGCMD(,R2)	Calculate true command length
00024E9C	4110 C2C2		00024EC2	4976		LA	R1,MSGCMD	Point to true command
00024EA0	83120008			4978		DC	X'83',X'12',X'0008'	Issue Hercules Diagnose X'008'
00024EA4	4780 C2AA		00024EAA	4979		BZ	MSGRET	Return if successful
00024EA8	0000			4980		DC	H'0'	CRASH for debugging purposes
00024EAA	9802 C2B0		00024EB0	4982	MSGRET	LM	R0,R2,MSGSAVE	Restore registers
00024EAE	07F2			4983		BR	R2	Return to caller
00024EB0	00000000 00000000			4985	MSGSAVE	DC	3F'0'	Registers save area
00024EBC	D200 C2CB 1000	00024ECB	00000000	4986	MSGMVC	MVC	MSGMSG(0),0(R1)	Executed instruction
00024EC2	D4E2C7D5 D6C8405C			4988	MSGCMD	DC	C'MSGNOH * '	*** HERCULES MESSAGE COMMAND ***
00024ECB	40404040 40404040			4989	MSGMSG	DC	CL95' '	The message text to be displayed

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				4991 *****
				4992 * VERIFY TABLE
				4993 *****
				4994 *
				4995 * A(actual results), A(expected results), A(#of results)
				4996 *
				4997 *****
00024F2C				4999 VERIFTAB DC 0F'0'
00024F2C	00001000			5000 DC A(SBFPNFOT)
00024F30	0000C000			5001 DC A(SBFPNFOT_GOOD)
00024F34	00000020			5002 DC A(SBFPNFOT_NUM)
				5003 *
00024F38	00001200			5004 DC A(SBFPNFFL)
00024F3C	0000C800			5005 DC A(SBFPNFFL_GOOD)
00024F40	00000010			5006 DC A(SBFPNFFL_NUM)
				5007 *
00024F44	00001300			5008 DC A(LBFPNFOT)
00024F48	0000CC00			5009 DC A(LBFPNFOT_GOOD)
00024F4C	00000040			5010 DC A(LBFPNFOT_NUM)
				5011 *
00024F50	00001700			5012 DC A(LBFPNFFL)
00024F54	0000DC00			5013 DC A(LBFPNFFL_GOOD)
00024F58	00000010			5014 DC A(LBFPNFFL_NUM)
				5015 *
00024F5C	00002000			5016 DC A(SBFPRMO)
00024F60	0000E000			5017 DC A(SBFPRMO_GOOD)
00024F64	00000120			5018 DC A(SBFPRMO_NUM)
				5019 *
00024F68	00004000			5020 DC A(SBFPRMOF)
00024F6C	00012800			5021 DC A(SBFPRMOF_GOOD)
00024F70	00000090			5022 DC A(SBFPRMOF_NUM)
				5023 *
00024F74	00005000			5024 DC A(LBFPRMO)
00024F78	00014C00			5025 DC A(LBFPRMO_GOOD)
00024F7C	00000240			5026 DC A(LBFPRMO_NUM)
				5027 *
00024F80	00009000			5028 DC A(LBFPRMOF)
00024F84	0001DC00			5029 DC A(LBFPRMOF_GOOD)
00024F88	00000090			5030 DC A(LBFPRMOF_NUM)
				5031 *
00024F8C	0000A000			5032 DC A(SBFPOUT)
00024F90	00020000			5033 DC A(SBFPOUT_GOOD)
00024F94	0000004C			5034 DC A(SBFPOUT_NUM)
				5035 *
00024F98	0000A800			5036 DC A(SBFPFLGS)
00024F9C	00021300			5037 DC A(SBFPFLGS_GOOD)
00024FA0	00000026			5038 DC A(SBFPFLGS_NUM)
				5039 *
00024FA4	0000AC00			5040 DC A(LBFPFLGS)
00024FA8	00021C80			5041 DC A(LBFPFLGS_GOOD)
00024FAC	00000026			5042 DC A(LBFPFLGS_NUM)
				5043 *
00024FB0	0000B000			5044 DC A(LBFPOUT)
00024FB4	00022600			5045 DC A(LBFPOUT_GOOD)
00024FB8	00000098			5046 DC A(LBFPOUT_NUM)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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0000000C	00000001	5047 *			
		5048 VERIFLEN EQU	(*-VERIFTAB)/12		#of table entries

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES														
AACTUAL	F	024E4C	4	4952	4879	4914													
AEXPECT	F	024E48	4	4951	4881	4886													
AHELPERS	A	00027C	4	215	205	246													
BFPDV2NT	J	000000	151500	131															
BLANKEQ	C	024E1E	3	4948	4887	4915													
CHARHEX	C	024E68	16	4954	4955														
CTLR0	F	0002F0	4	256	224	225	226												
D2IMCT	U	000006	1	803	474	700													
D2IMODES	C	0006F8	1	795	803	485	502	711	728										
DIDBRF	I	00059A	4	598	238														
DIDBRNF	I	000524	4	540	236														
DIDBRRM	I	000648	4	683	240														
DIDBRTAB	F	00071C	4	831	712	729													
DIEBRF	I	0003D2	4	373	231														
DIEBRNF	H	00035C	2	314	229														
DIEBRRM	I	000480	4	459	233														
DIEBRTAB	F	000700	4	813	486	503													
FAIL	I	000238	4	213	4847														
FAILADR	C	024E16	8	4947	4886	4888	4914	4916											
FAILDESC	C	024DE0	48	4943	4872														
FAILFLAG	X	024E78	1	4956	4845	4868													
FAILMSG1	C	024DCC	68	4941	4873	4874													
FAILMSG2	C	024E10	53	4945	4906	4907	4934	4935											
FAILPSW	X	0002E0	8	254	213														
FAILVALS	C	024E21	36	4949	4890	4891	4892	4894	4895	4896	4898	4899	4900	4902	4903	4904	4918	4919	
					4920	4922	4923	4924	4926	4927	4928	4930	4931	4932					
FPCMCT	U	000004	1	785	466	690													
FPCMODES	C	0006F4	1	779	785	472	480	497	696	706	723								
FPCREGNT	X	0002F4	4	257	321	379	394	418	479	546	604	619	643	697	705				
FPCREGTR	X	0002F8	4	258	334	403	496	559	628	698	722								
FPR0	U	000000	1	152	322	325	327	335	338	340	380	383	385	395	397	404	407	409	
					419	421	482	487	499	504	547	550	552	560	563	565	605	608	
					610	620	622	629	632	634	644	646	708	713	725	730	814	816	
					817	818	819	820	821	832	834	835	836	837	838	839			
FPR1	U	000001	1	153	323	325	336	338	381	383	395	405	407	419	483	500	548	550	
					561	563	606	608	620	630	632	644	709	726	814	816	817	818	
					819	820	821	832	834	835	836	837	838	839					
FPR10	U	00000A	1	162															
FPR11	U	00000B	1	163															
FPR12	U	00000C	1	164															
FPR13	U	00000D	1	165															
FPR14	U	00000E	1	166															
FPR15	U	00000F	1	167															
FPR2	U	000002	1	154		324	325	328	337	338	341	382	383	386	395	406	407	410	419
						484	488	501	505	549	550	553	562	563	566	607	608	611	620
						631	632	635	644	710	714	727	731	814	816	817	818	819	820
						821	832	834	835	836	837	838	839						
FPR3	U	000003	1	155															
FPR4	U	000004	1	156															
FPR5	U	000005	1	157															
FPR6	U	000006	1	158															
FPR7	U	000007	1	159															
FPR8	U	000008	1	160															
FPR9	U	000009	1	161															
GOODPSW	X	0002D0	8	253	250														
HELPERS	H	024C00	2	4787	170	215													

[illegible]

MACRO DEFN REFERENCES

No defined macros

DESC	SYMBOL	SIZE	POS	ADDR
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Entry: 0

Image	IMAGE	151500	00000-24FCB	00000-24FCB
Region		151500	00000-24FCB	00000-24FCB
CSECT	BFPDV2NT	151500	00000-24FCB	00000-24FCB

STMT

FILE NAME

```
1 c:\Users\Fish\Documents\Visual Studio 2008\Projects\MyProjects\ASMA-0\bfp-001-divtoint\bfp-001-divtoint.asm
```

```
** NO ERRORS FOUND **
```